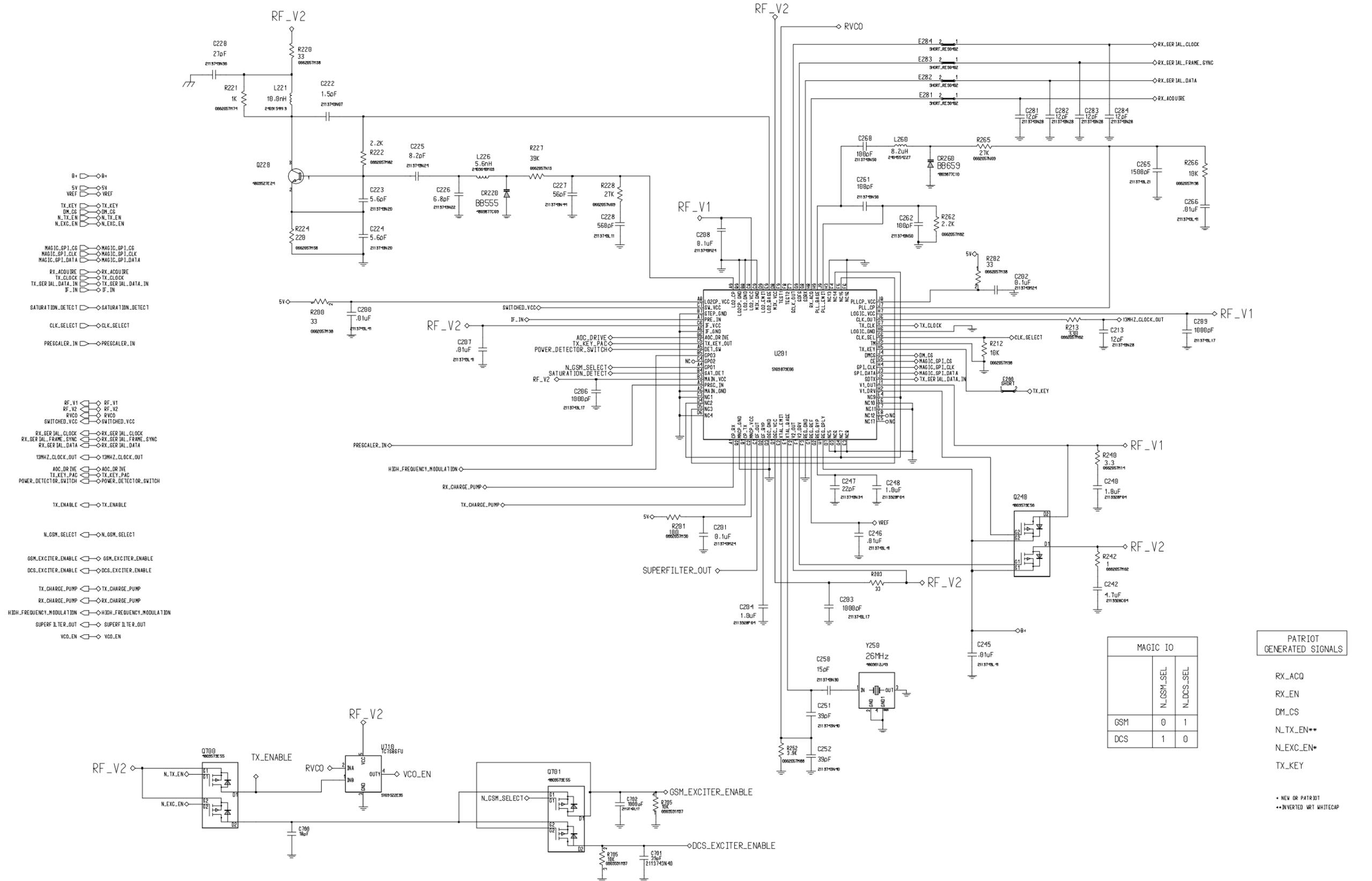


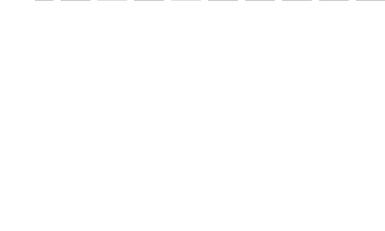
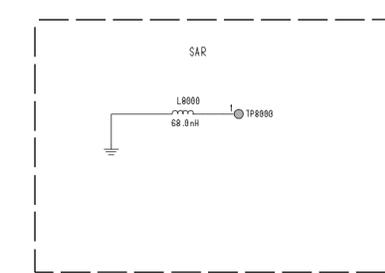
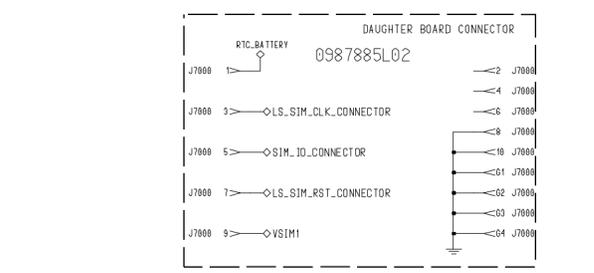
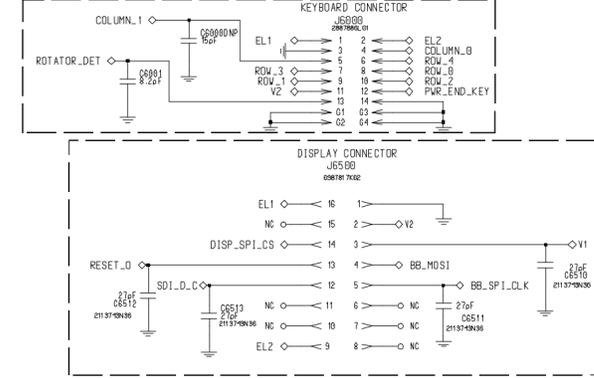
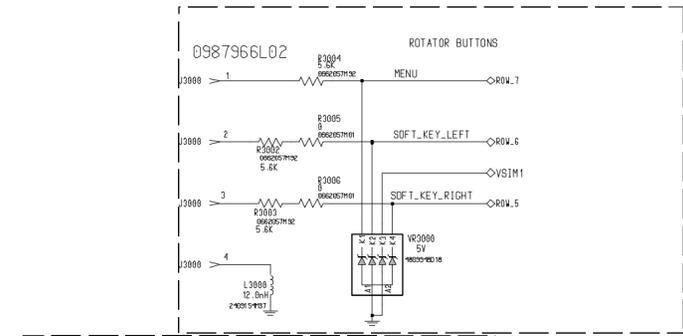
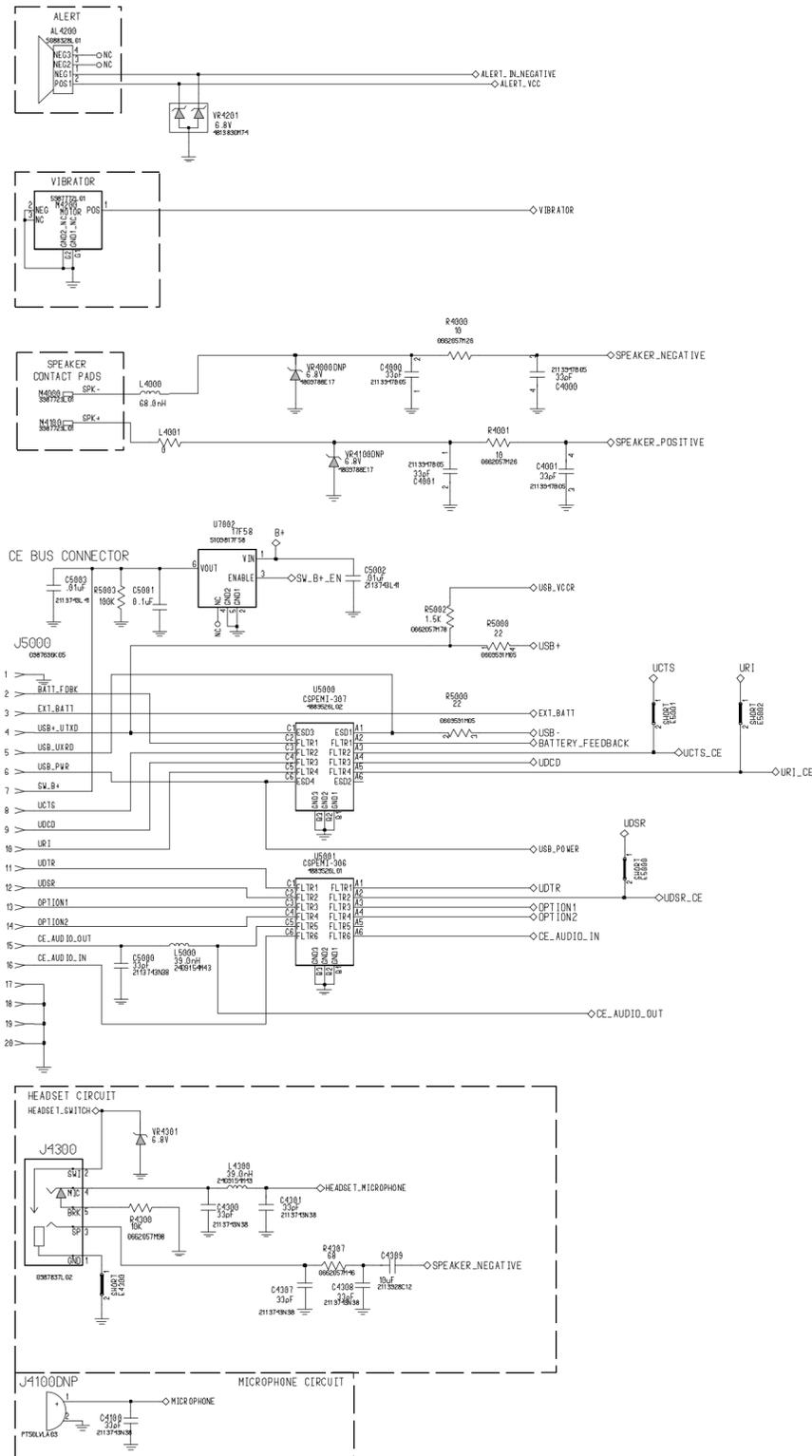


# MAGIC\_DM BLOCK

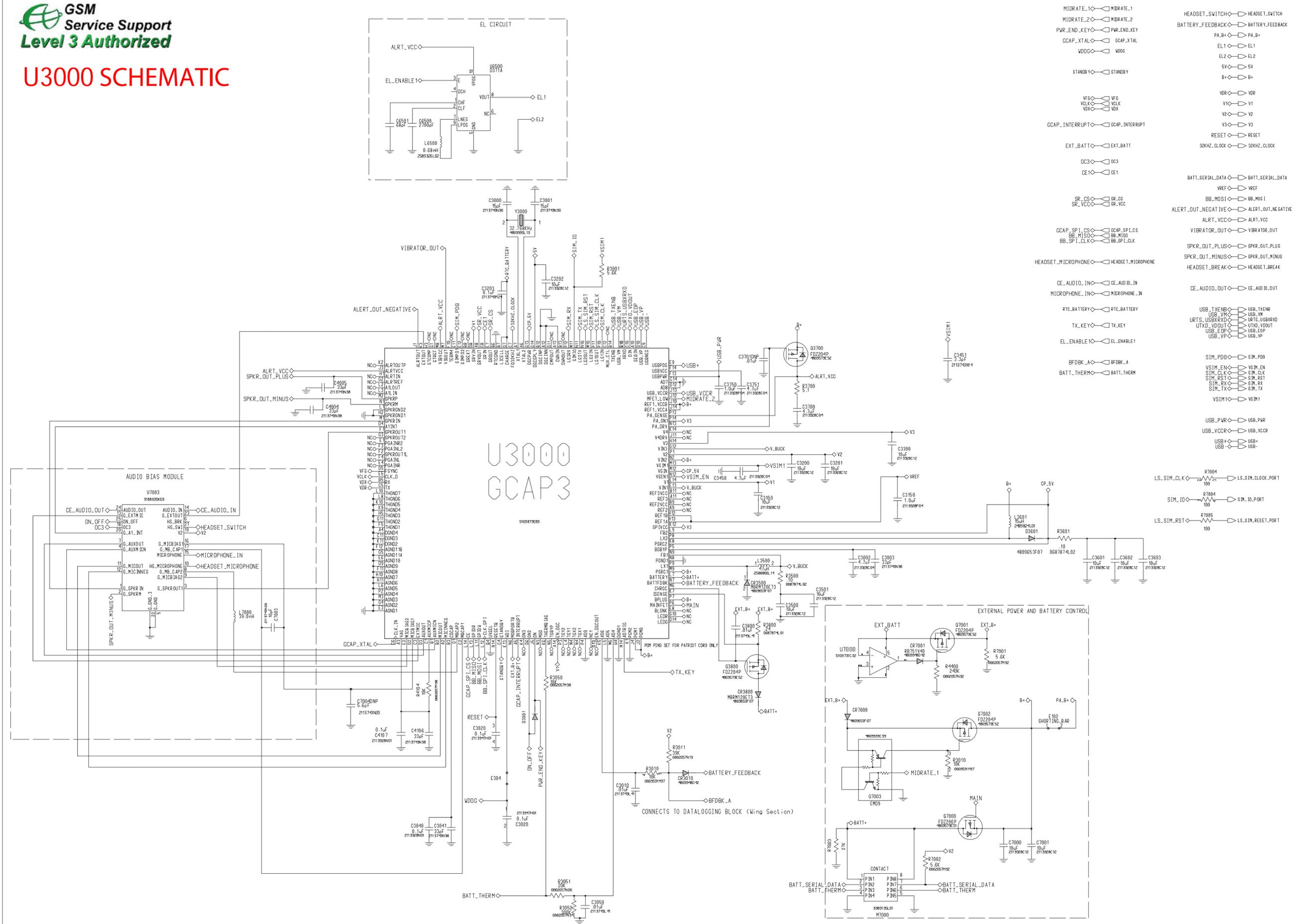




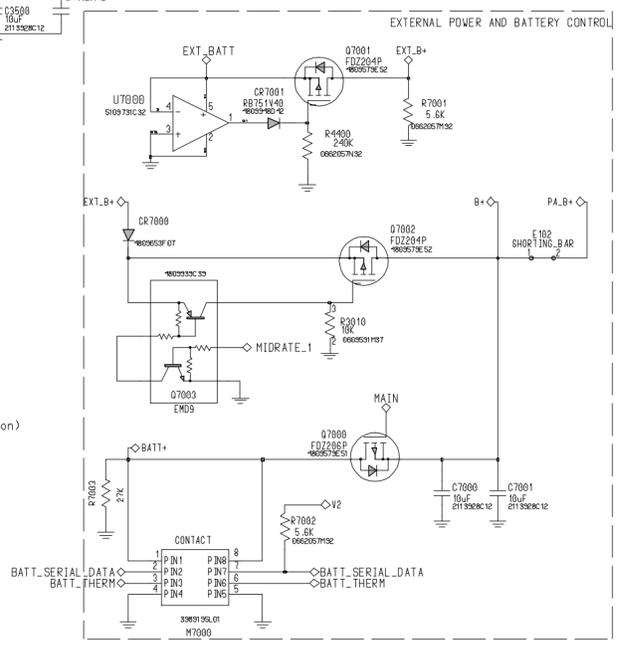
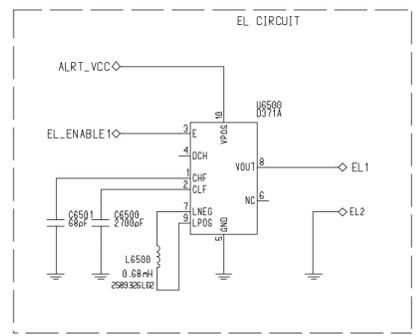




# U3000 SCHEMATIC



- |                    |                    |                    |                    |
|--------------------|--------------------|--------------------|--------------------|
| MIDRATE_1          | MIDRATE_1          | HEADSET_SWITCH     | HEADSET_SWITCH     |
| MIDRATE_2          | MIDRATE_2          | BATTERY_FEEDBACK   | BATTERY_FEEDBACK   |
| PWR_END_KEY        | PWR_END_KEY        | PA.B+              | PA.B+              |
| GCAP_XTAL          | GCAP_XTAL          | EL1                | EL1                |
| WDOG               | WDOG               | EL2                | EL2                |
|                    |                    | 5V                 | 5V                 |
| STANDBY            | STANDBY            | B+                 | B+                 |
|                    |                    | VDR                | VDR                |
| VFS                | VFS                | V1                 | V1                 |
| VLC                | VLC                | V2                 | V2                 |
| VDX                | VDX                | V3                 | V3                 |
|                    |                    | RESET              | RESET              |
| GCAP_INTERRUPT     | GCAP_INTERRUPT     | 32KHZ_CLOCK        | 32KHZ_CLOCK        |
|                    |                    |                    |                    |
| EXT_BATT           | EXT_BATT           | BATT_SERIAL_DATA   | BATT_SERIAL_DATA   |
| DC3                | DC3                | VREF               | VREF               |
| CE1                | CE1                | BB_MOS1            | BB_MOS1            |
|                    |                    | ALERT_OUT_NEGATIVE | ALERT_OUT_NEGATIVE |
| SR_CS              | SR_CS              | ALRT_VCC           | ALRT_VCC           |
| SR_VCC             | SR_VCC             | VIBRATOR_OUT       | VIBRATOR_OUT       |
|                    |                    | SPKR_OUT_PLUS      | SPKR_OUT_PLUS      |
| GCAP_SP1_CS        | GCAP_SP1_CS        | SPKR_OUT_MINUS     | SPKR_OUT_MINUS     |
| BB_MISO            | BB_MISO            | HEADSET_BREAK      | HEADSET_BREAK      |
| BB_SP1_CLK         | BB_SP1_CLK         |                    |                    |
|                    |                    | CE_AUDIO_IN        | CE_AUDIO_IN        |
| HEADSET_MICROPHONE | HEADSET_MICROPHONE | MICROPHONE_IN      | MICROPHONE_IN      |
|                    |                    |                    |                    |
| RIC_BATTERY        | RIC_BATTERY        | USB_TXENB          | USB_TXENB          |
|                    |                    | USB_VH             | USB_VH             |
| TX_KEY             | TX_KEY             | URTS_USB_RXD       | URTS_USB_RXD       |
|                    |                    | UTXD_VDDOUT        | UTXD_VDDOUT        |
| EL_ENABLE          | EL_ENABLE          | USB_EOP            | USB_EOP            |
|                    |                    | USB_VP             | USB_VP             |
| BFBK_A             | BFBK_A             |                    |                    |
| BATT_THERM         | BATT_THERM         | SIM_P00            | SIM_P00            |
|                    |                    | VSIM_EN            | VSIM_EN            |
|                    |                    | SIM_CLK            | SIM_CLK            |
|                    |                    | SIM_RST            | SIM_RST            |
|                    |                    | SIM_RX             | SIM_RX             |
|                    |                    | SIM_TX             | SIM_TX             |
|                    |                    | VSIM1              | VSIM1              |
|                    |                    |                    |                    |
|                    |                    | USB_PWR            | USB_PWR            |
|                    |                    | USB_VCCR           | USB_VCCR           |
|                    |                    | USB+               | USB+               |
|                    |                    | USB-               | USB-               |
|                    |                    |                    |                    |
|                    |                    | LS_SIM_CLK         | LS_SIM_CLOCK_PORT  |
|                    |                    |                    |                    |
|                    |                    | SIM_ID             | SIM_ID_PORT        |
|                    |                    |                    |                    |
|                    |                    | LS_SIM_RST         | LS_SIM_RESET_PORT  |
|                    |                    |                    |                    |



U3000  
GCAP3

CONNECTS TO DATALOGGING BLOCK (Wing Section)

