

MOTOROLA CONFIDENTIAL PROPRIETARY

4 1 3 A

Engineer :

Yun Sun Lee
Deputy Mayor

Drawn By:
Yun Sun Lee

R&D CHK:

DOC CTDL CLK

DOCUMENT CLASS:

MFG CTRL CHK:

DA CLK:

QA CHK:

Changed buy:

w17698

MOTOROLA INC.

TITLE: IZAR NA
Bluetoo

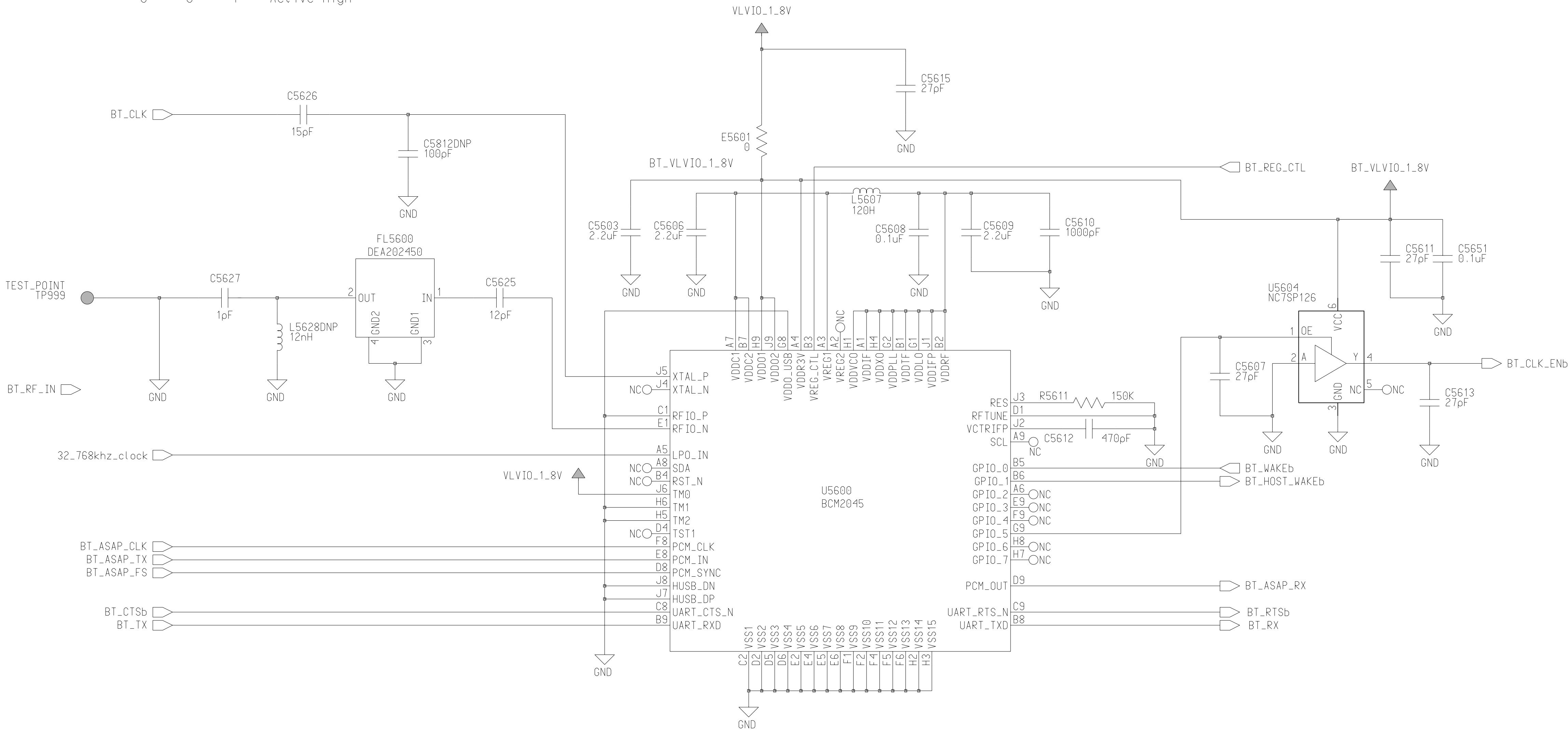
Size:
11x17

REV:	Drawing Number:	Page:	Of:
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Date:
Tuesday, August 31, 2004

Time:
3:47:09 p

BT_CLK_EN polarity			
TM2	TM1	TM0	Description
0	0	0	Active low
0	0	1	Active high



B

C

D

Engineer:
Chris Pipe
Drawn by:
Chris Pipe
R&D CHK:
DOC CTRL CHK:
MFG CTRL CHK:
QA CHK:

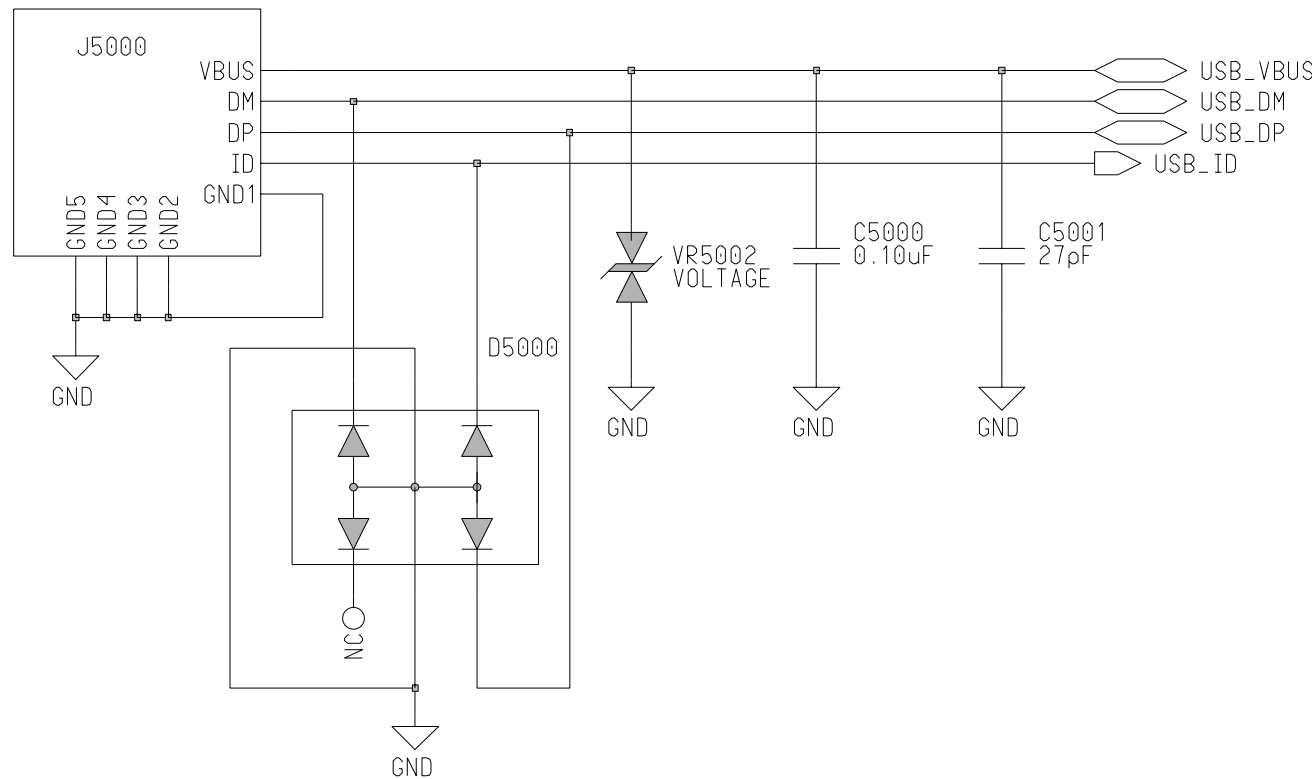


TITLE: MISC.CONNECTORS
Size: 11x21

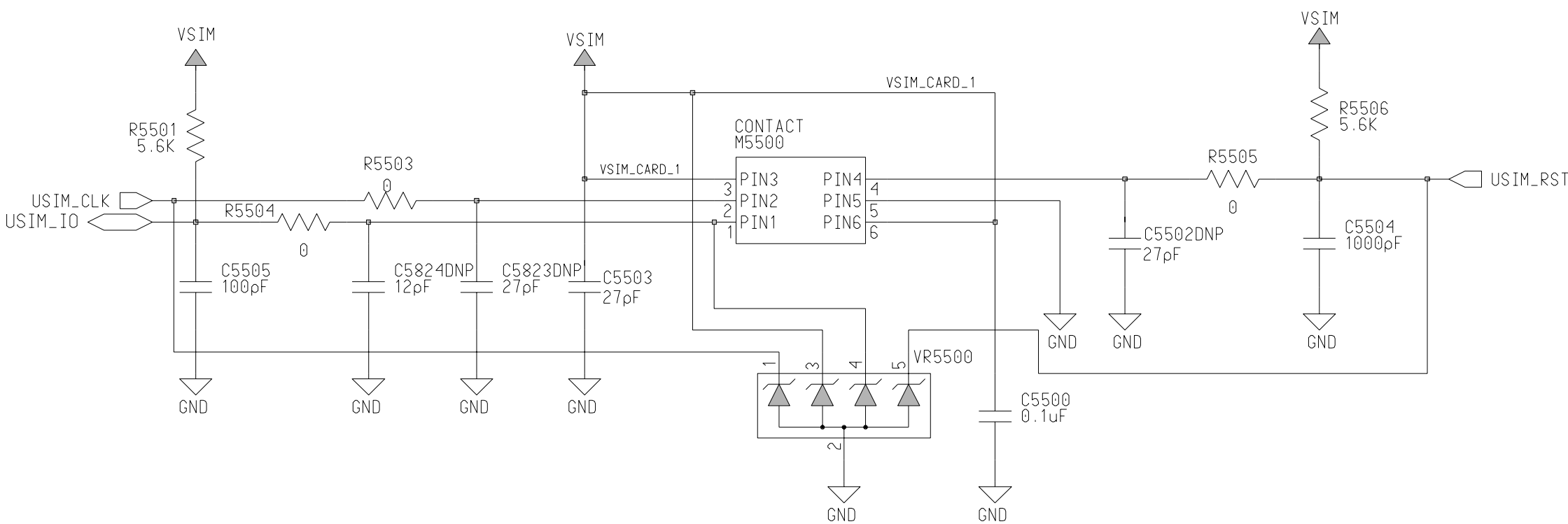
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Changed by: w16564 Date: Monday, February 13, 2006 Time: 9:23:31 am

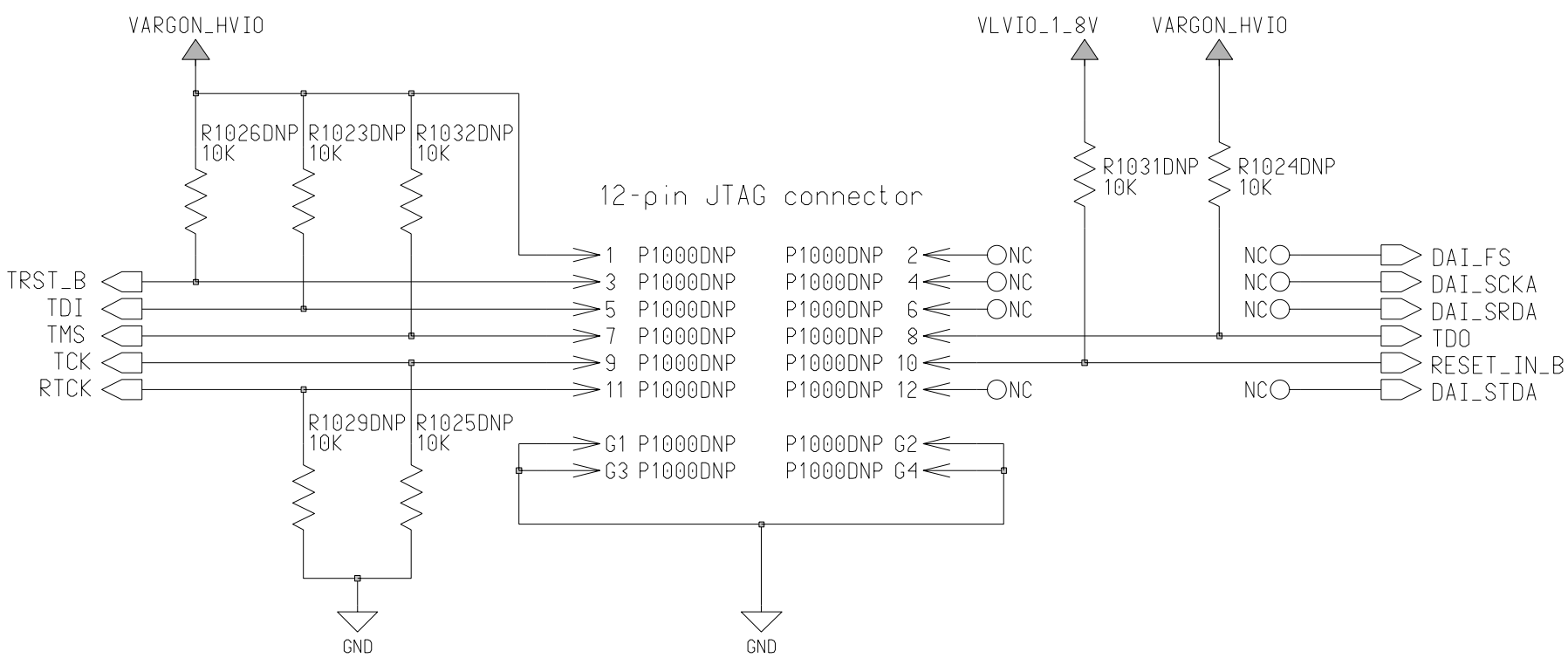
EMU CONNECTOR



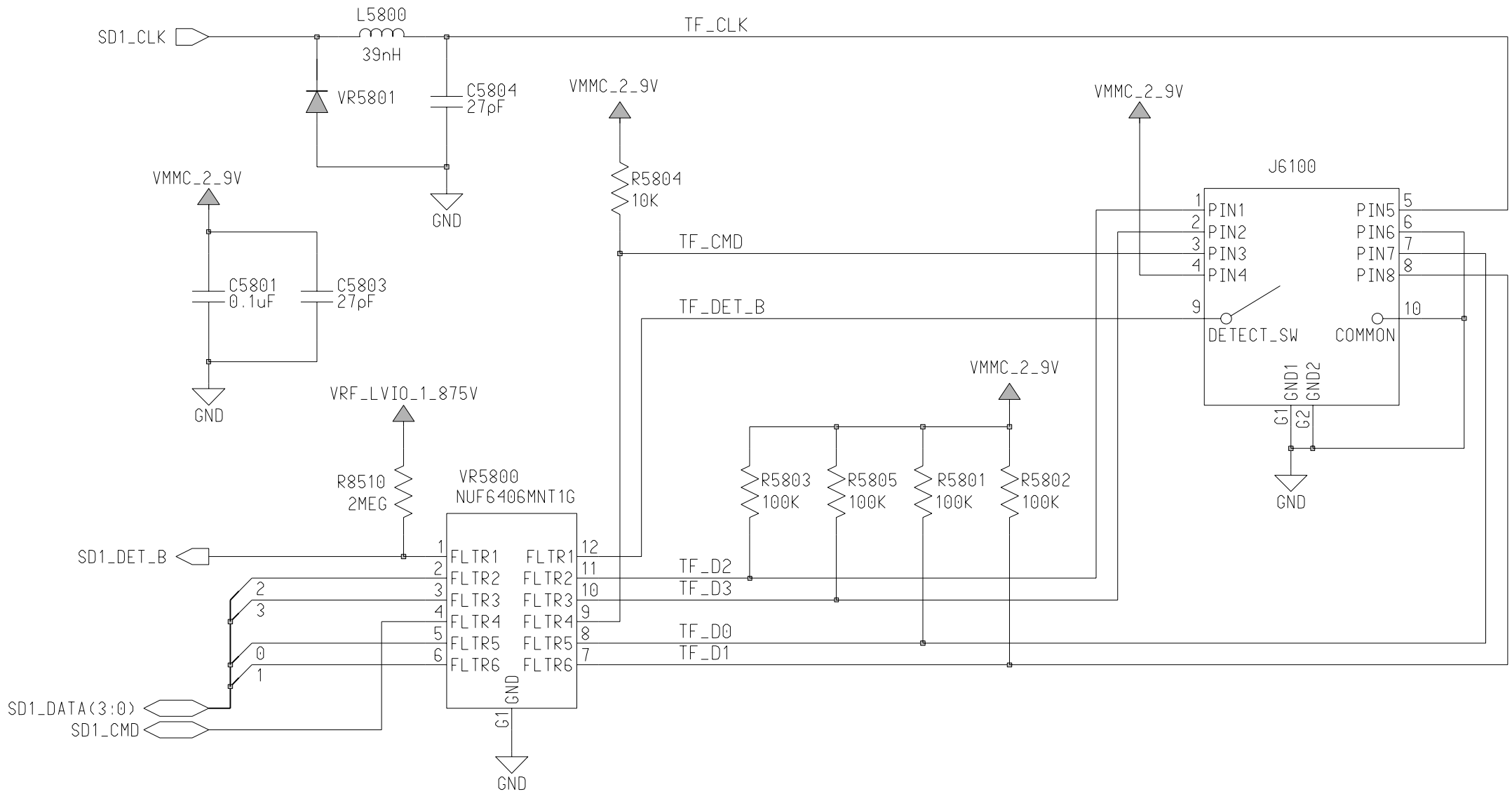
USIM

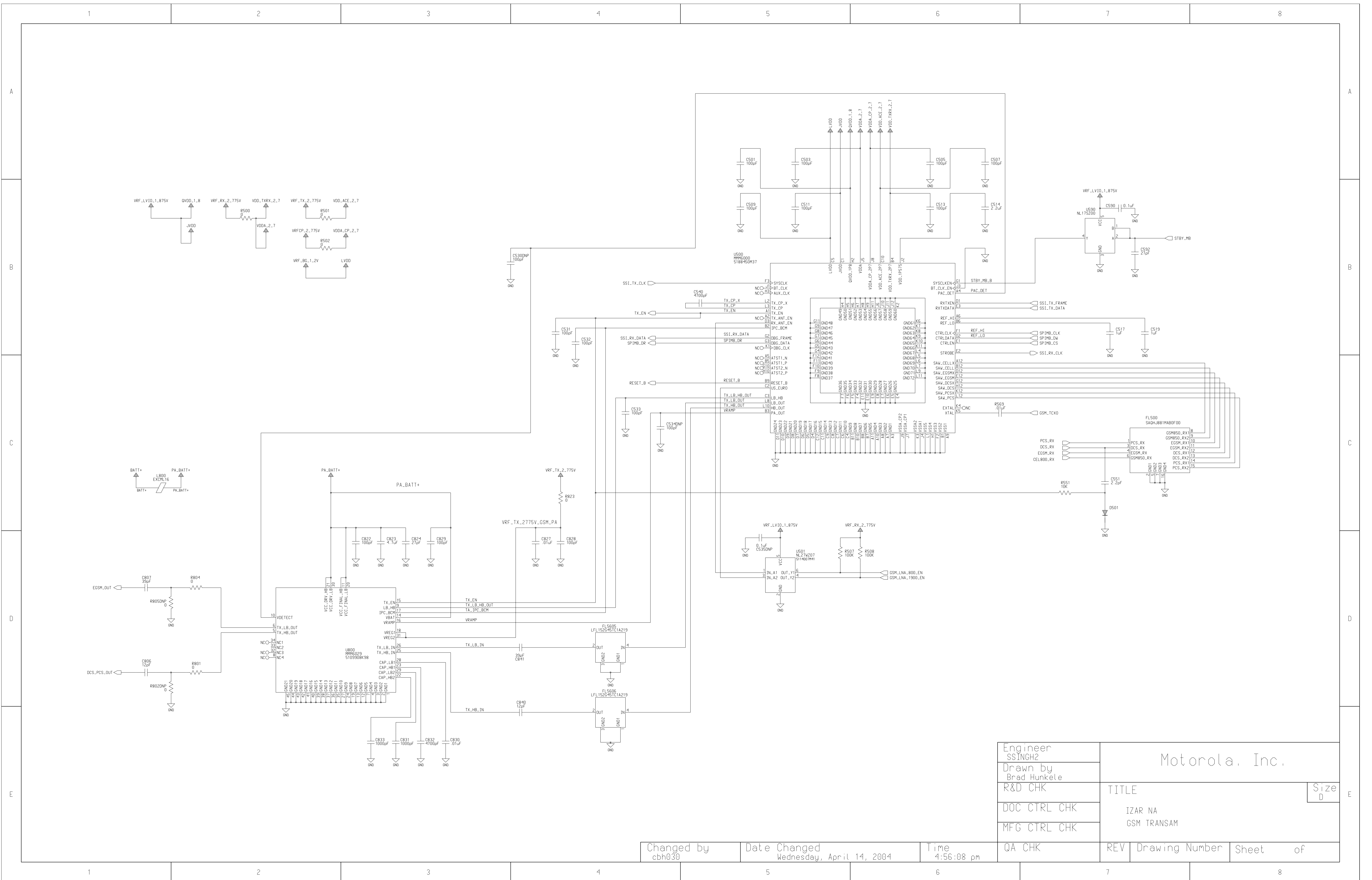


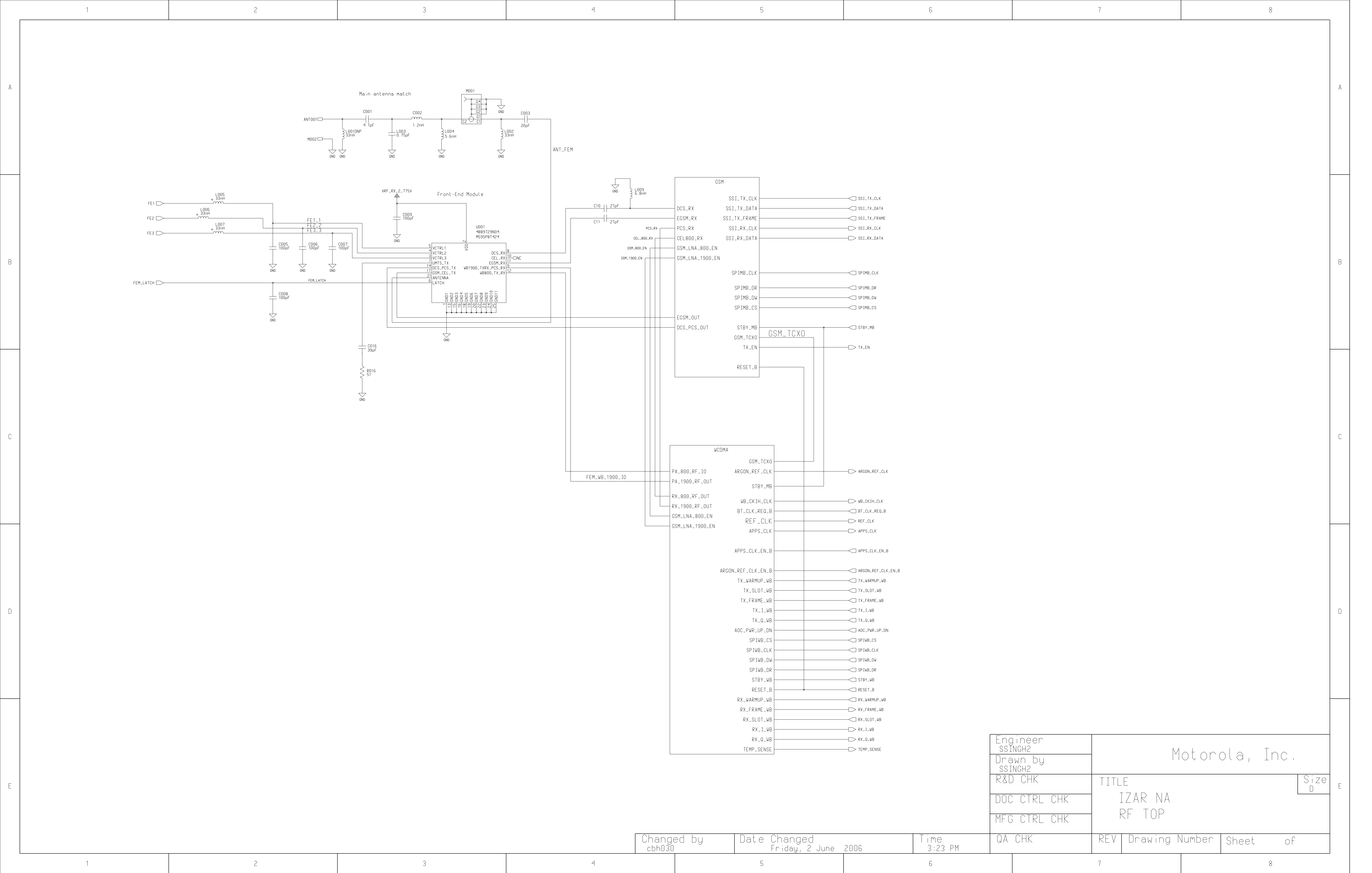
JTAG/DAI



TRANSFLASH

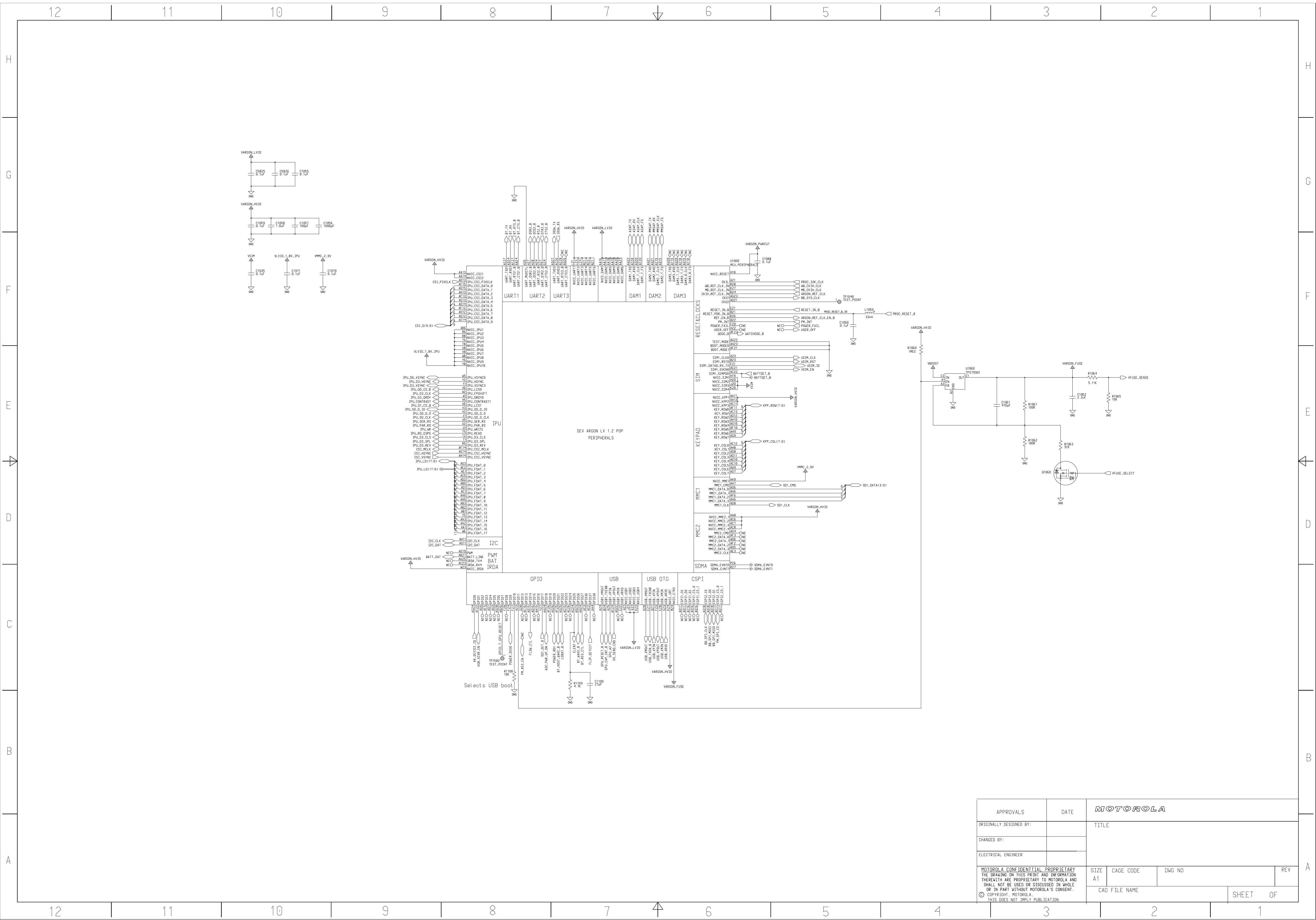






Engineer SSINGH2	Motorola, Inc.		
Drawn by SSINGH2			
R&D CHK	TITLE IZAR NA RF TOP		Size D
DOC CTRL CHK			
MFG CTRL CHK			
QA CHK	REV	Drawing Number	Sheet of


Changed by cbh030	Date Changed Friday, 2 June 2006	Time 3:23 PM
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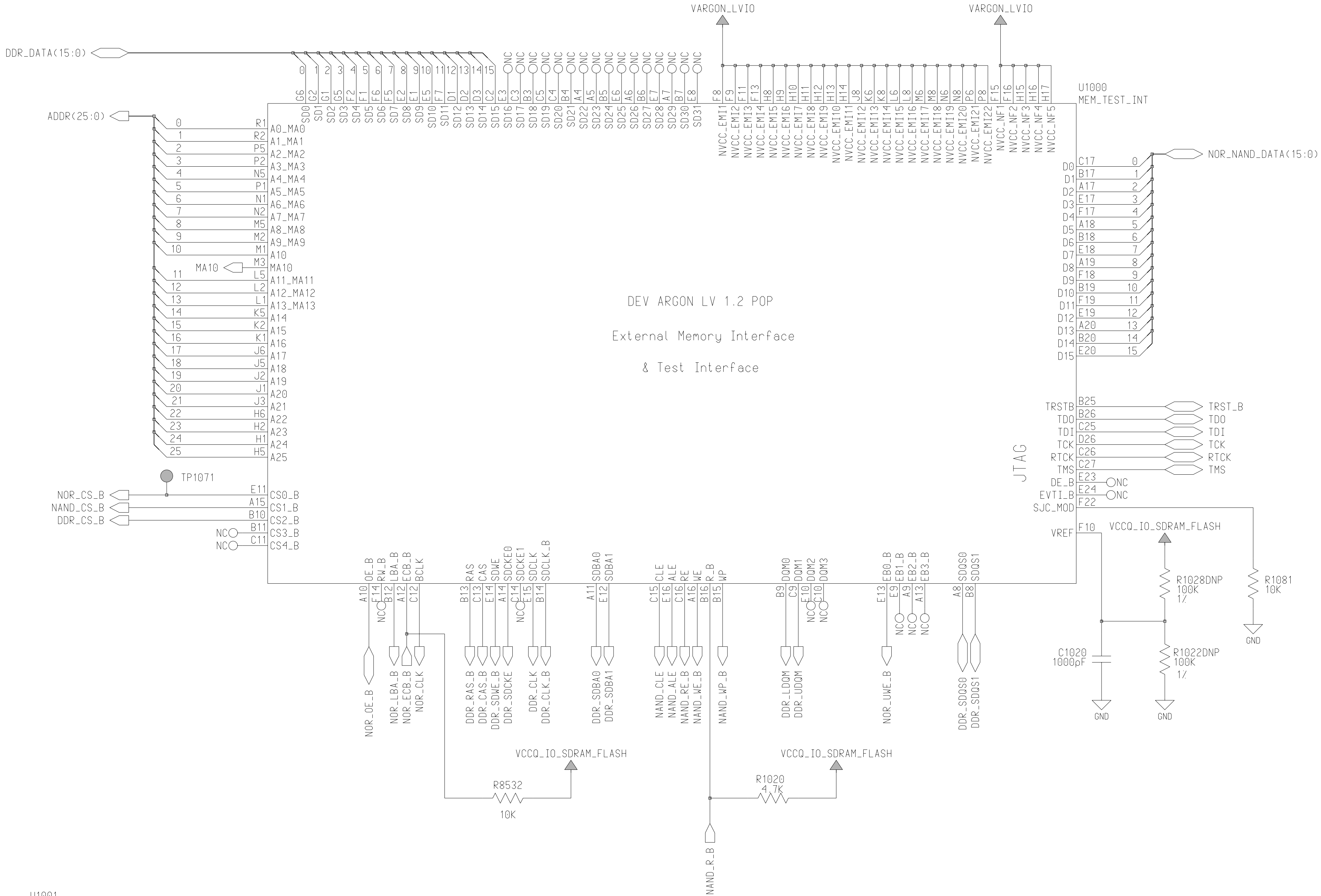
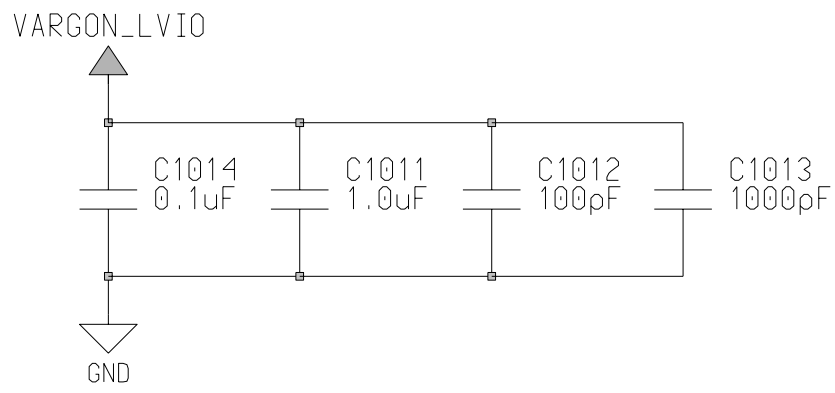


APPROVALS	DATE	MOTOROLA		
ORIGINALLY DESIGNED BY:		TITLE		
CHANGED BY:				
ELECTRICAL ENGINEER				
MOTOROLA CONFIDENTIAL PROPRIETARY THE DRAWING ON THIS PRINT AND INFORMATION THEREWITH ARE PROPRIETARY TO MOTOROLA AND SHALL NOT BE USED OR DISCLOSED IN WHOLE OR IN PART WITHOUT MOTOROLA'S CONSENT. © COPYRIGHT, MOTOROLA, THIS DOES NOT IMPLY PUBLICATION		SIZE A1	CAGE CODE	REV
		DWG NO		
CAD FILE NAME			SHEET OF	

MOTOROLA CONFIDENTIAL PROPRIETARY

4 1 3 A

Engineer: G15829	 MOTOROLA INC.		
Drawn by: G15829			
R&D CHK:	TITLE: IZAR NA Argon Memory		Size: 11x17
DOC CTRL CHK:			
MFG CTRL CHK:			
QA CHK:	REV:	Drawing Number: 8xxxxxxx	Page: Of:
Changed by:	Date:		Time:



5199213K02 (2x32/64/64 - Spansion NOR / Hynix NAND / Micron DDR)

MOTOROLA CONFIDENTIAL PROPRIETARY

4 1 3 A

Engineer :
G15829

Drawn by:
G15829

R&D CHK:

DOC CTRL CHK:

MFG CTRL CHK:

QA CHK:

 **MOTOROLA INC.**

TITLE: IZAR NA
Arogn DSP

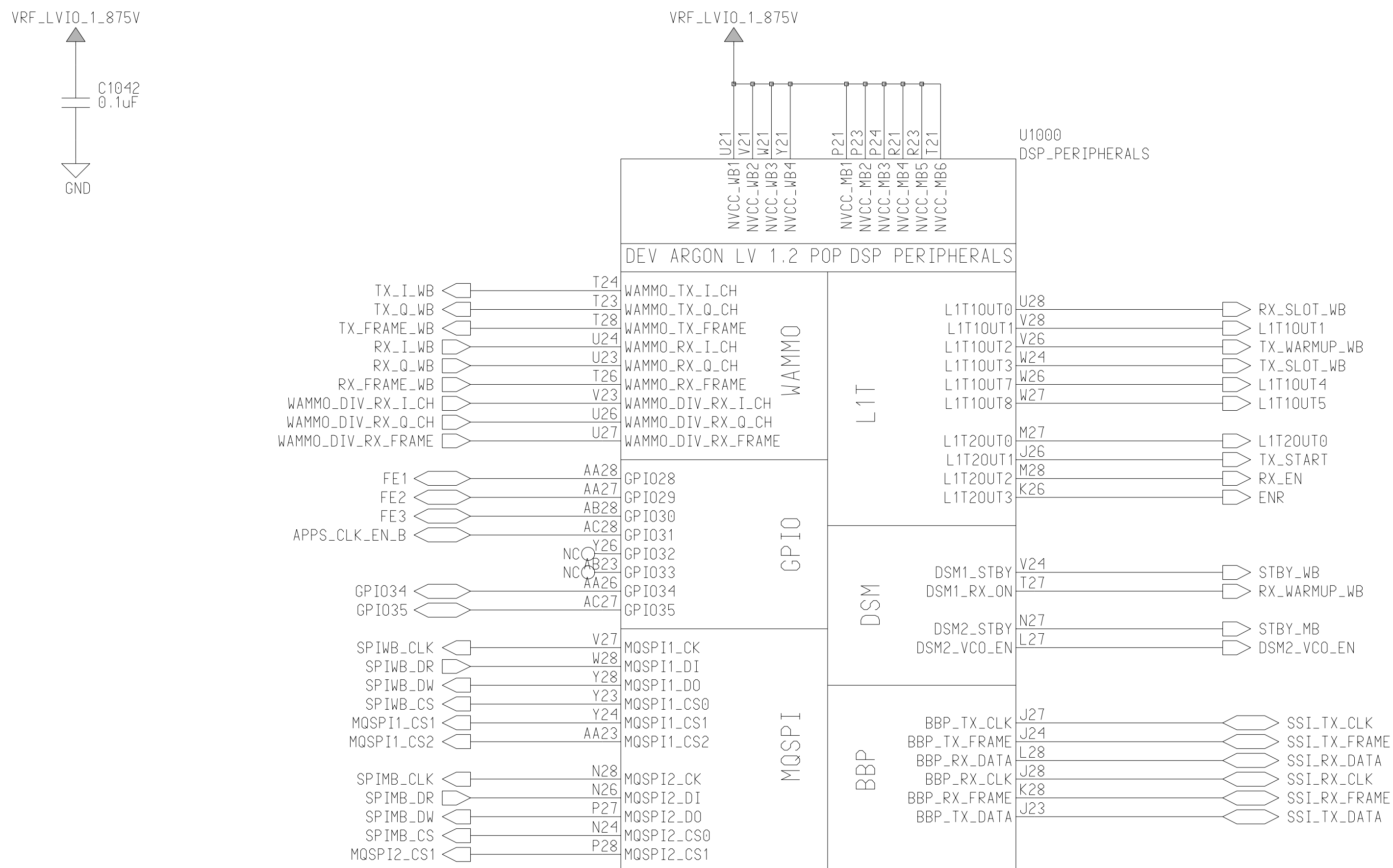
Size:
11x17

REV:	Drawing Number: 8xxxxxxxxxx	Page:	Of:
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Changed by:











Date: _____

Time:










```
U1000
_DSP_PERIPHERALS
```

DEV ARGON LV 1.2 POP DSP PERIPHERALS

TX_I_WB		T24	WAMMO_TX_I_CH	
TX_Q_WB		T23	WAMMO_TX_Q_CH	
TX_FRAME_WB		T28	WAMMO_TX_FRAME	
RX_I_WB		U24	WAMMO_RX_I_CH	
RX_Q_WB		U23	WAMMO_RX_Q_CH	
RX_FRAME_WB		T26	WAMMO_RX_FRAME	
WAMMO_DIV_RX_I_CH		V23	WAMMO_DIV_RX_I_CH	
WAMMO_DIV_RX_Q_CH		U26	WAMMO_DIV_RX_Q_CH	
WAMMO_DIV_RX_FRAME		U27	WAMMO_DIV_RX_FRAME	

Signal	Pin	Function
FE1	AA28	GP1028
FE2	AA27	GP1029
FE3	AB28	GP1030
APPS_CLK_EN_B	AC28	GP1031
	Y26	GP1032
	AB23	GP1033
GP1034	AA26	GP1034
GP1035	AC27	GP1035

SPIWB_CLK		V27	MQSPI1_CLK	
SPIWB_DR		W28	MQSPI1_D0	
SPIWB_DW		Y28	MQSPI1_D0	
SPIWB_CS		Y23	MQSPI1_CS0	
MQSPI1_CS1		Y24	MQSPI1_CS1	
MQSPI1_CS2		AA23	MQSPI1_CS2	

SPIMB_CLK	N28	MQSP12_CK
SPIMB_DR	N26	MQSP12_DI
SPIMB_DW	P27	MQSP12_DO
SPIMB_CS	N24	MQSP12_CS0
MQSP12_CS1	P28	MQSP12_CS1

L1T

L1T1OUT0
L1T1OUT1
L1T1OUT2
L1T1OUT3
L1T1OUT7
L1T1OUT8
L1T2OUT0
L1T2OUT1
L1T2OUT2
L1T2OUT3

DSM1_STBY
DSM1_RX_ON

BBP_TX_CLK
BBP_TX_FRAME

```
BBP_RX_DATA
BBP_RX_CLK
BBP_RX_FRAME
BBP_TX_DATA
```

U28
V28
V28
W28
W28
W28

M27
J28
M28
K28

V24
T27
N27

L2	
J2	
J2	
L3	

J28
K28
J23

U28		RX_SLOT_WB
V28		L1T1OUT1
V26		TX_WARMUP_WB
W24		TX_SLOT_WB
W26		L1T1OUT4
W27		L1T1OUT5
M27		L1T2OUT0
J26		TX_START
M28		RX_EN
K26		ENR

V24
T27

STBY_WB
RX_WARMUP_WB

L27 S1B1_MB
 DSM2_VCO_EN

 J27 SSI_TX_CLK
 J24 SSI_TX_FRAME
 J28

J28	SSI_RX_DATA
K28	SSI_RX_CLK
	SSI_RX_FRAME
J23	SSI_TX_DATA

A

B

C

D

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MOTOROLA CONFIDENTIAL PROPRIETARY

4 1 3 A



Engineer:

G15829

Drawn by:

G15829

R&D CHK:

DOC CTRL CHK:

MFG CTRL CHK:

QA CHK:

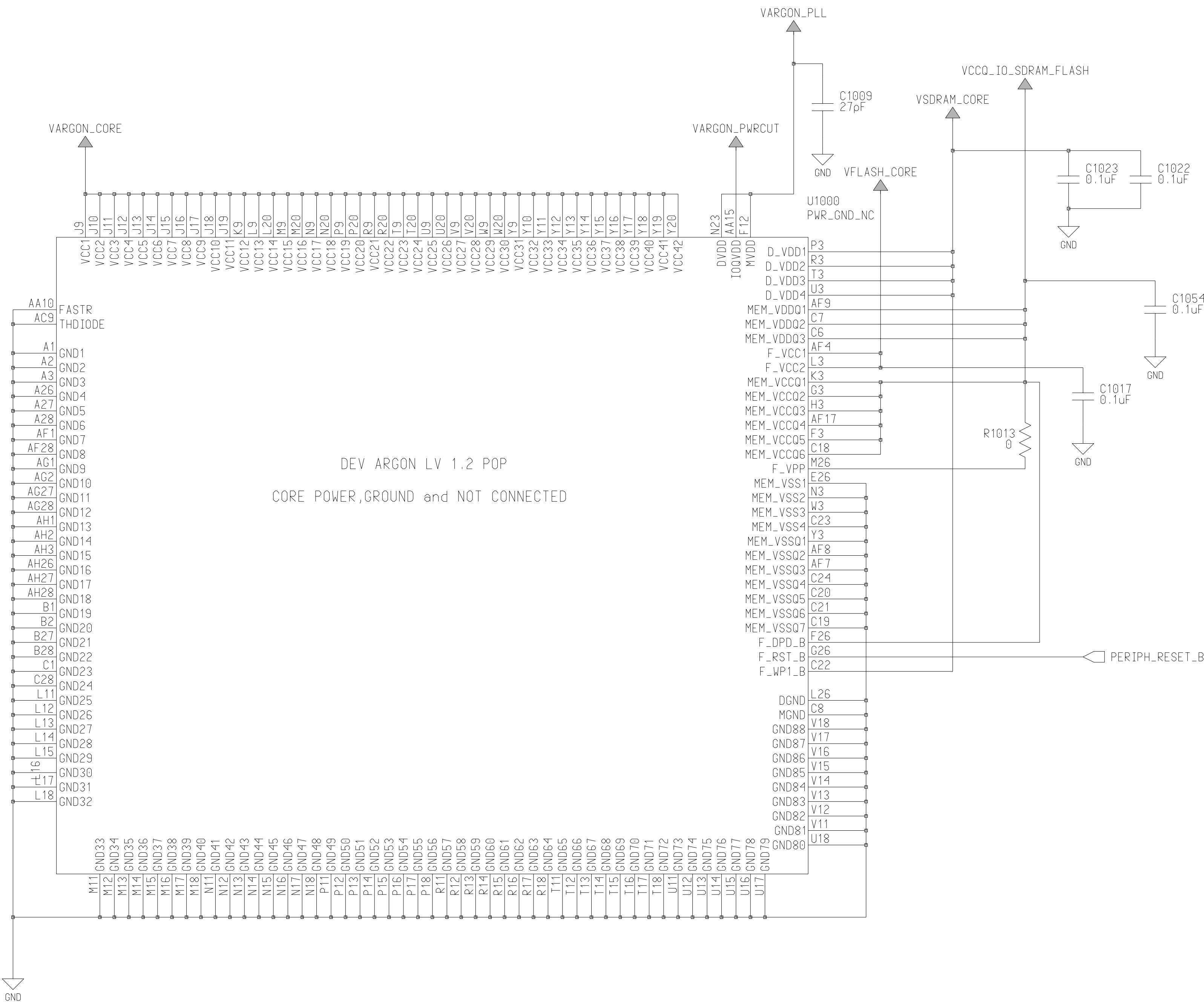
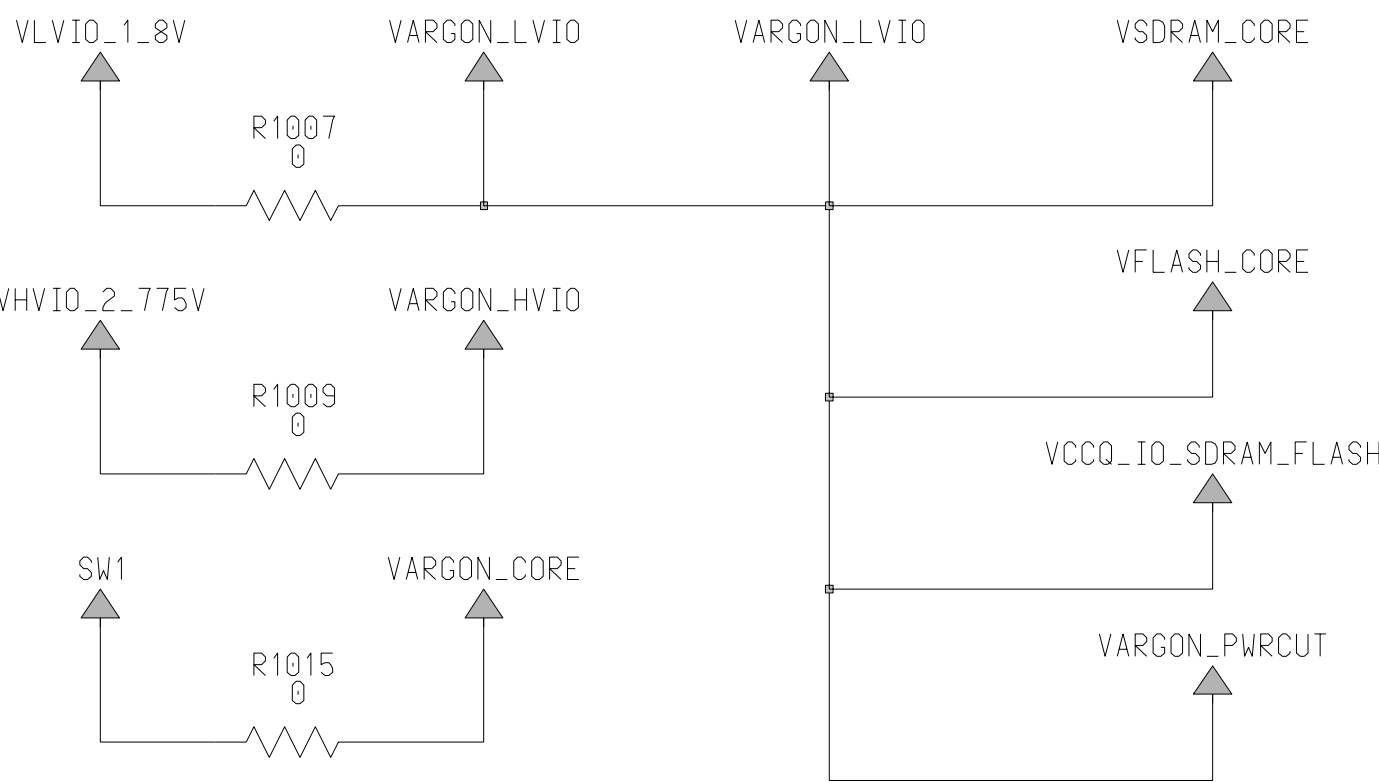
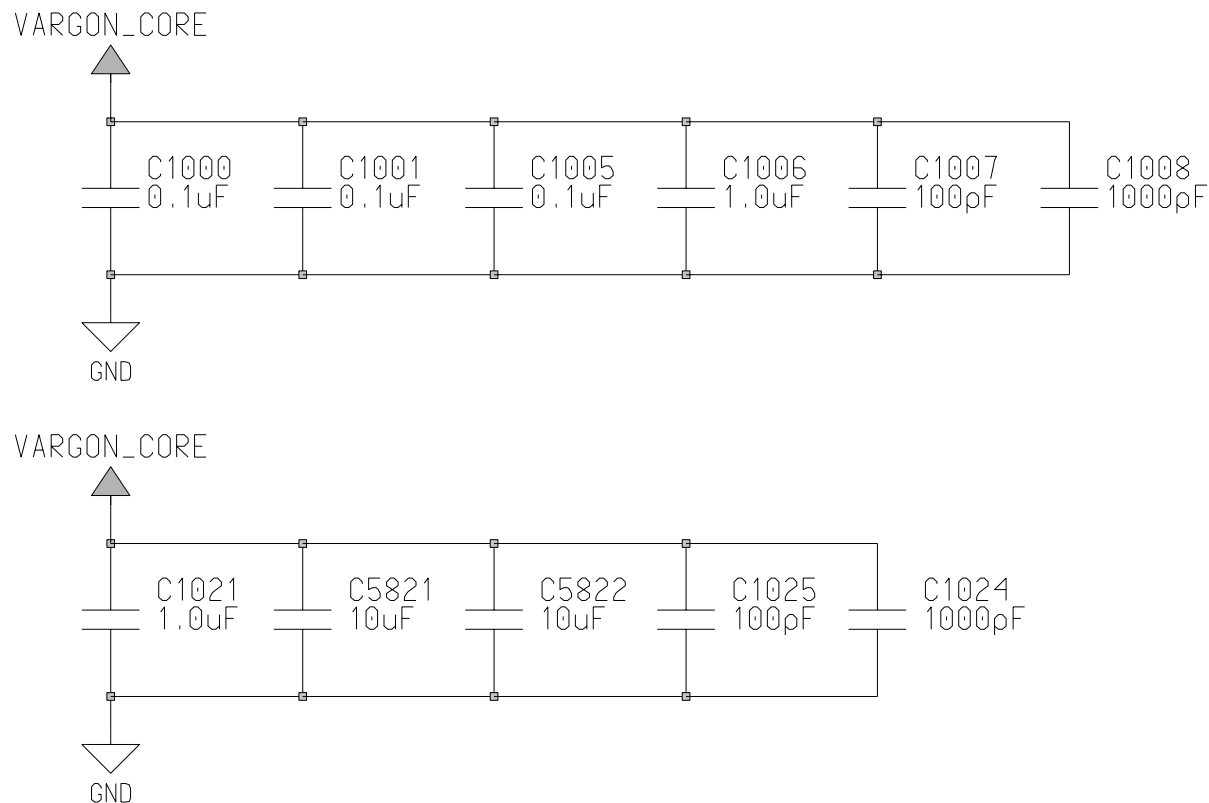
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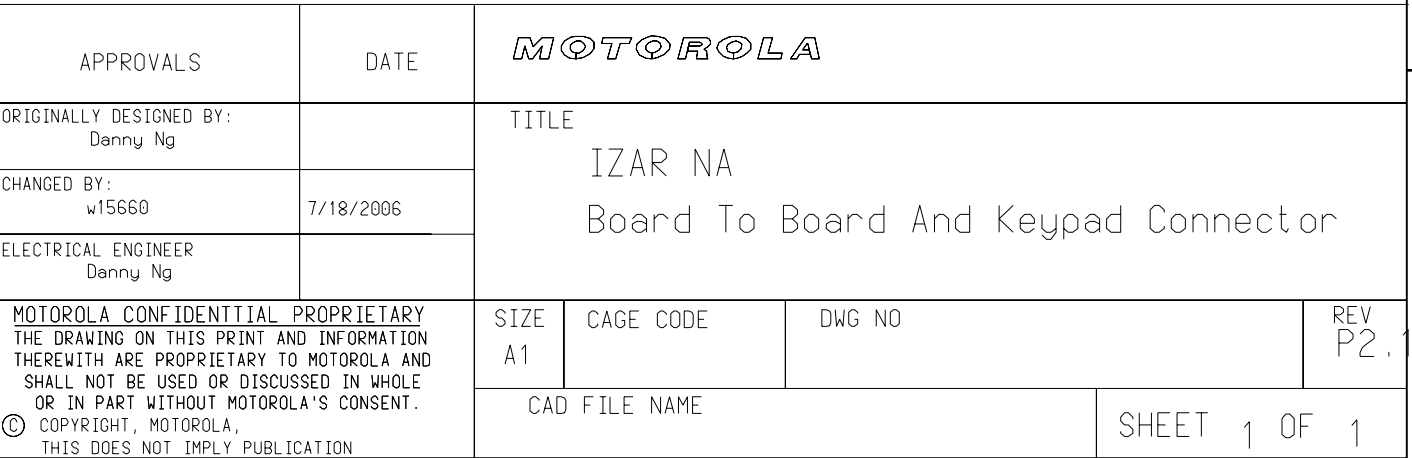
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Argon Core Power

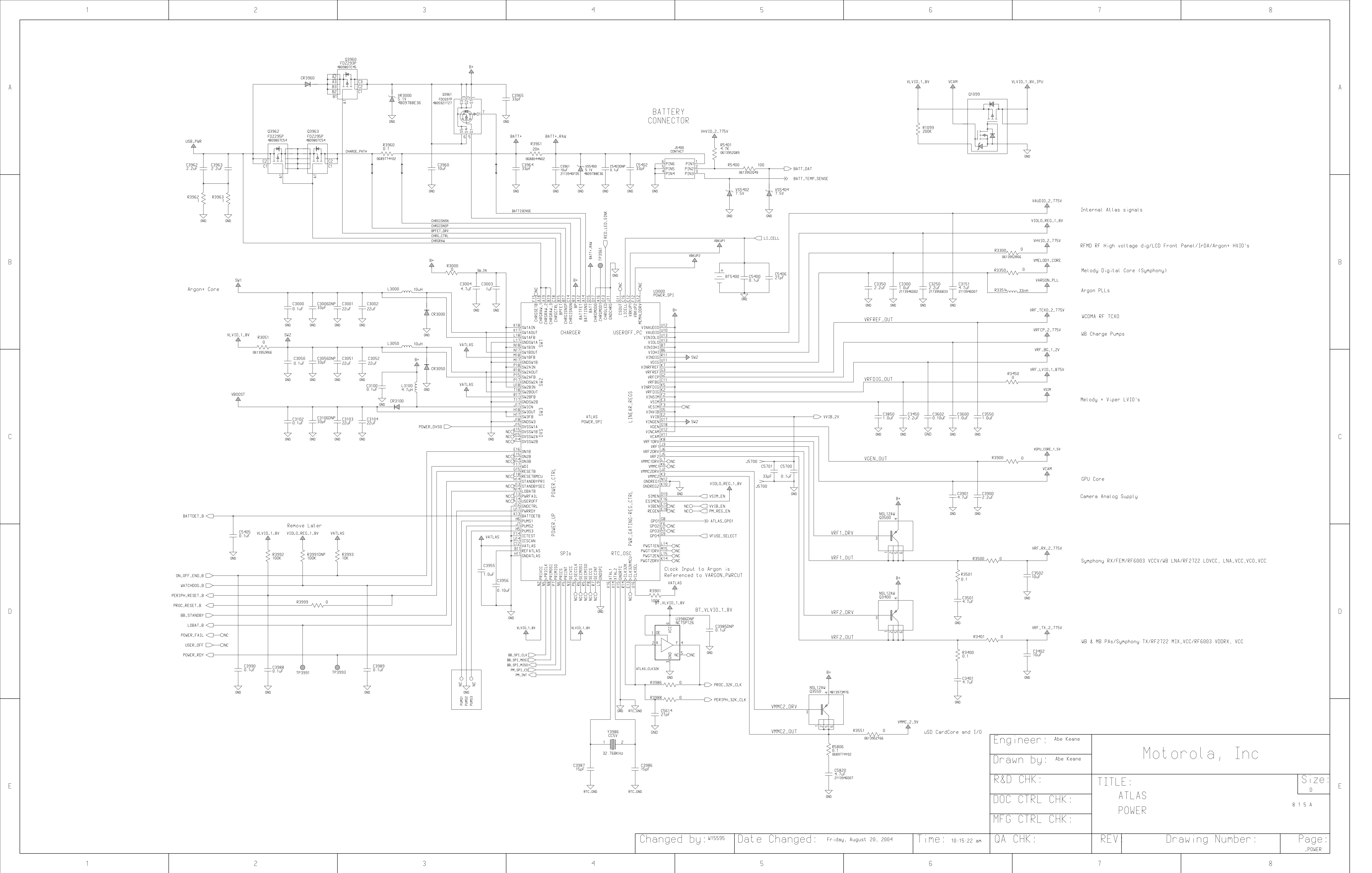
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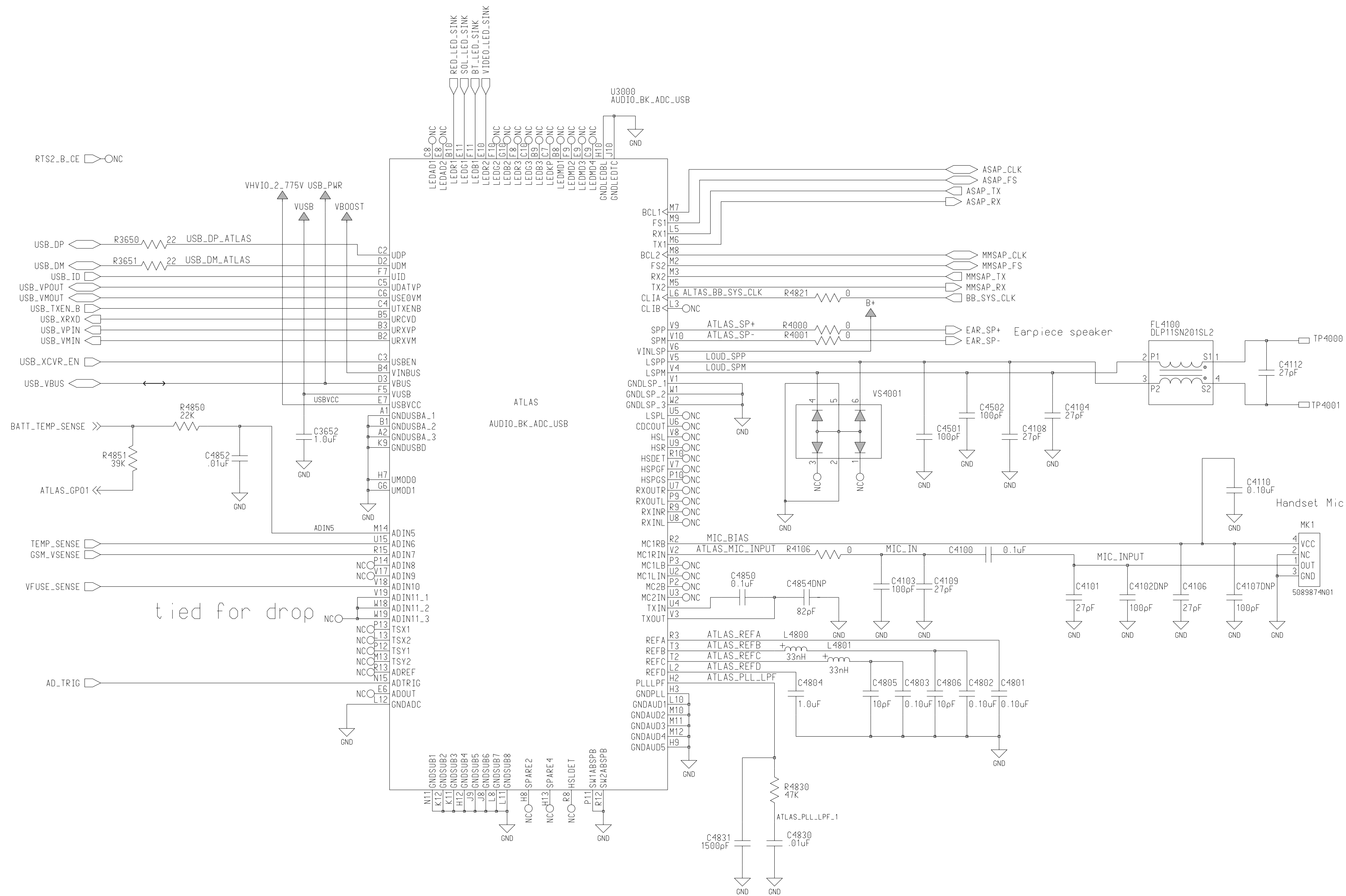
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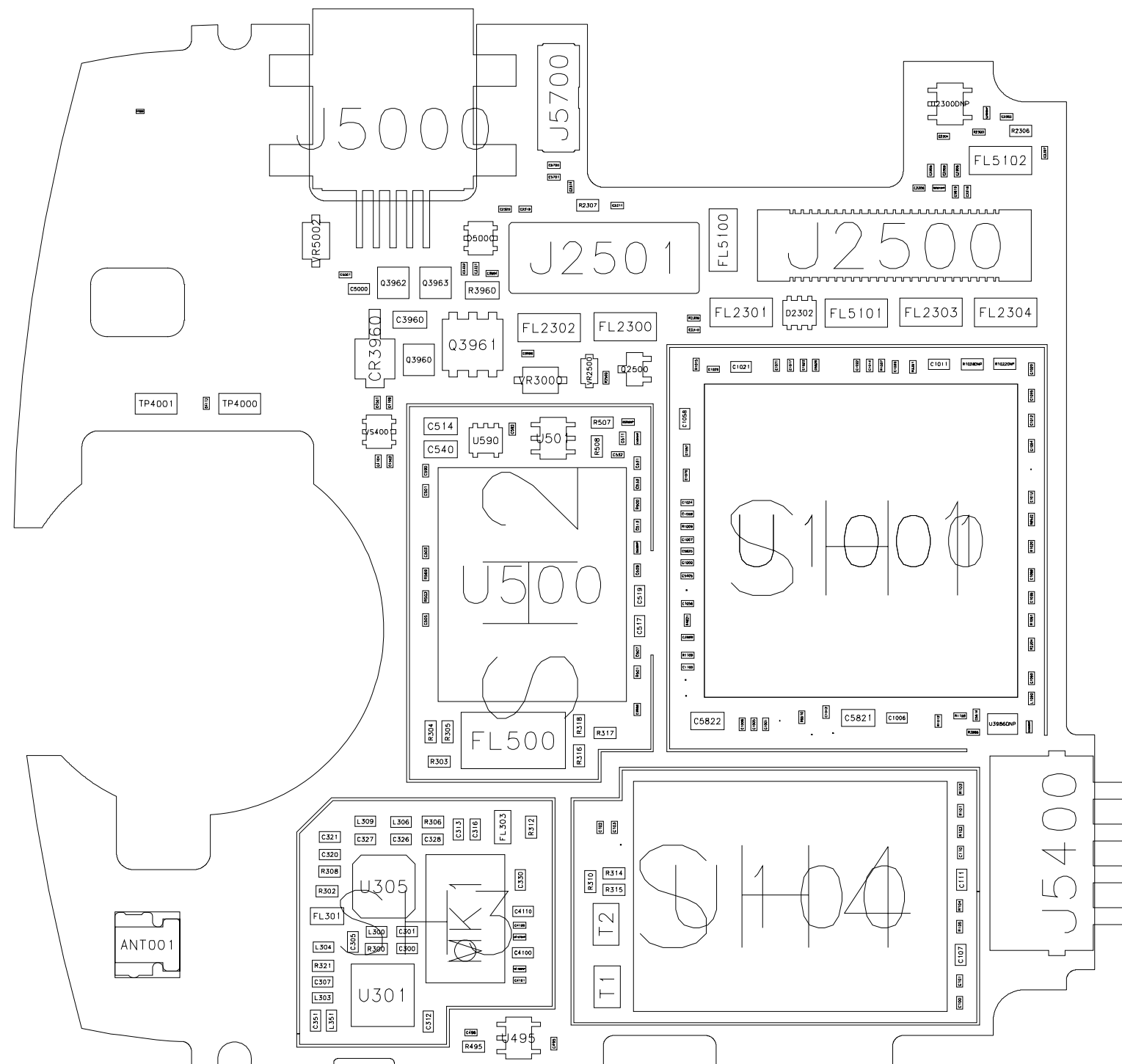












TOP SIDE PARTS