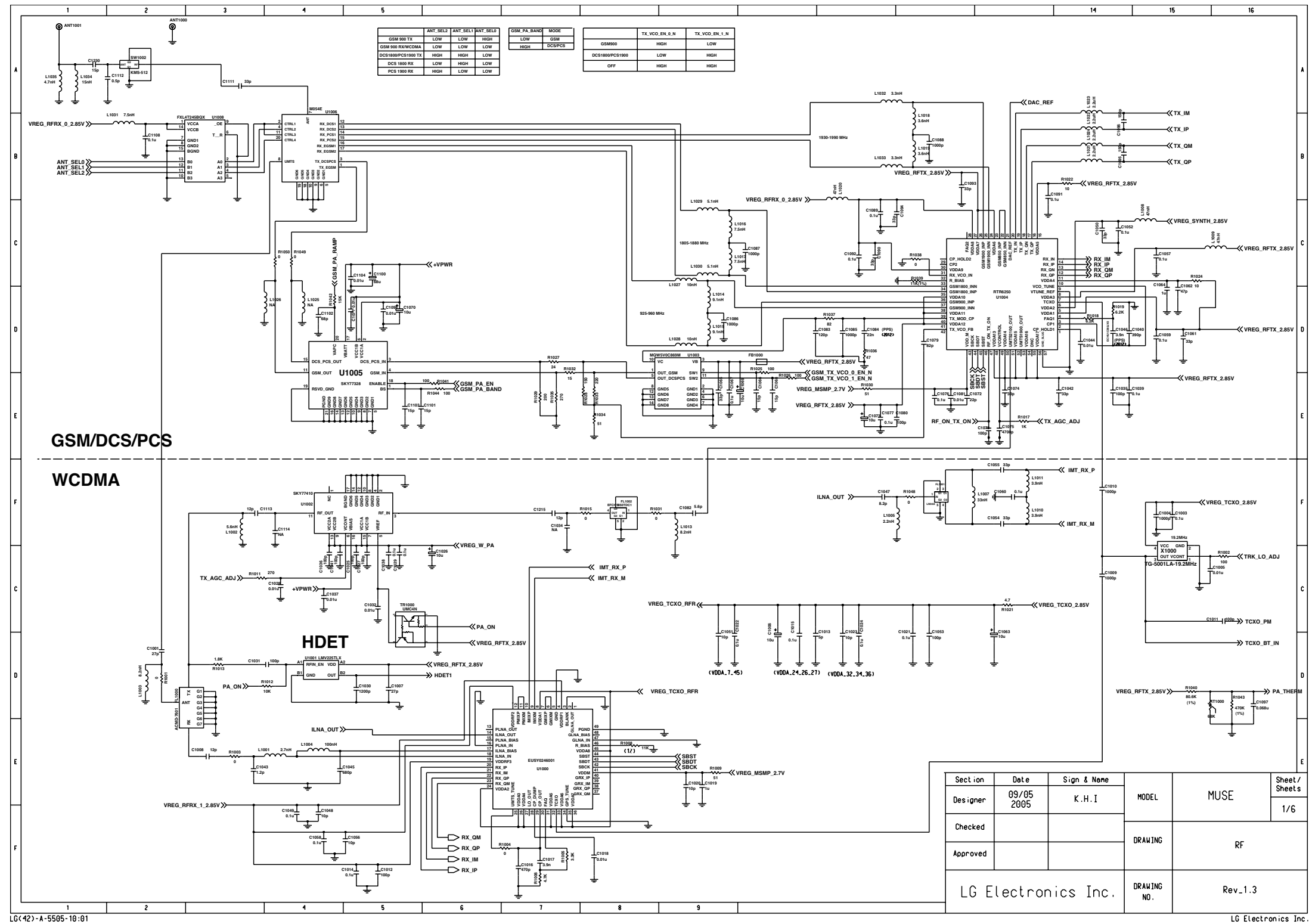
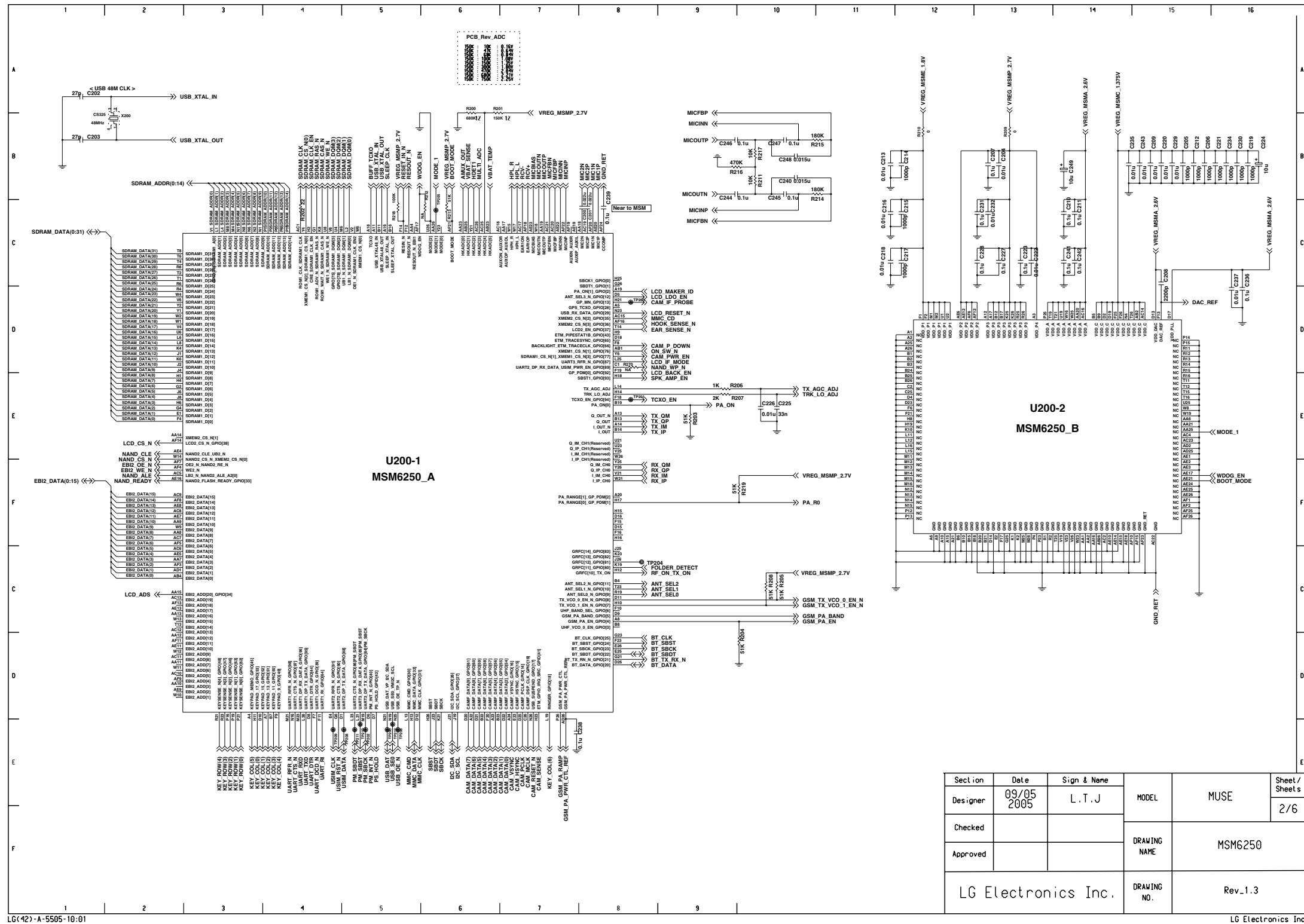


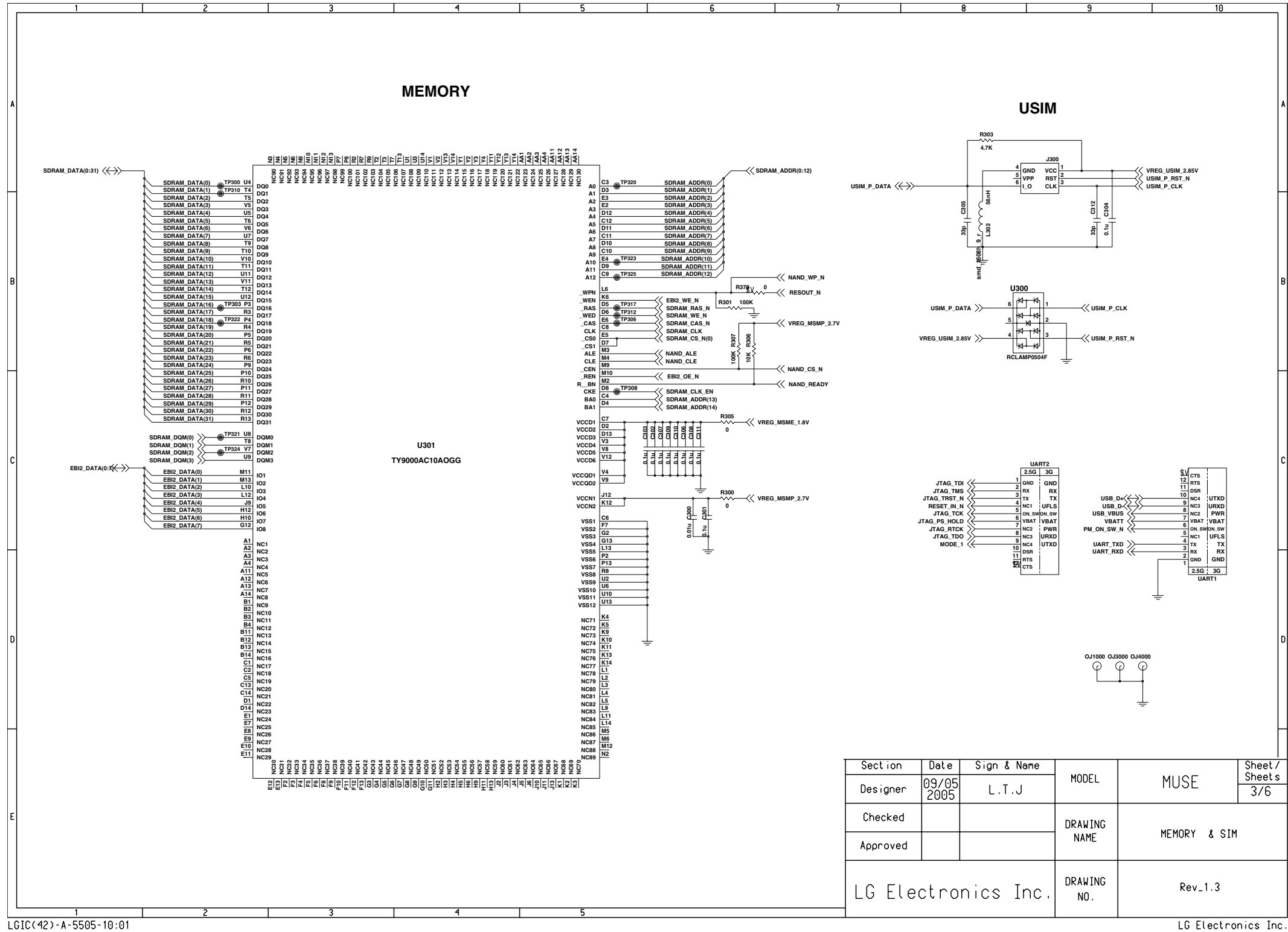
## 7. CIRCUIT DIAGRAM



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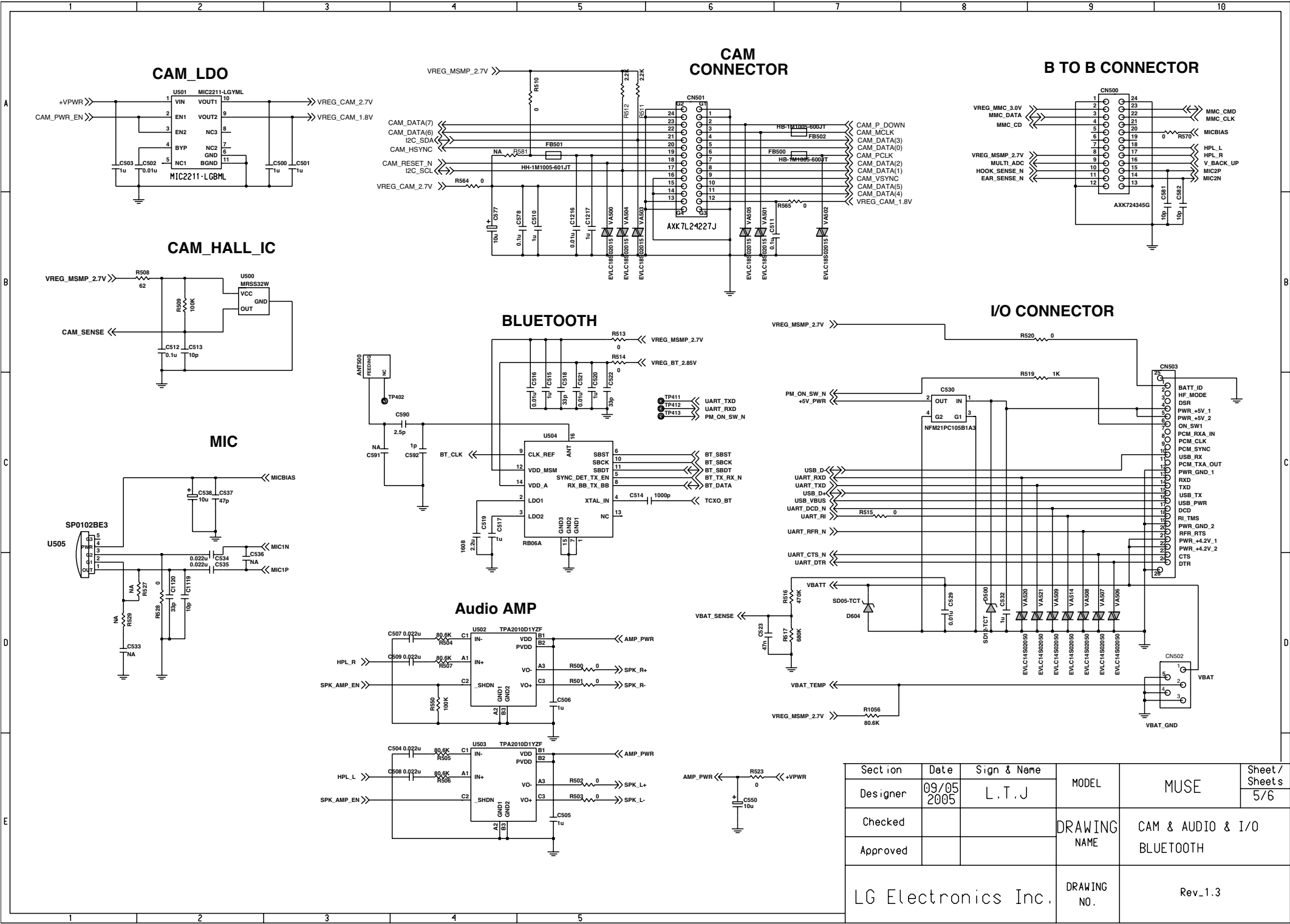


The schematic diagram illustrates the internal circuitry of a PMIC (U402), specifically the PM6650. It shows the connection of various pins to external components such as capacitors, resistors, and transistors. Key sections include:

- Power Input/Output:** Connections for +VPWR, VREG\_PA, VREG\_BT, and USB\_VBUS.
- Battery Protection:** Circuits involving Q400, Q401, and Q402 for ICHARGE, BATT\_FET\_N, and VBAT.
- Voltage Regulation:** Multiple LDO regulators (e.g., VREG\_TCXO, VREG\_SINTX, VREG\_RFTX, VREG\_RFRX, VREG\_BT, VREG\_MSMP, VREG\_LCD, VREG\_MSM) with their respective feedback networks and output filters.
- Control Logic:** Internal logic blocks like U401 (RCLAMP0504F) and U400 (TC7S04FU) for signal processing and level shifting.
- Sensors and Monitors:** Components like TP489 and TP490 for temperature monitoring.

Section	Date	Sign & Name	MODEL	MUSE	Sheet / Sheets
Designer	09/05 2005	L.T.J			
Checked			DRAWING NAME	PMIC	
Approved					
LG Electronics Inc.			DRAWING NO.	Rev_1.3	

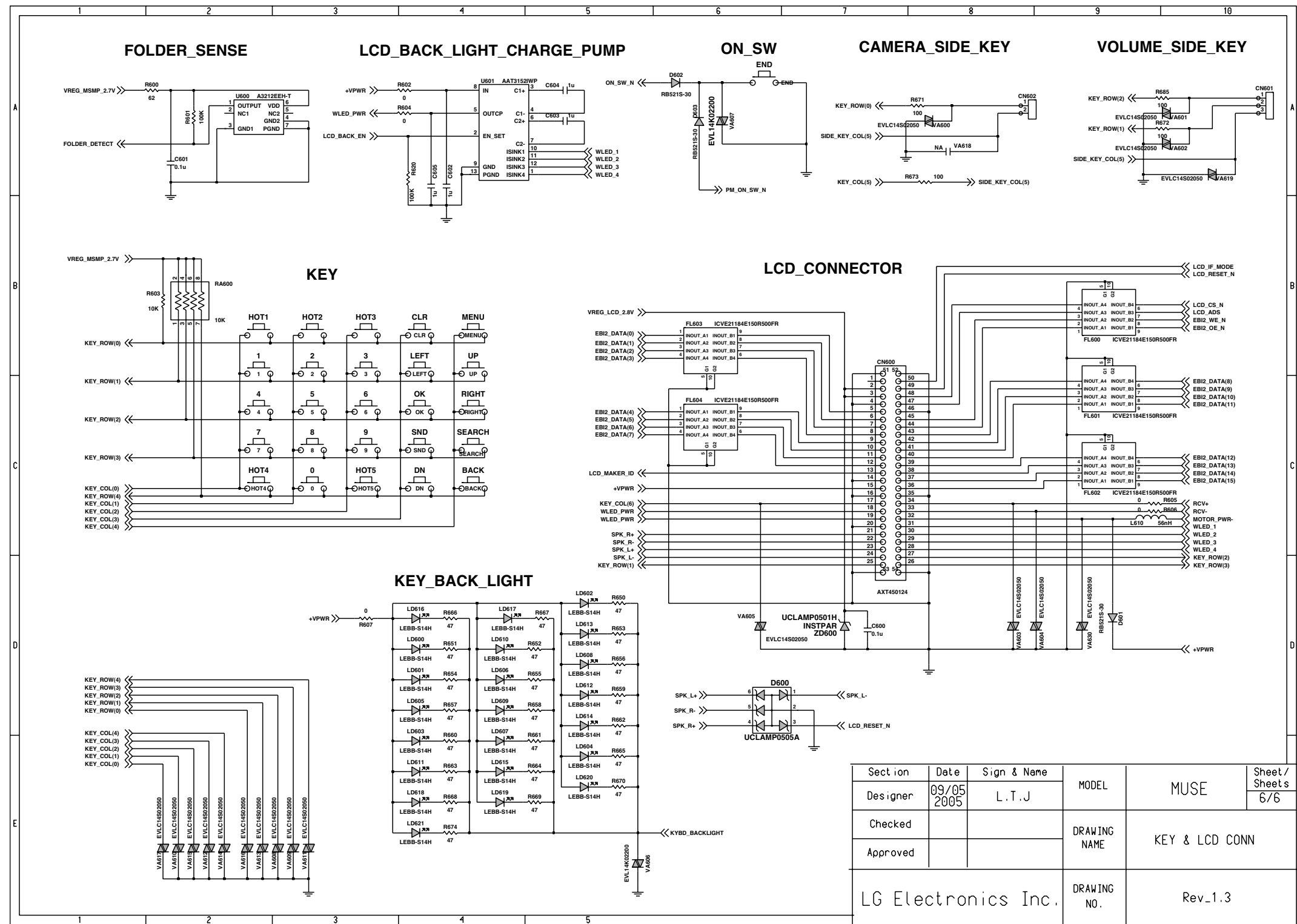
7. CIRCUIT DIAGRAM



LGIC(42)-A-5505-10:01

LG Electronics Inc.

# 7. CIRCUIT DIAGRAM

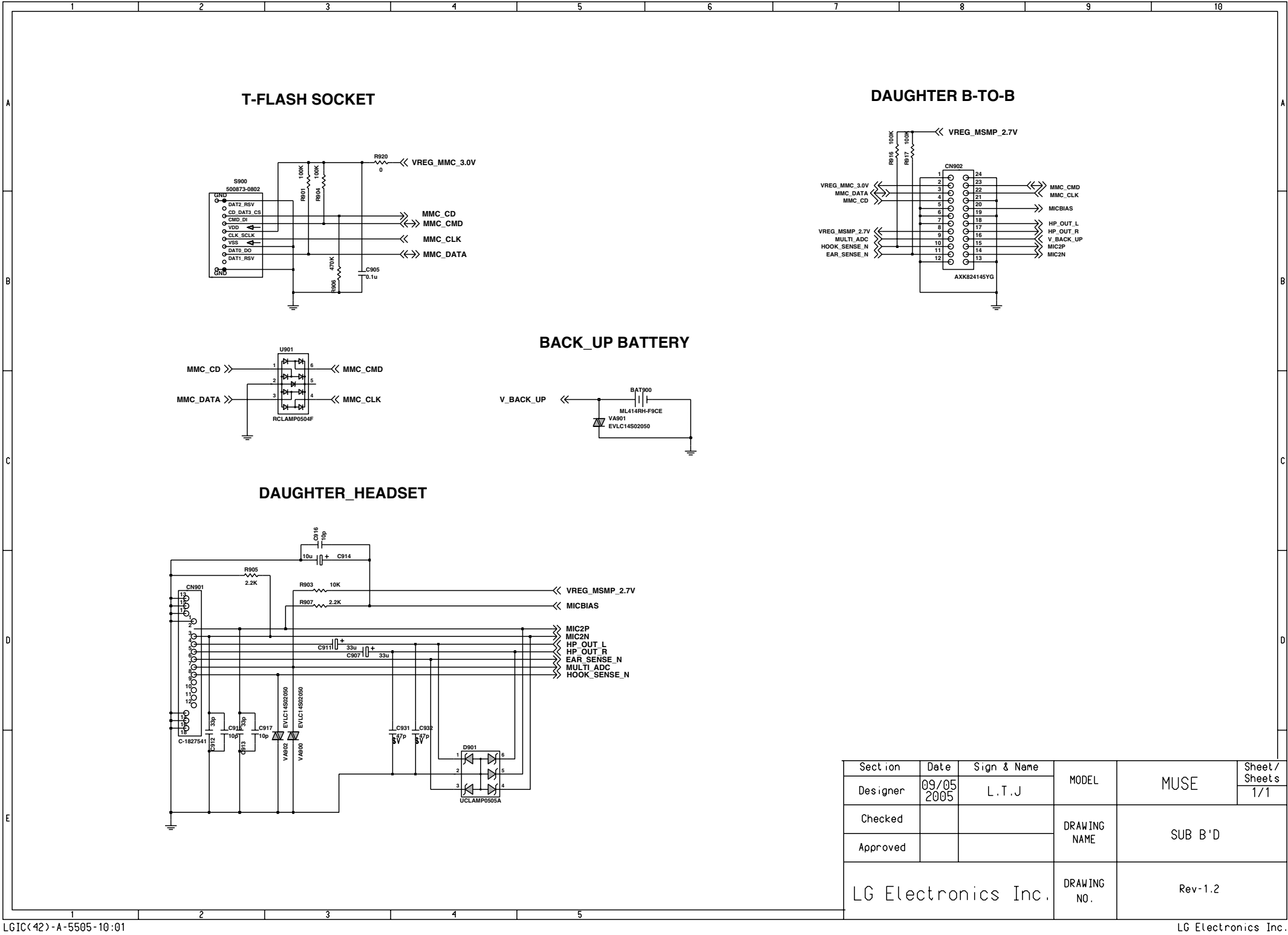


LGIC(42)-A-5505-10:01

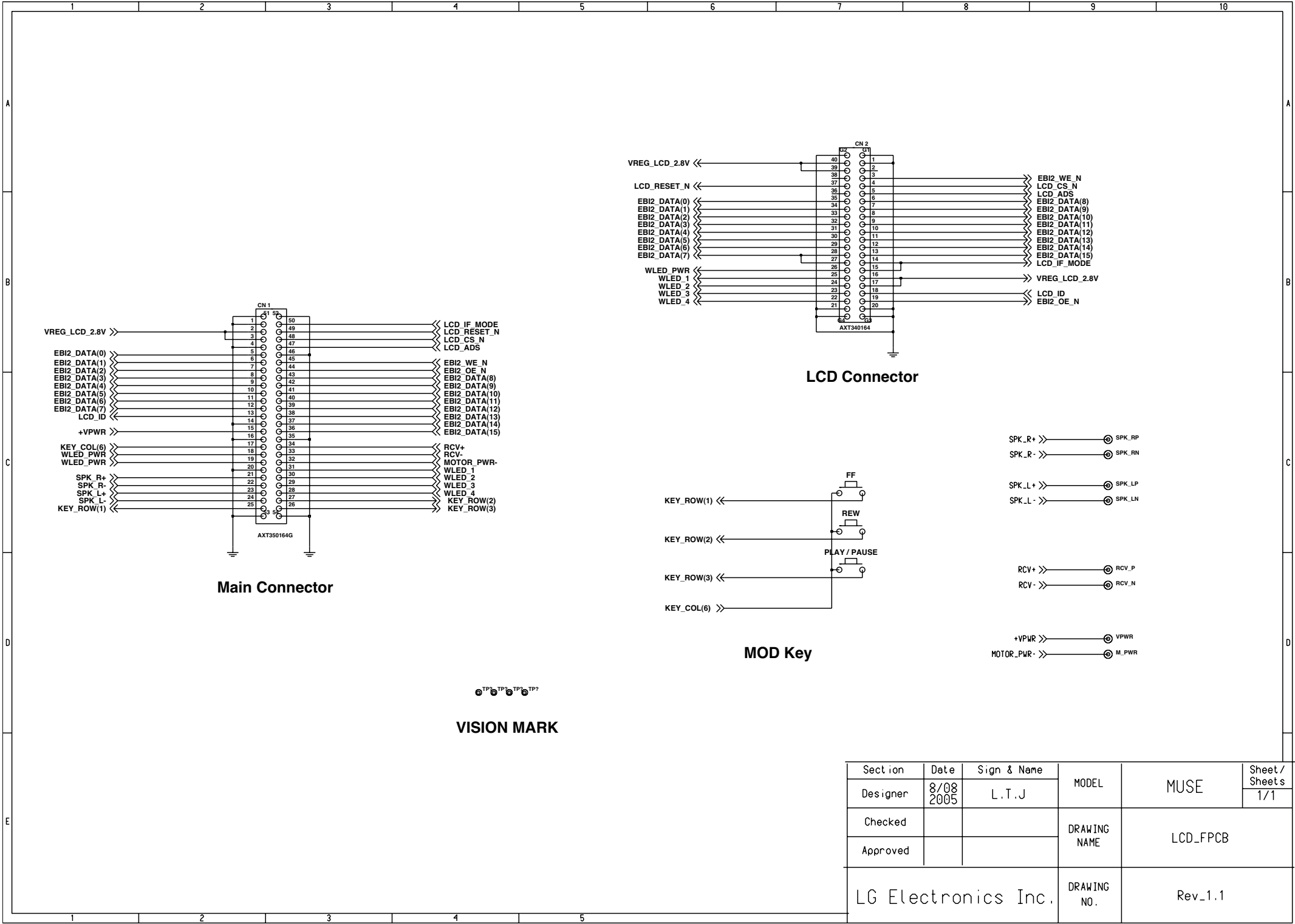
LG Electronics Inc.

Section	Date	Sign & Name	MODEL	MUSE	Sheet / Sheets
Designer	09/05/2005	L.T.J			6/6
Checked			DRAWING NAME	KEY & LCD CONN	
Approved			DRAWING NO.	Rev.1.3	
LG Electronics Inc.					

7. CIRCUIT DIAGRAM



7. CIRCUIT DIAGRAM

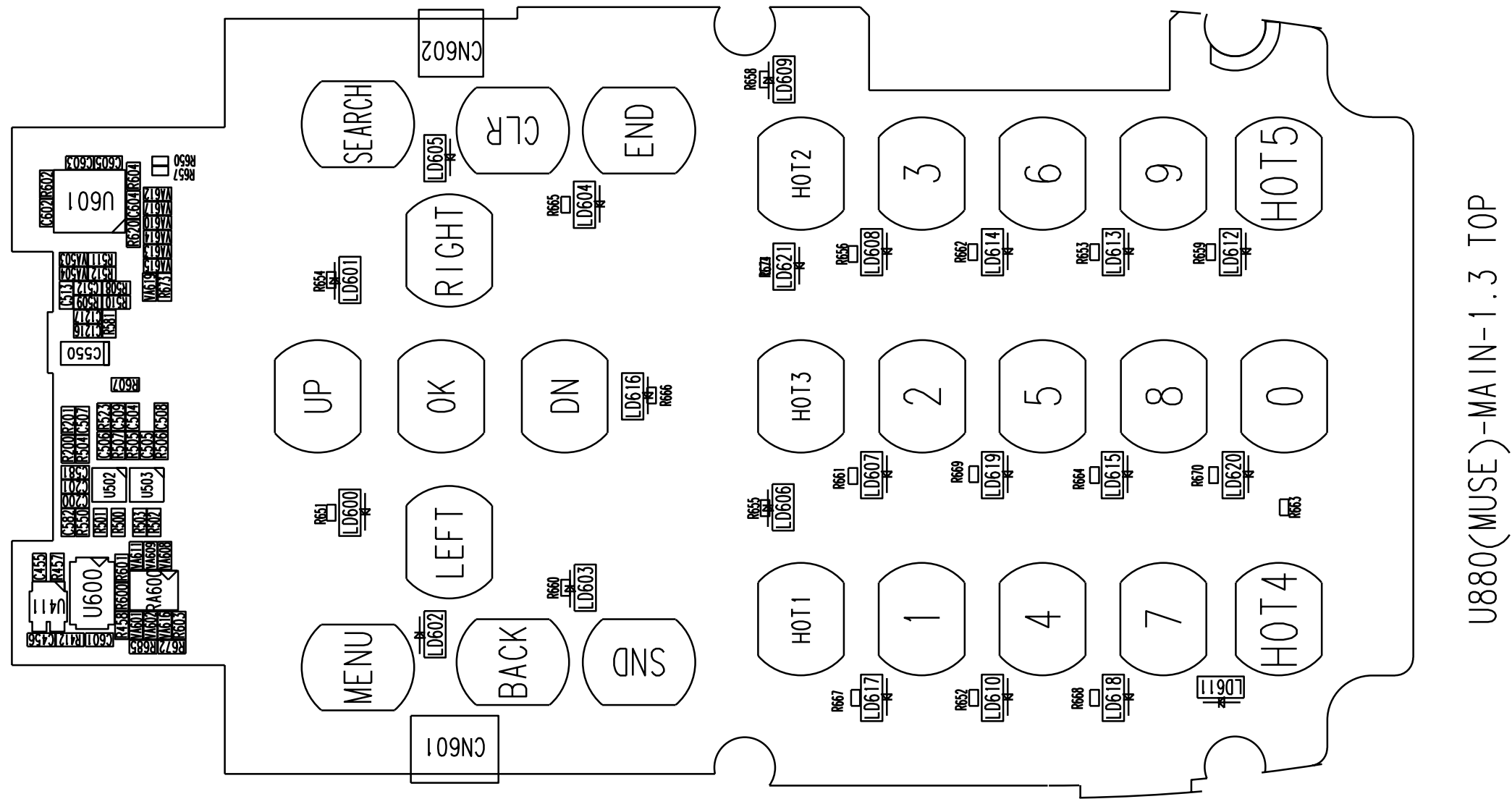


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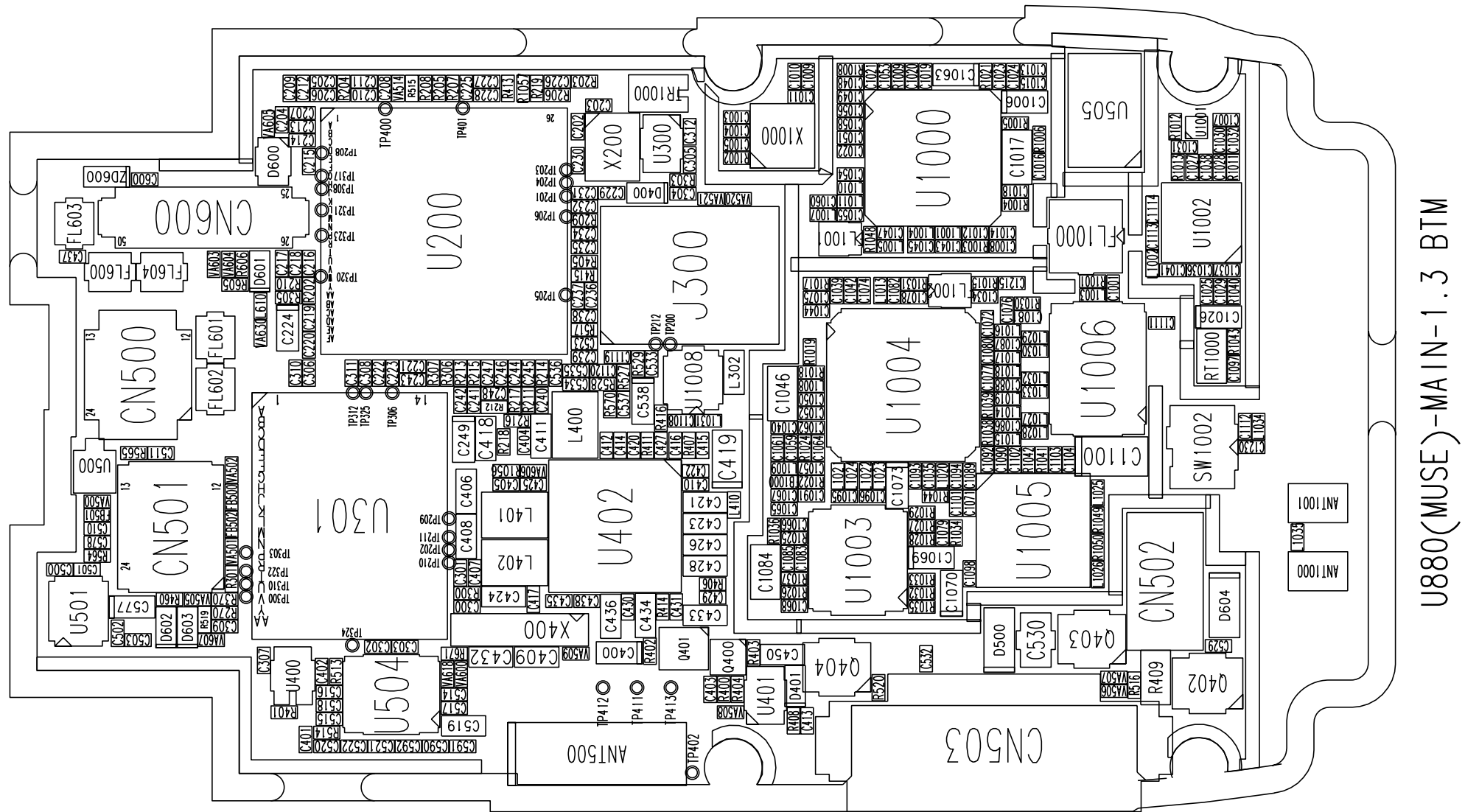
LG Electronics Inc.



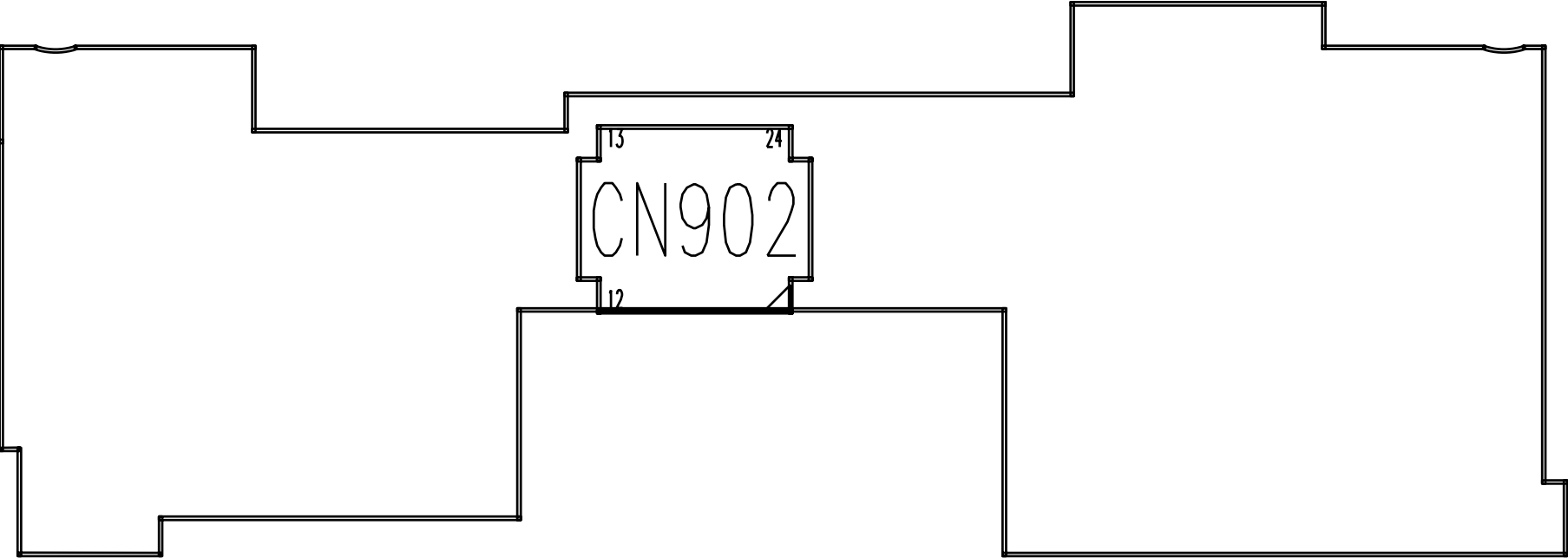
## 8. PCB LAYOUT



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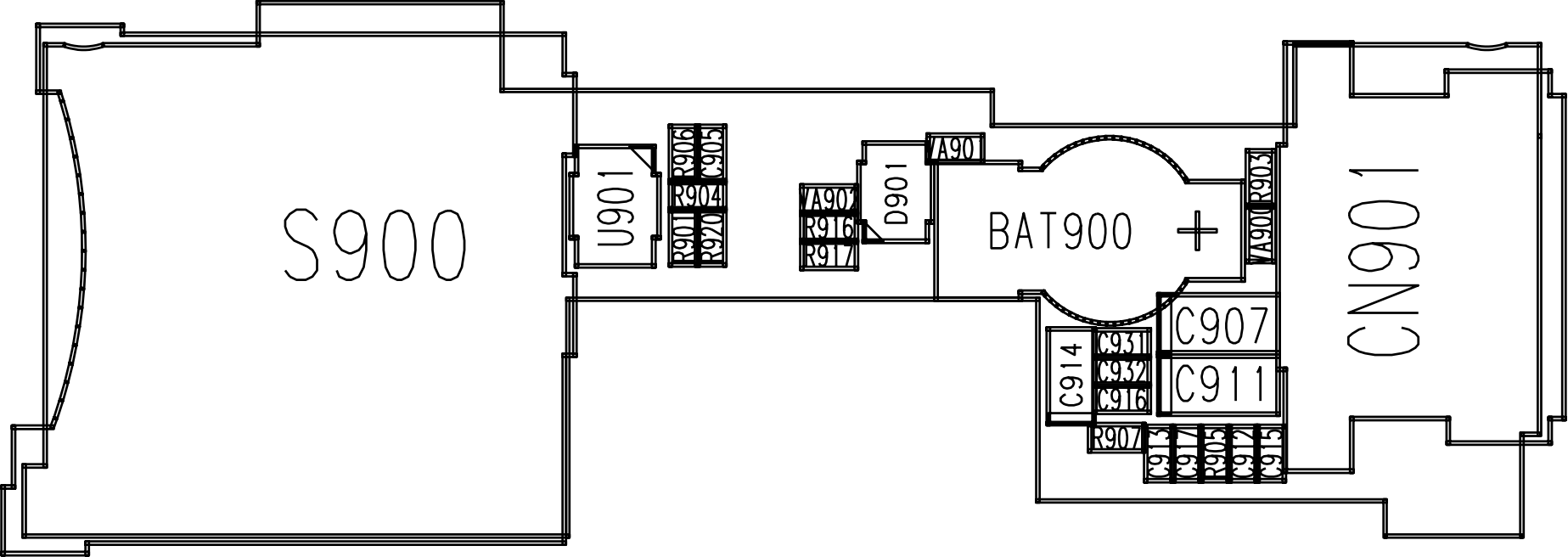


8. PCB LAYOUT



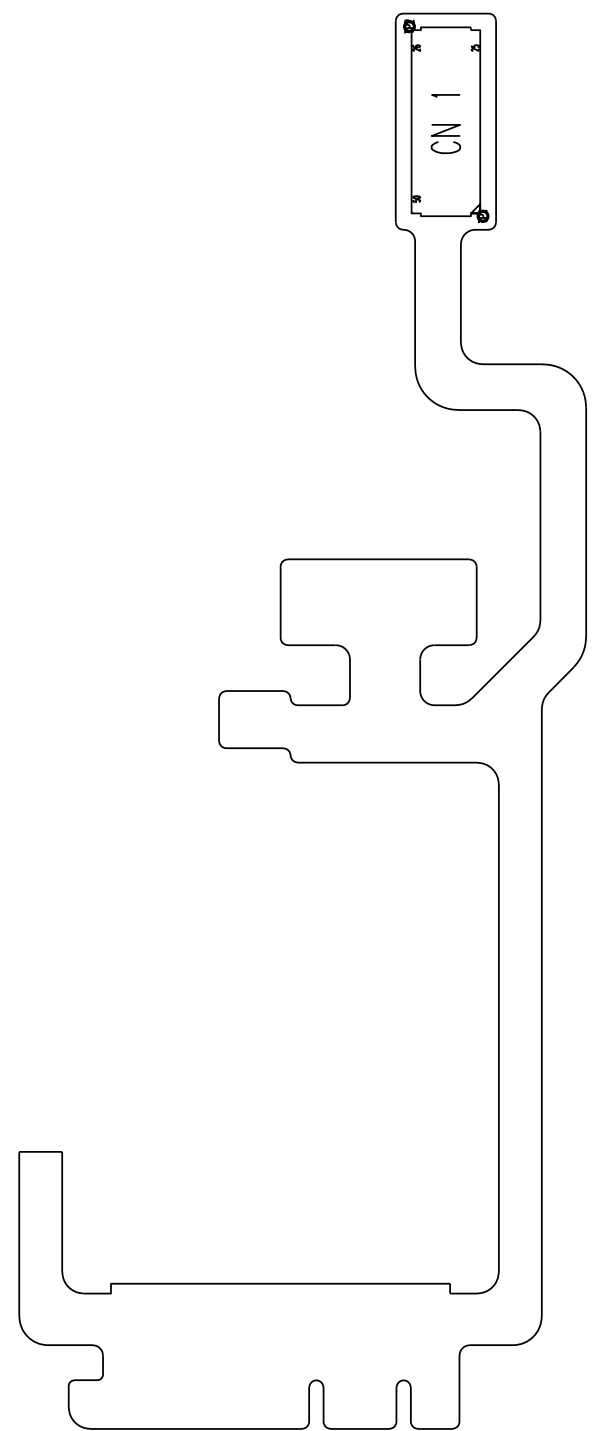
MUSE SUB  
MUSE SPJY0016301-1.2 TOP

8. PCB LAYOUT



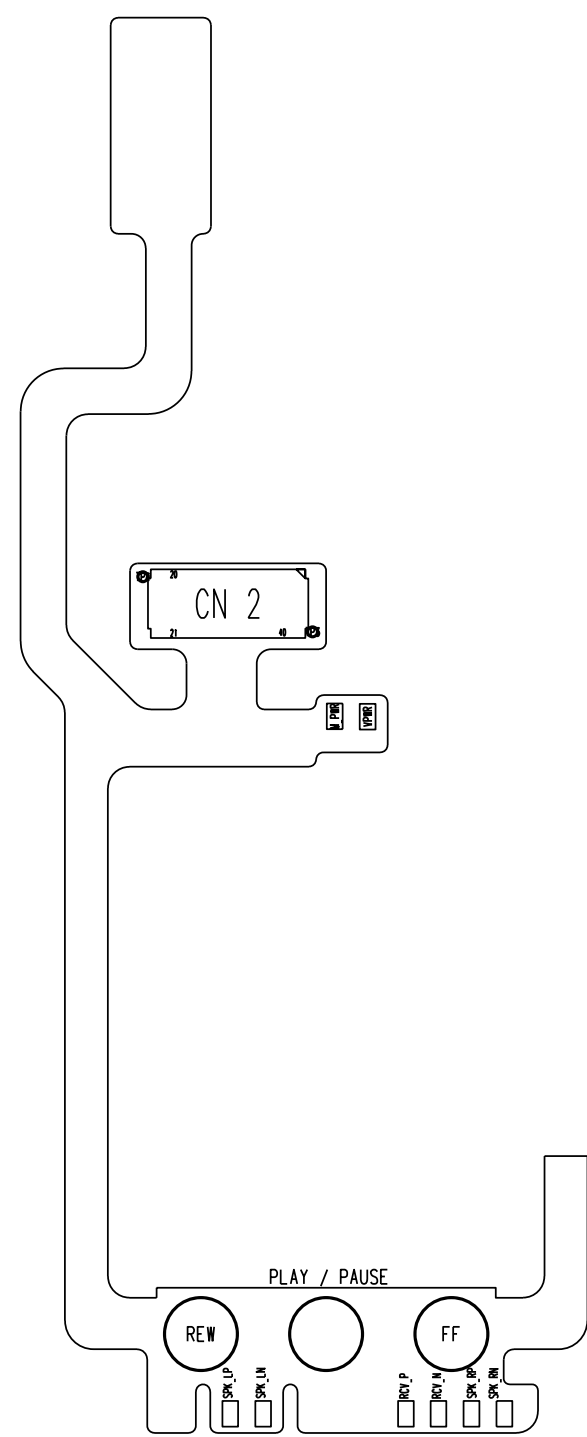
MUSE SUB  
MUSE SPJY0016301-1.2 BTM

8. PCB LAYOUT



U880(MUSE) SPCY0053901-1.1-BTM

8. PCB LAYOUT



U880(MUSE) SPCY0053901-1.1-T0P