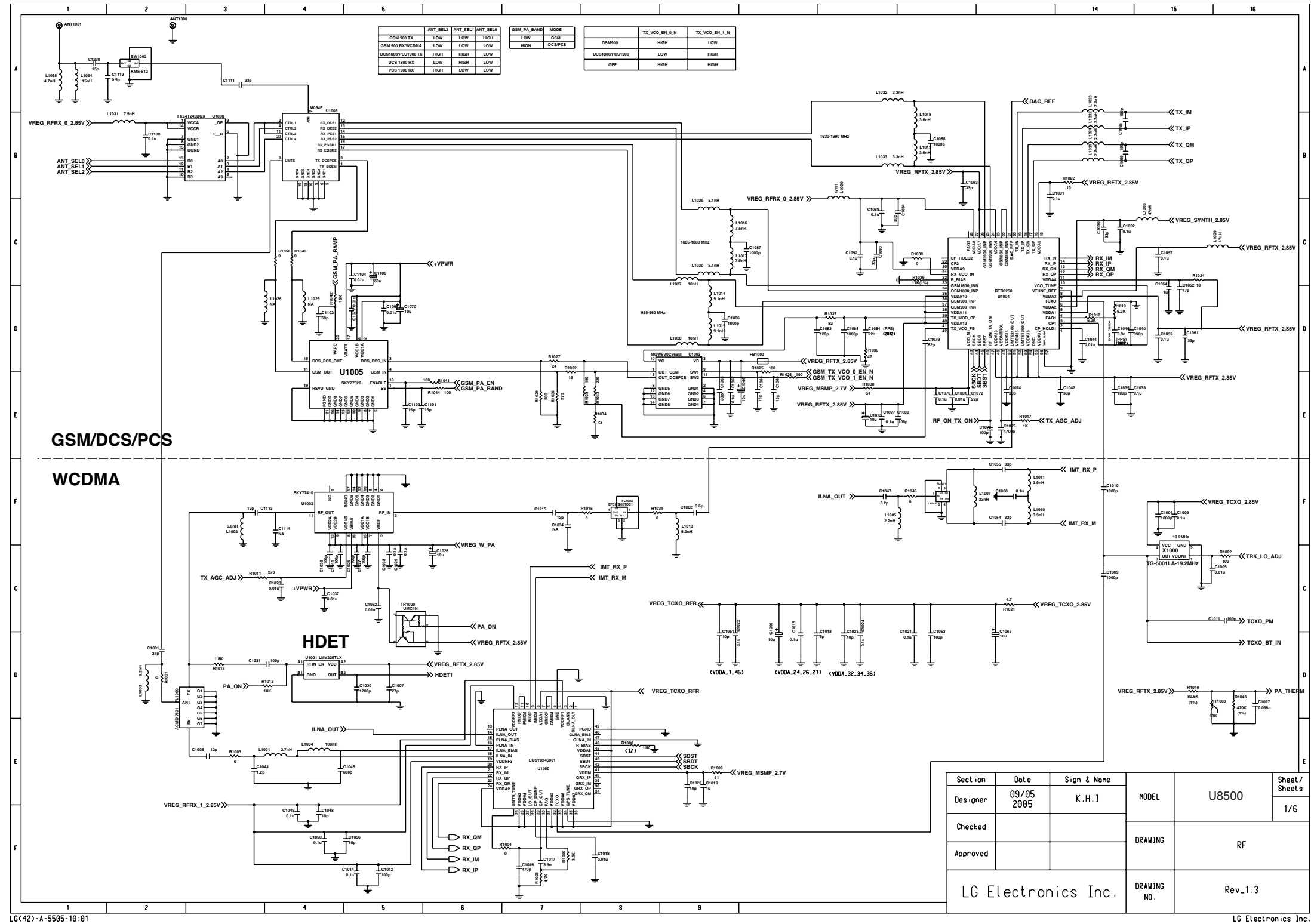
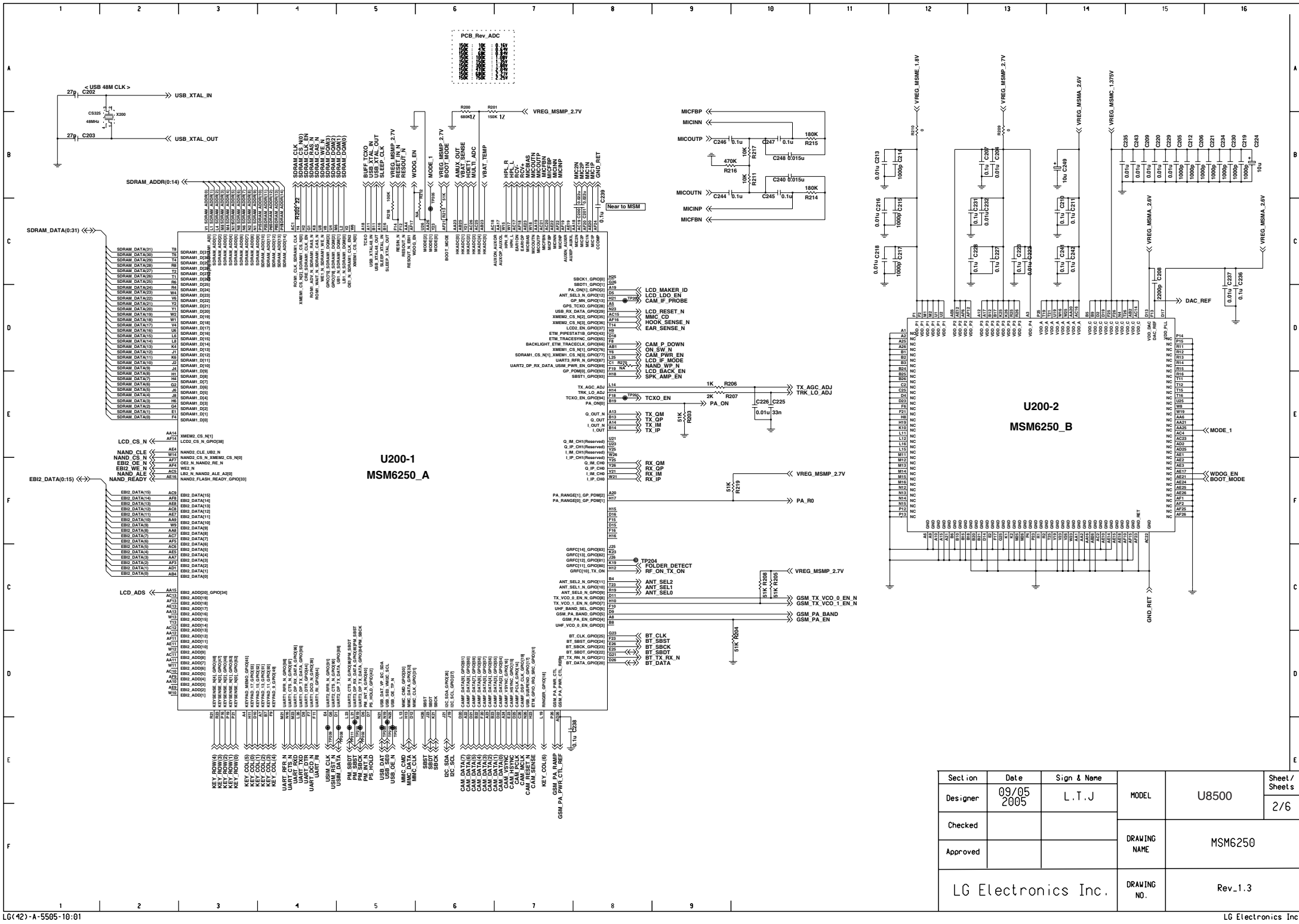


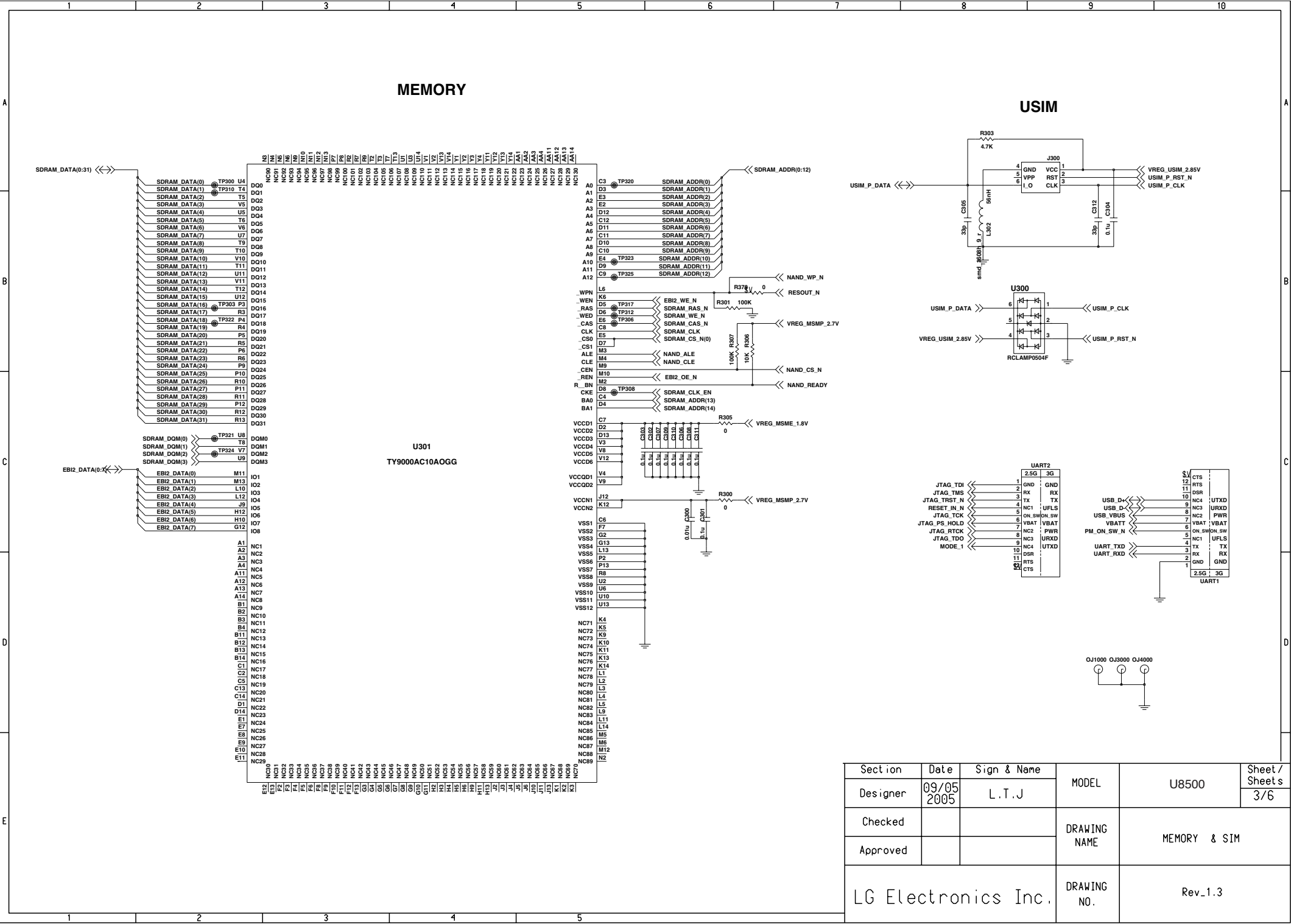
7. CIRCUIT DIAGRAM



7. CIRCUIT DIAGRAM

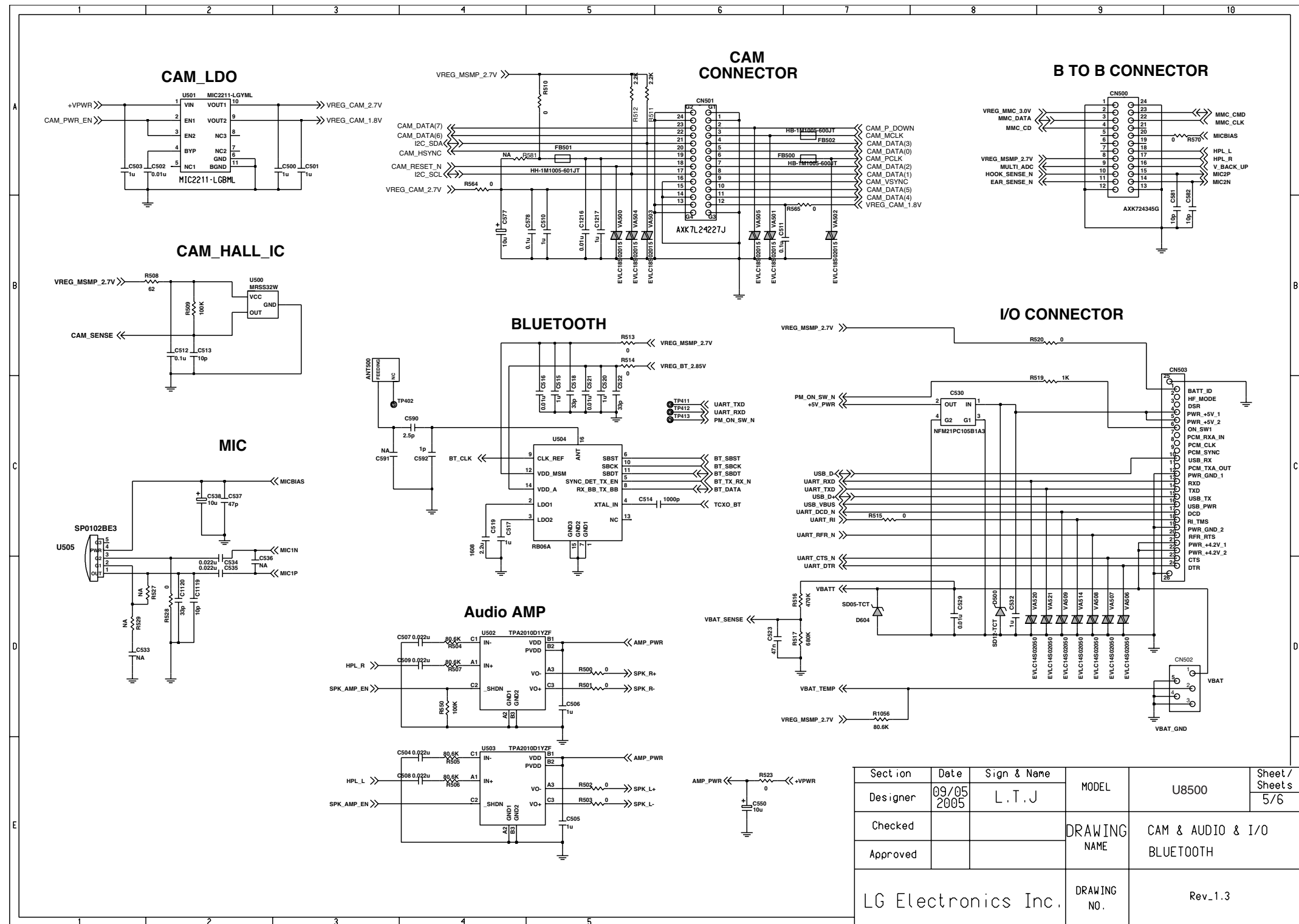


7. CIRCUIT DIAGRAM

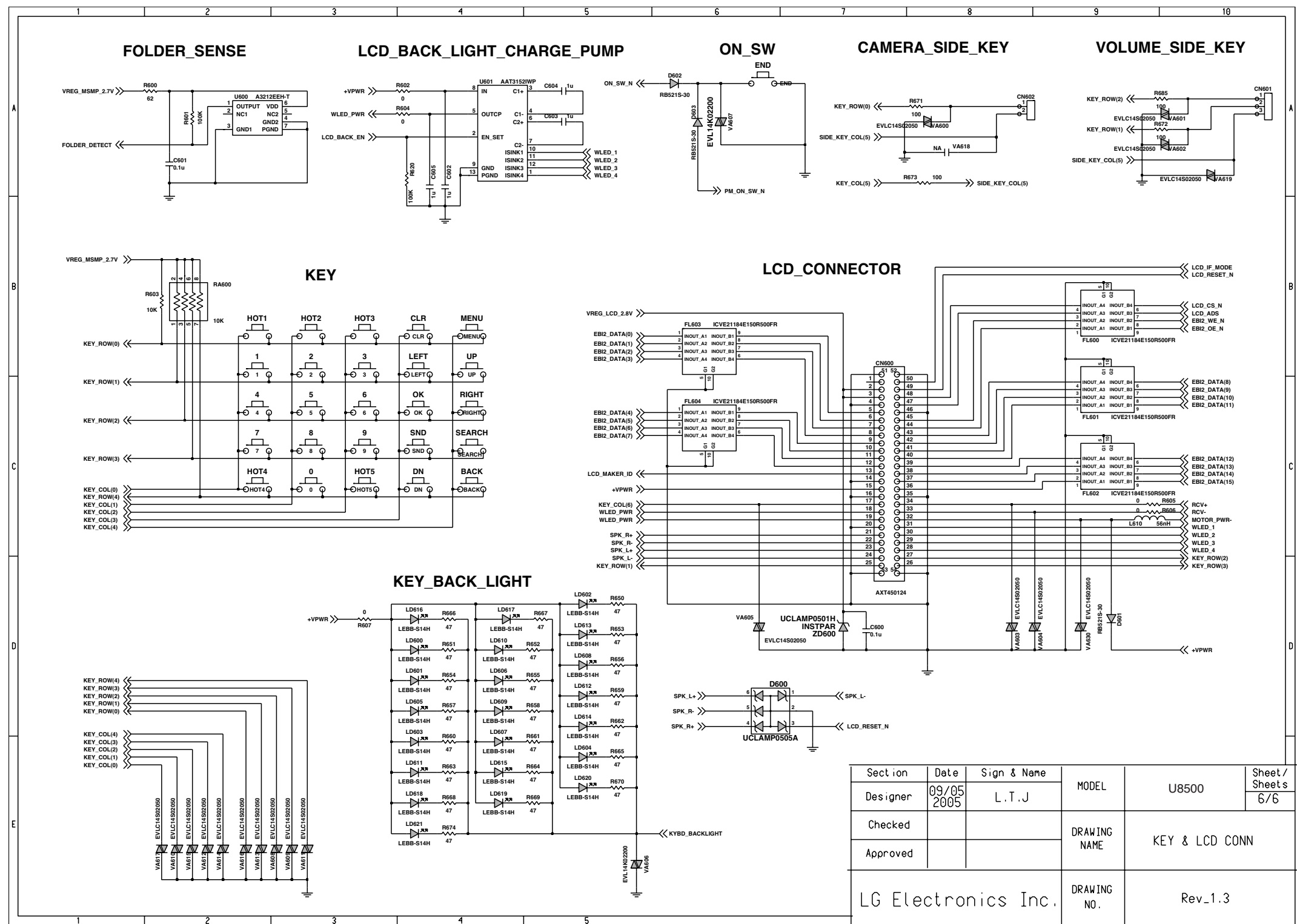


Section	Date	Sign & Name	MODEL	U8500	Sheet / Sheets
Designer	09/05/2005	L.T.J			
Checked			DRAWING NAME	PMIC	
Approved					
LG Electronics Inc.			DRAWING NO.	Rev_1.3	

7. CIRCUIT DIAGRAM



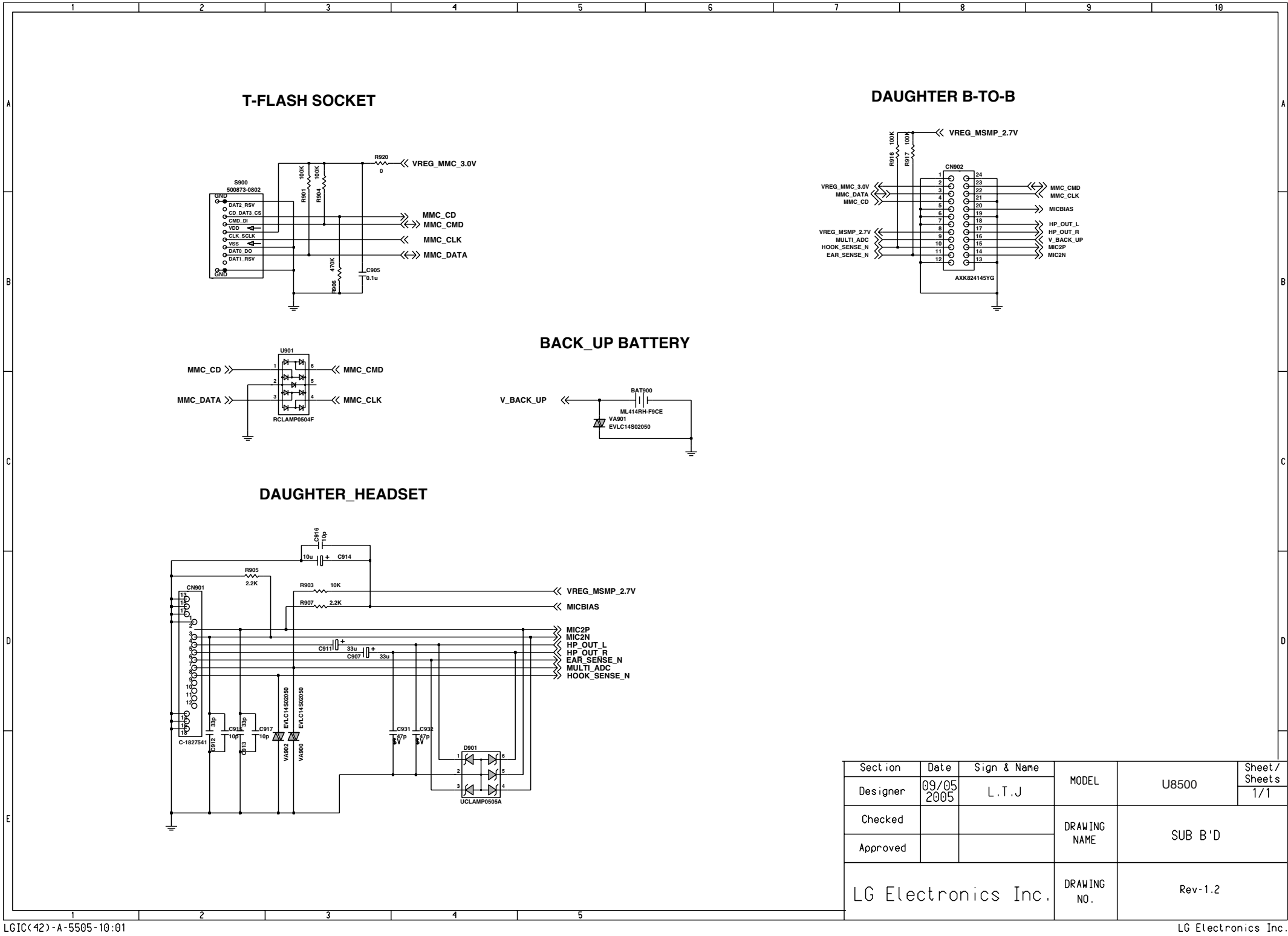
7. CIRCUIT DIAGRAM



LGIC(42)-A-5505-10:01

LG Electronics Inc.

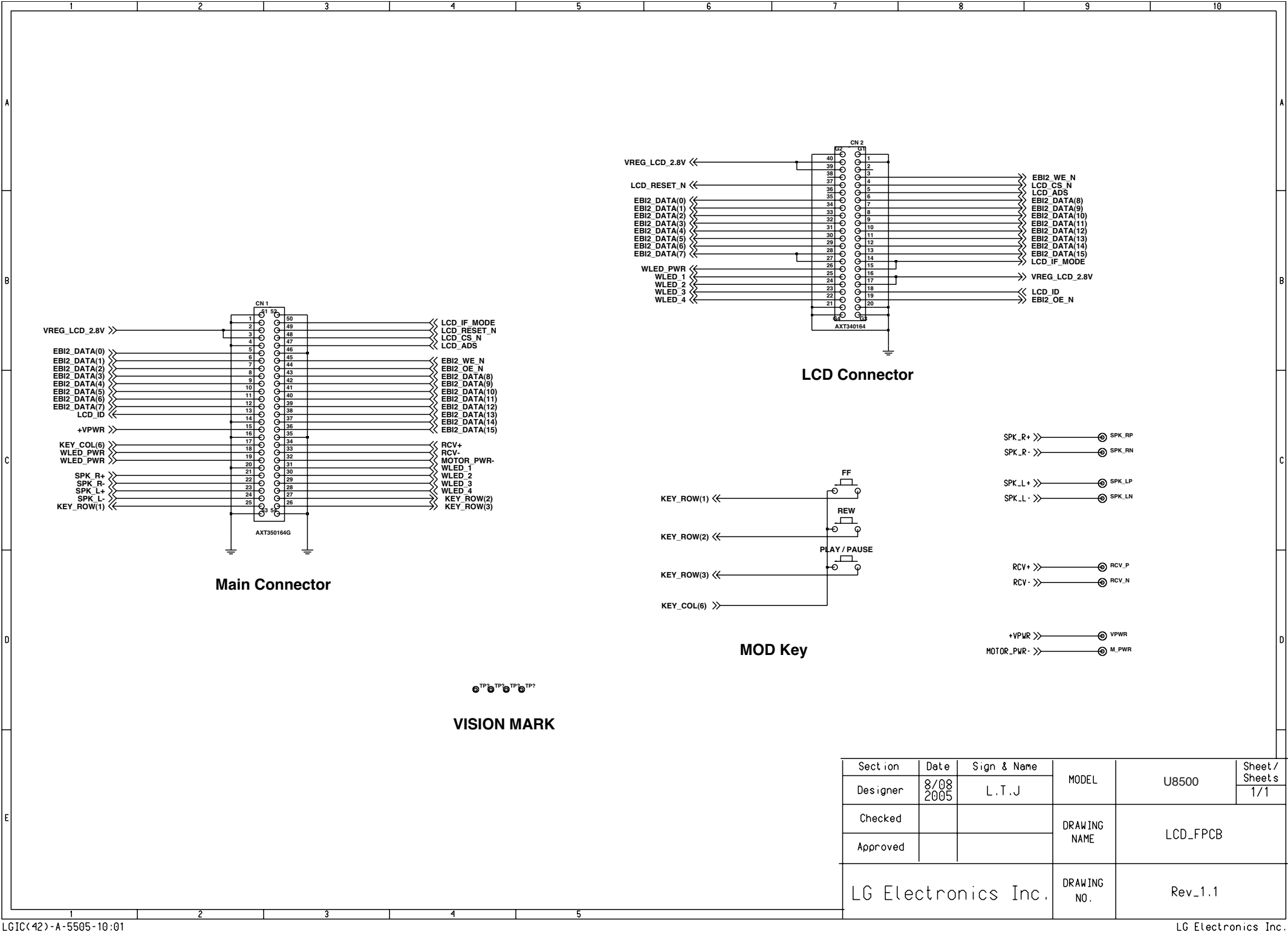
7. CIRCUIT DIAGRAM



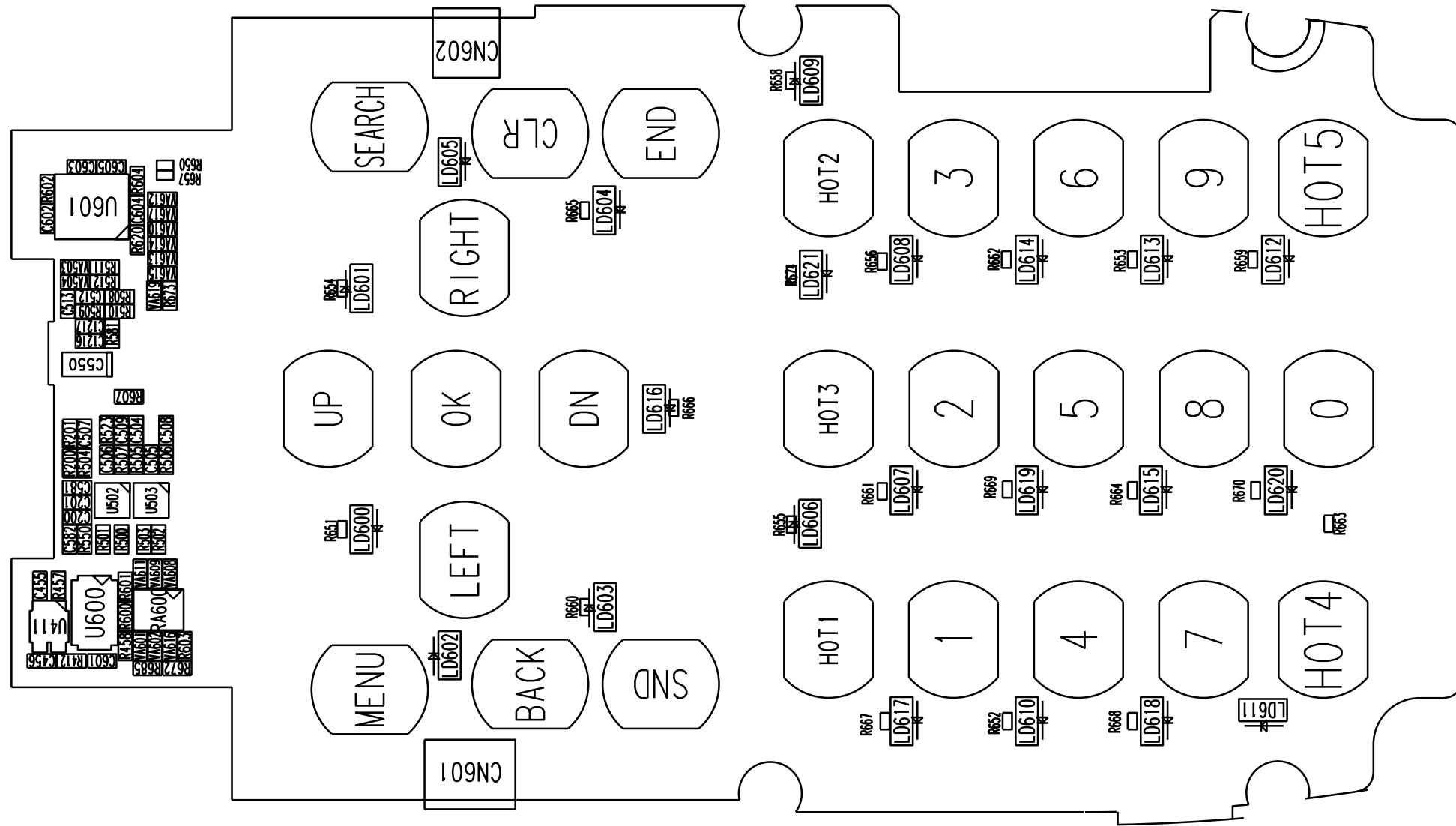
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LG Electronics Inc.

7. CIRCUIT DIAGRAM

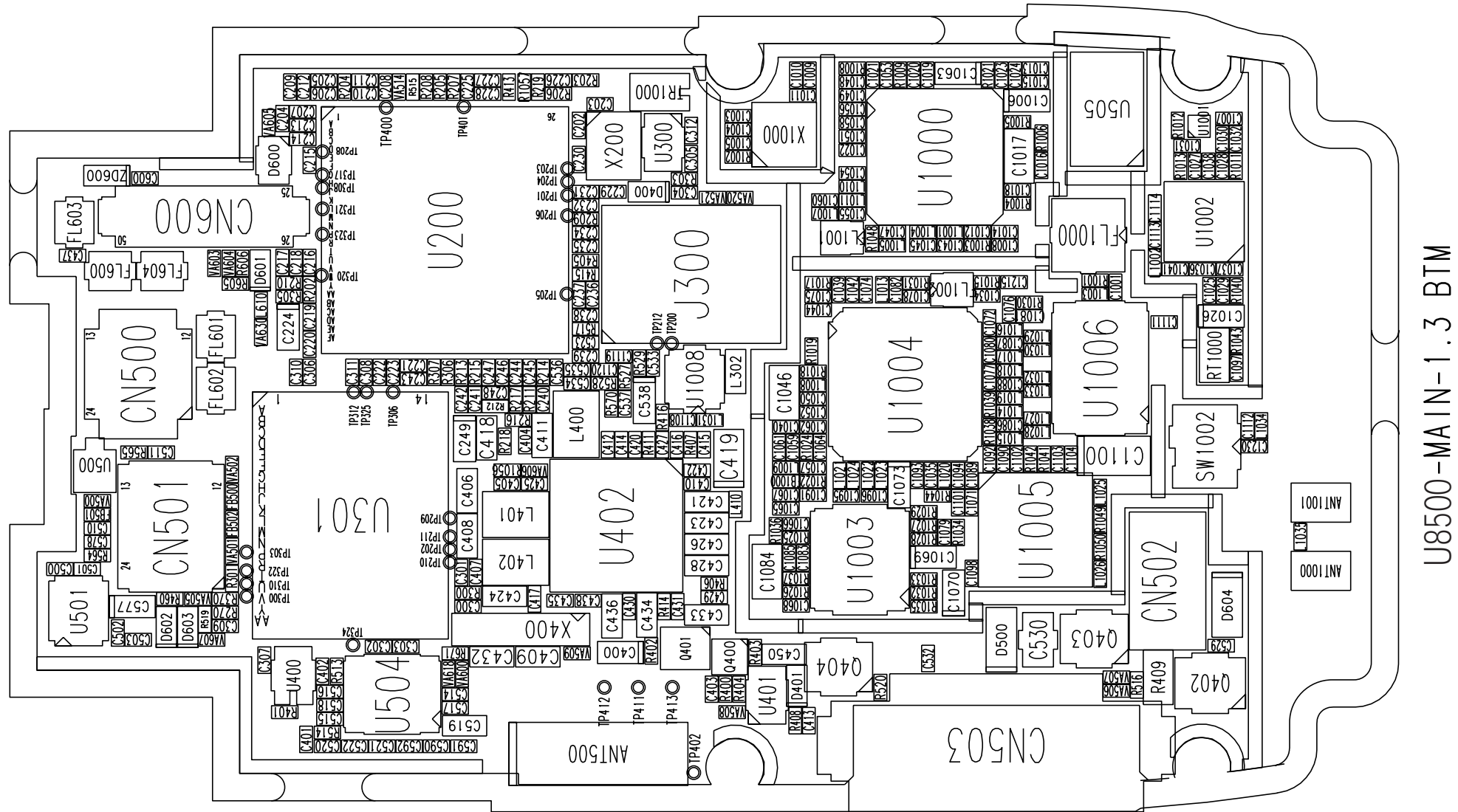


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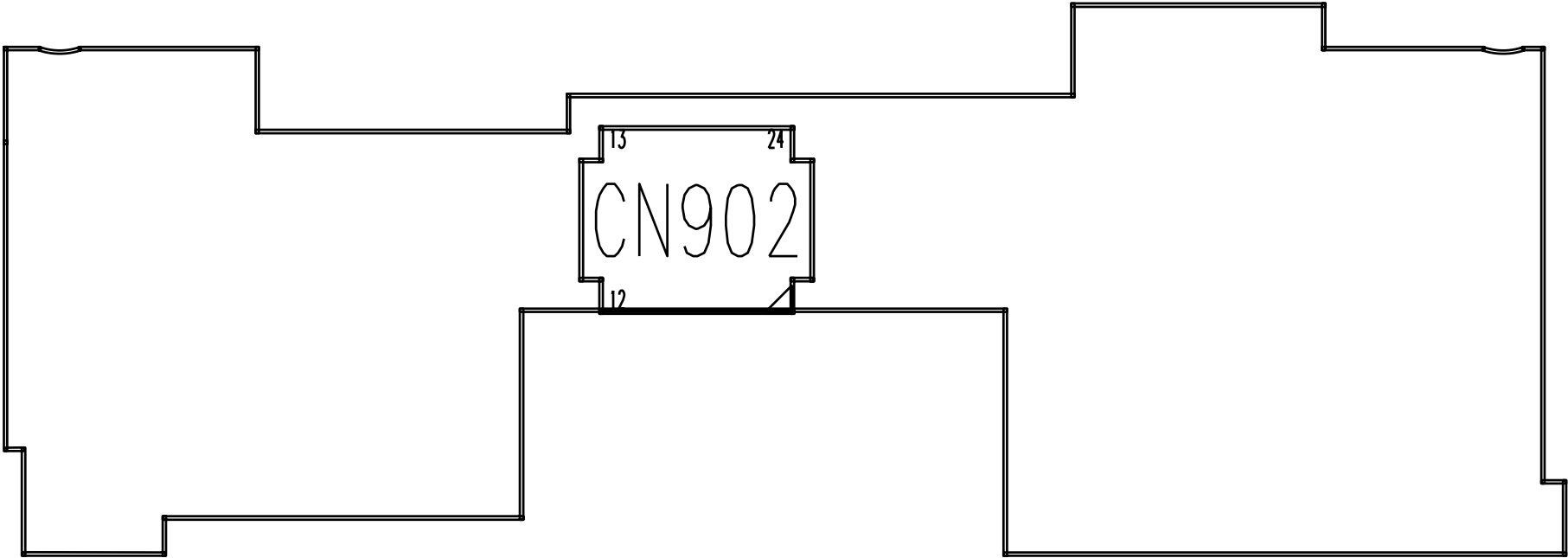


U8500-MAIN-1.3 TOP

8. PCB LAYOUT

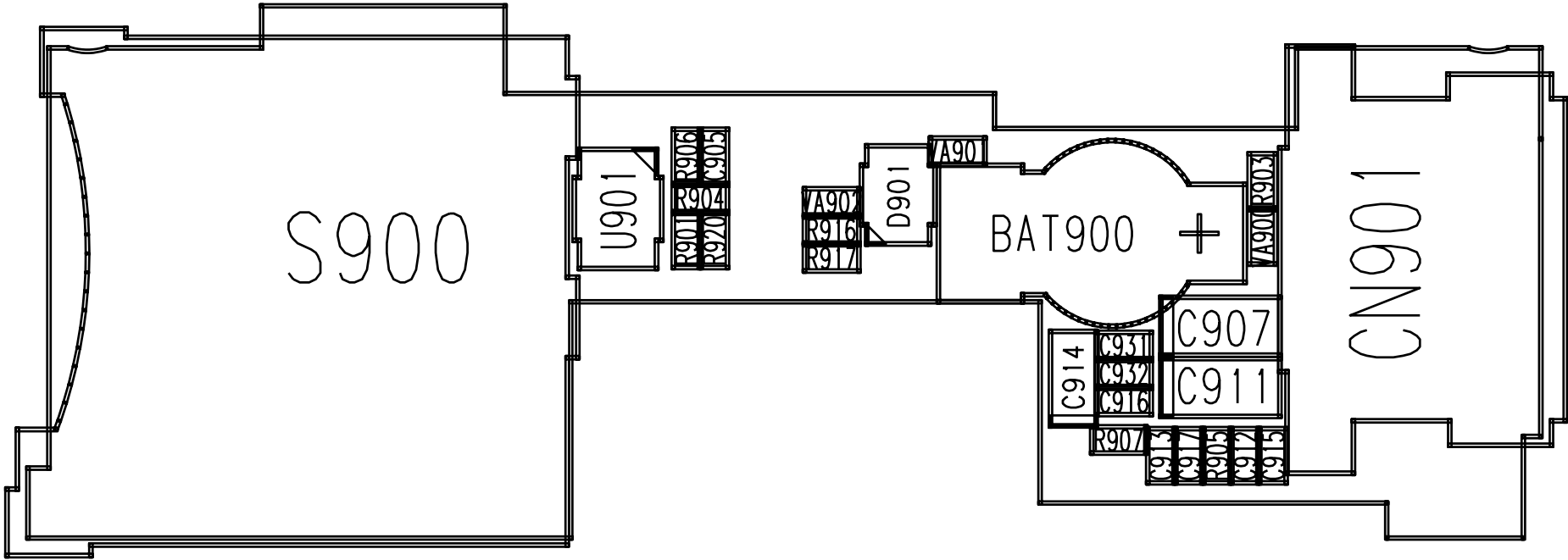


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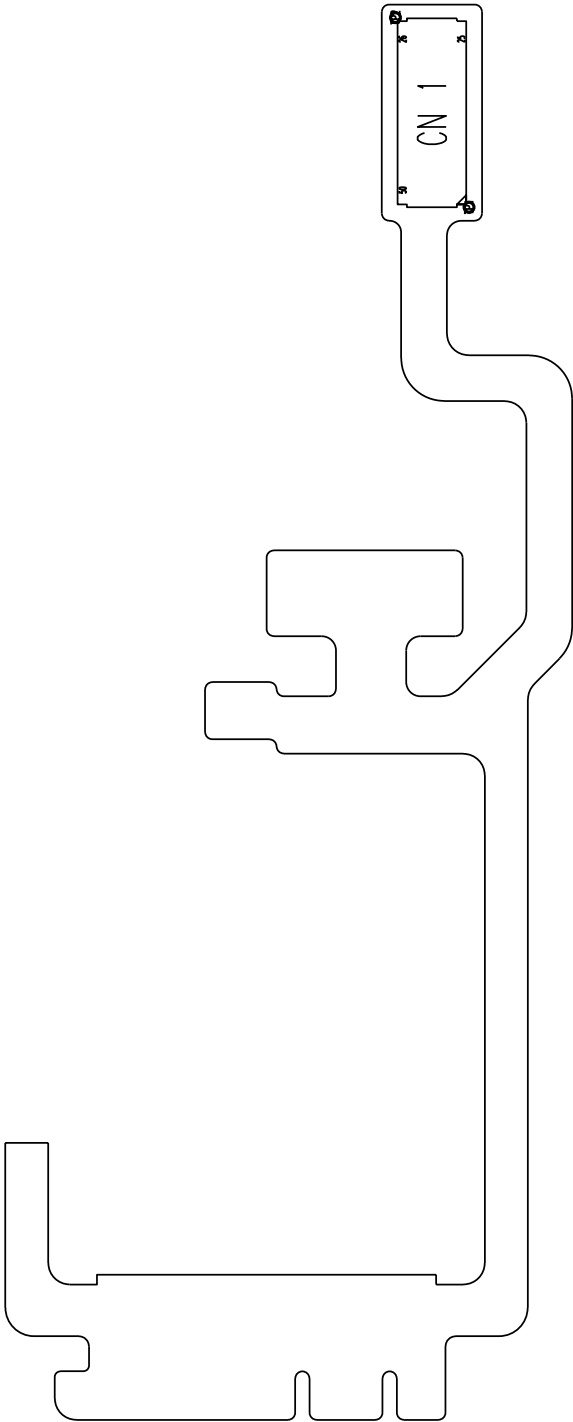
SUB
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8. PCB LAYOUT



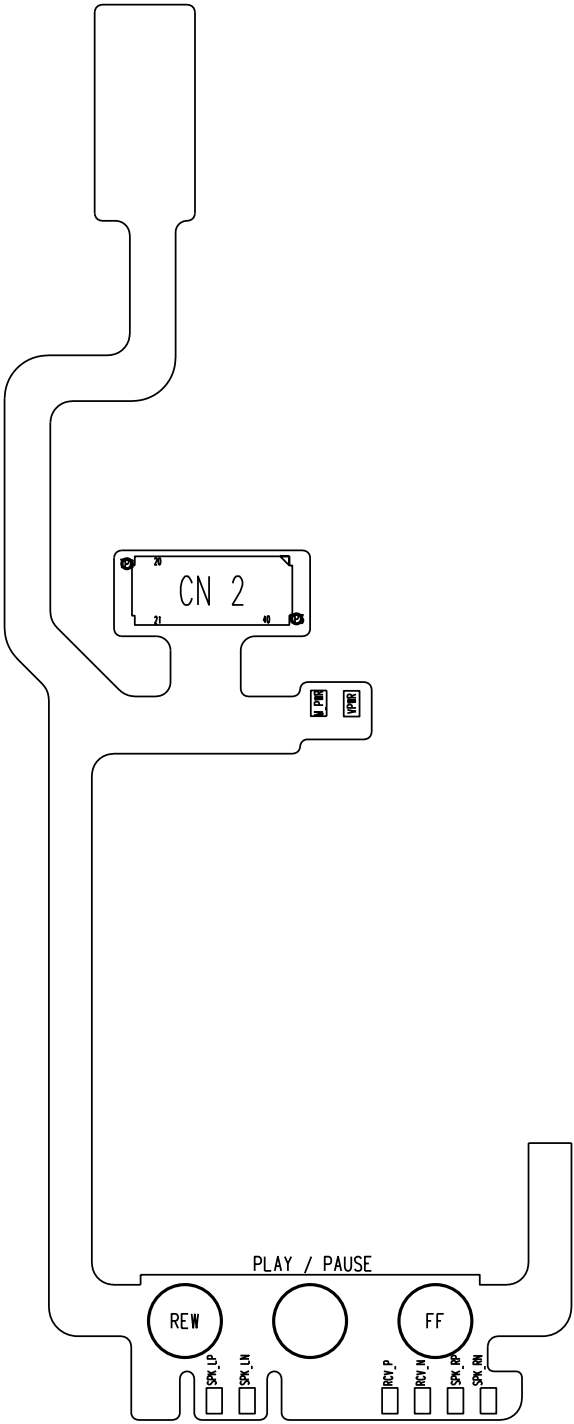
SUB
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8. PCB LAYOUT



U8500 SPCY0053901-1.1-BTM

8. PCB LAYOUT



U8500 SPCY0053901-1.1-T0P