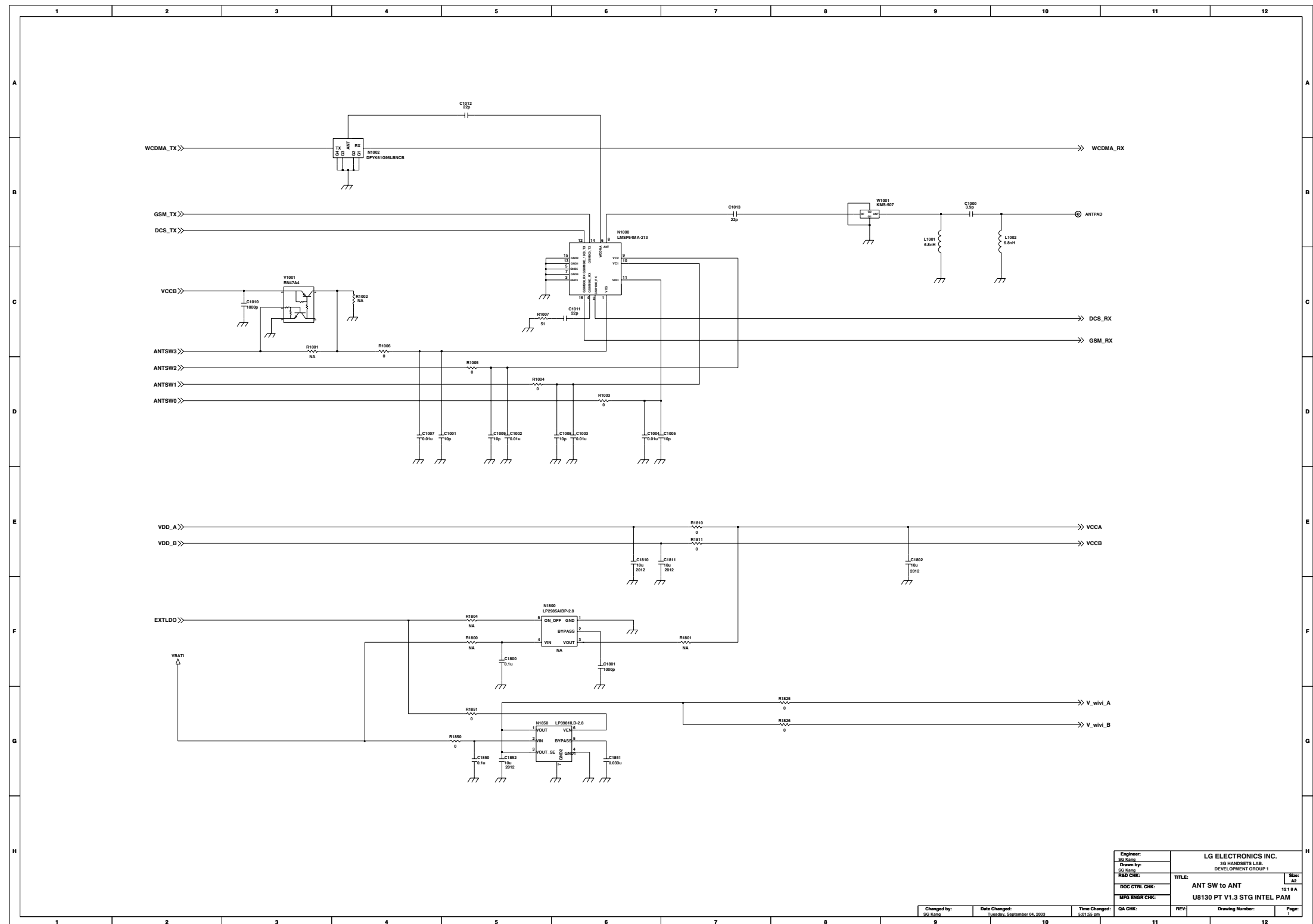
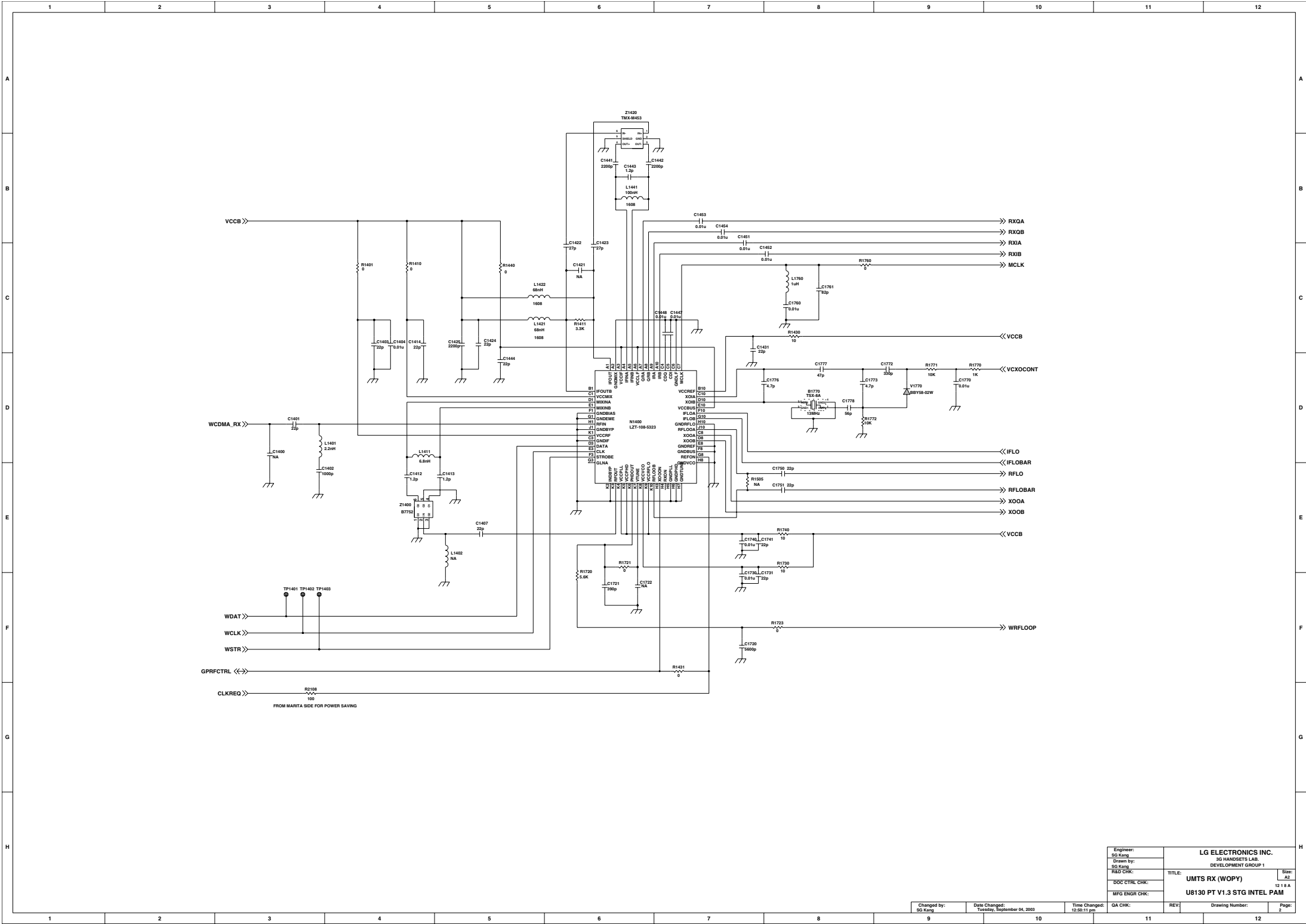


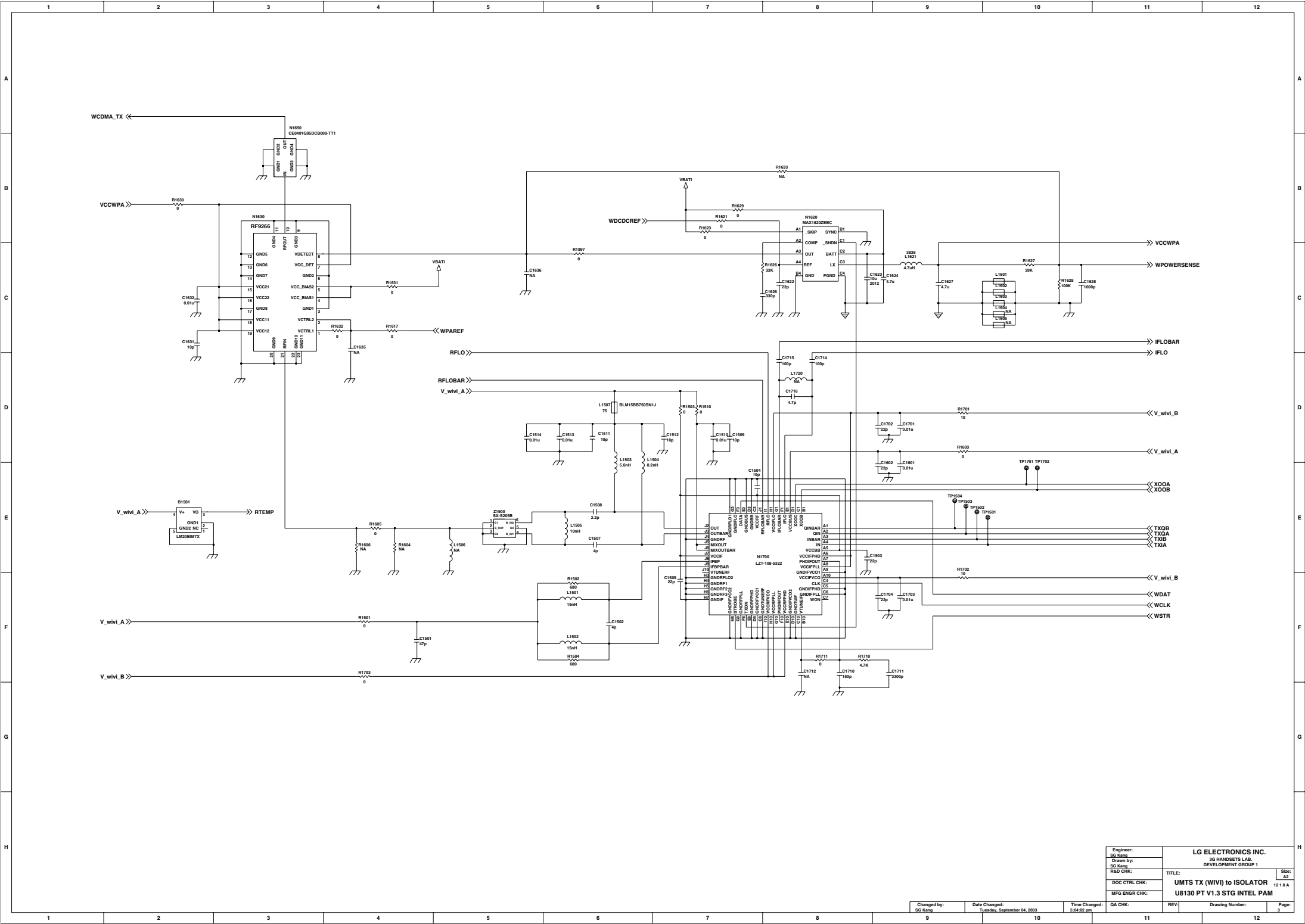
9. CIRCUIT DIAGRAM



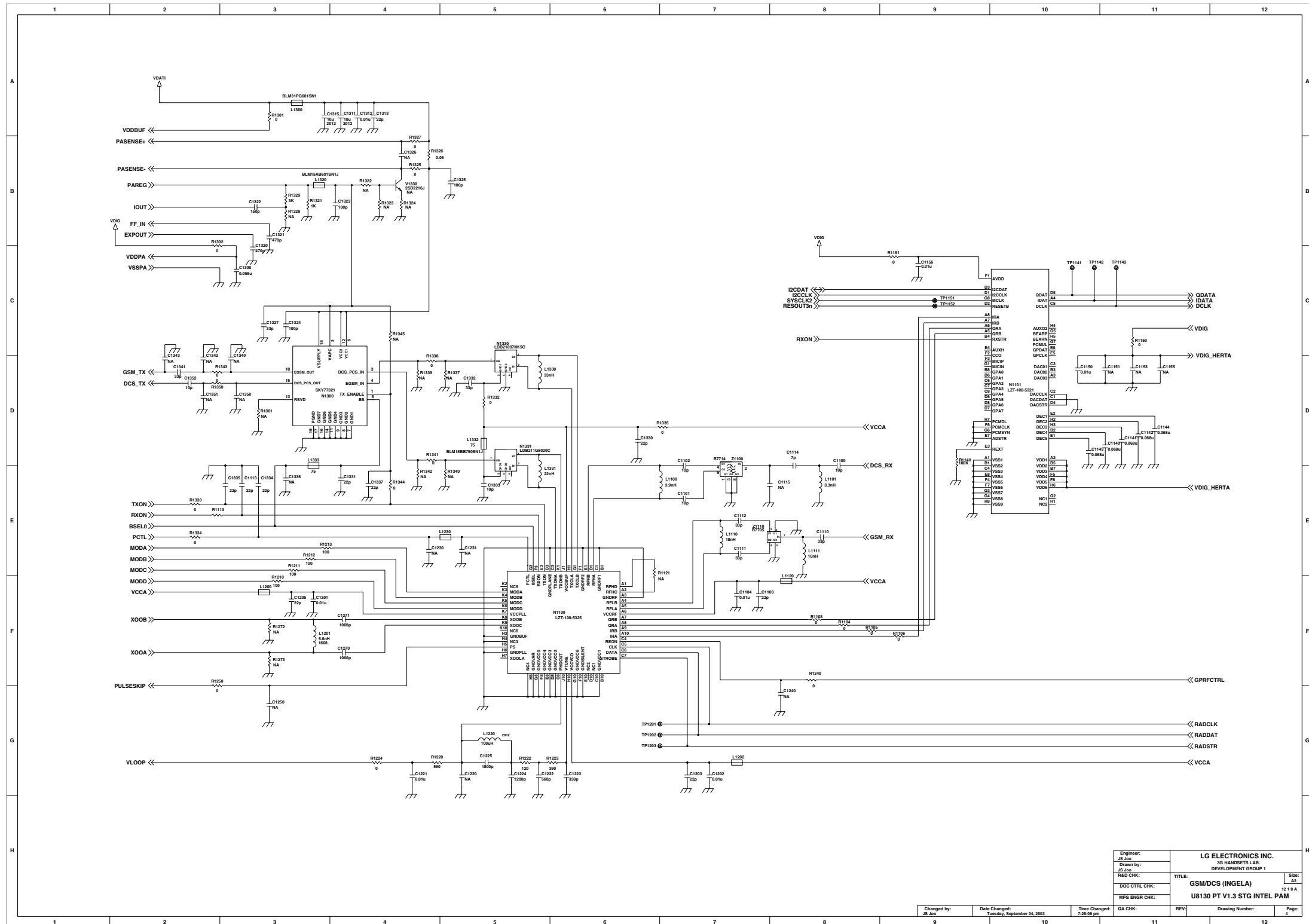
9. CIRCUIT DIAGRAM



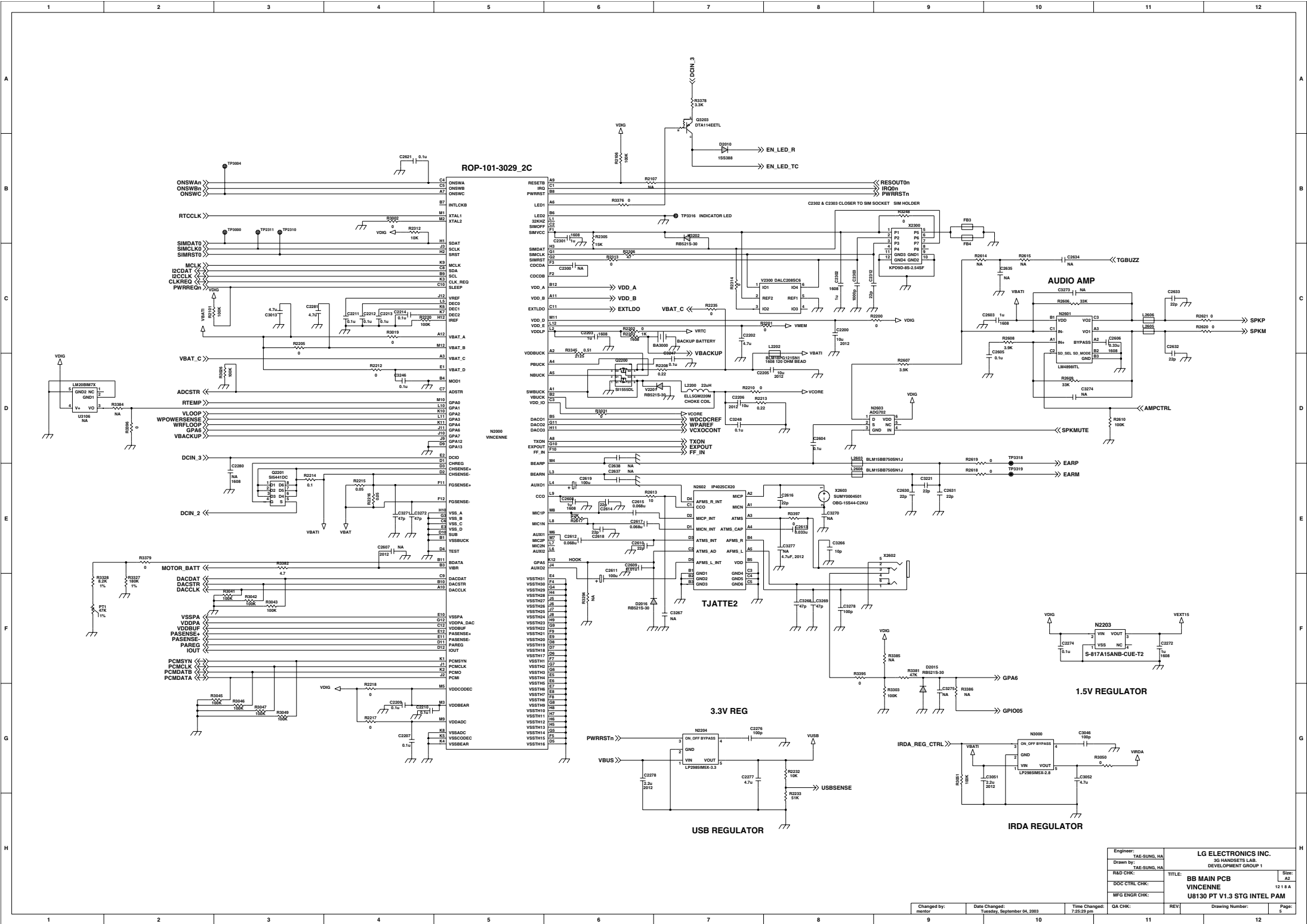
9. CIRCUIT DIAGRAM



9. CIRCUIT DIAGRAM

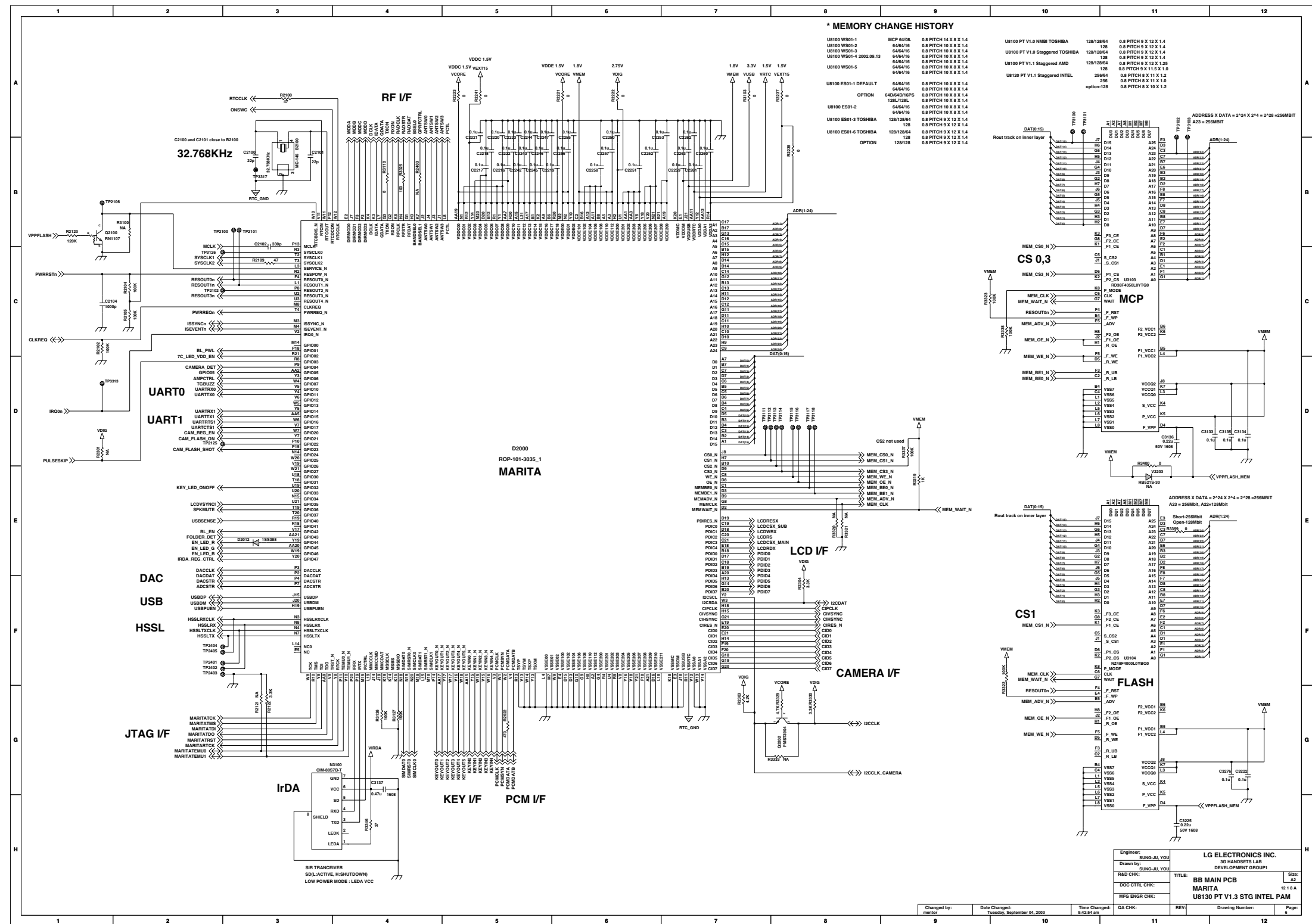


9. CIRCUIT DIAGRAM

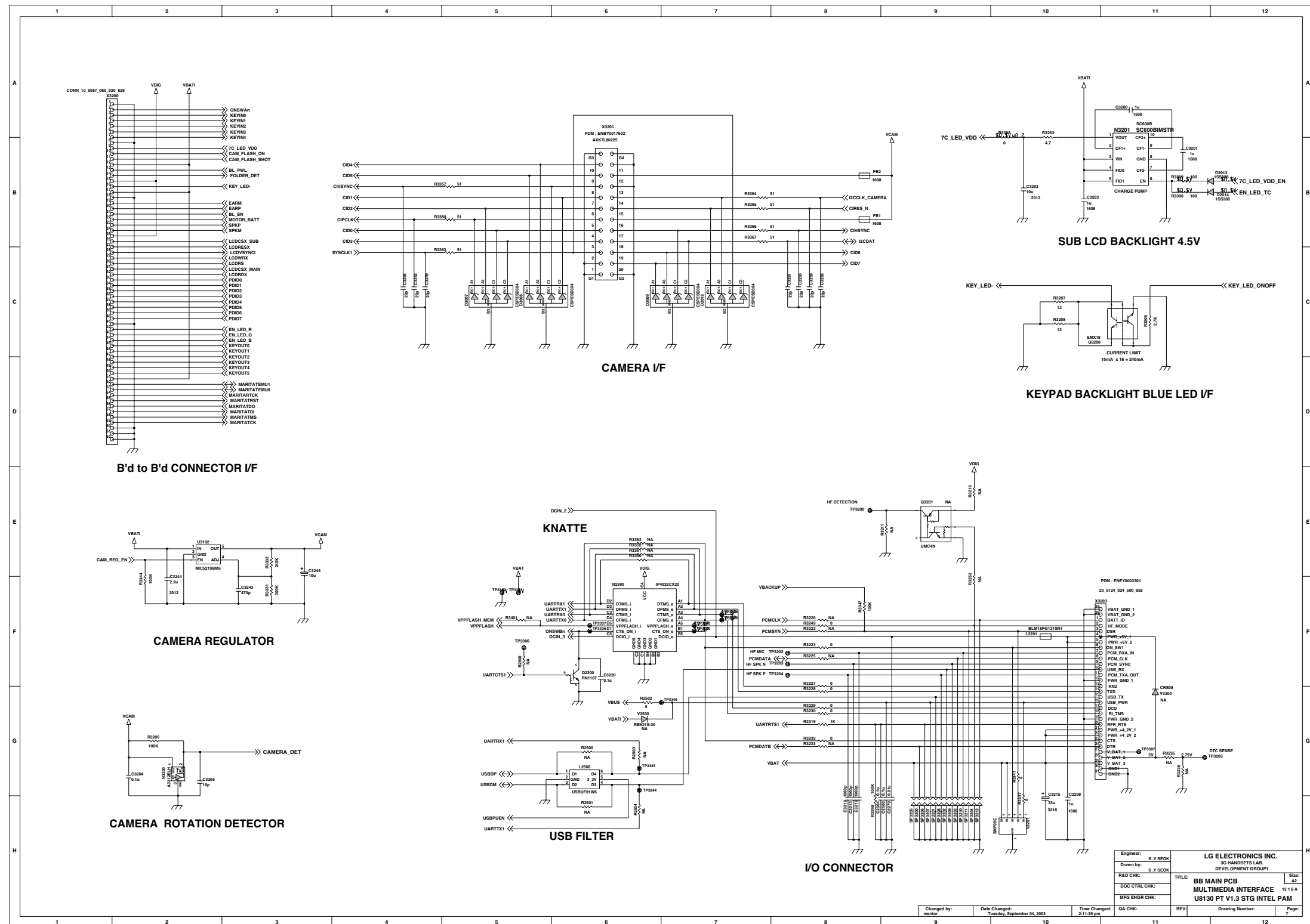


Engineer:	TAE-SUNG, HA	LG ELECTRONICS INC.	1218A
Drawn by:	TAE-SUNG, HA	DEVELOPMENT GROUP 1	A2
DOC CTRL CHK:		TITLE: BB MAIN PCB	
MPG ENGR CHK:		VINCENNE	
		U8130 PT V1.3 STG INTEL PAM	
Changed by:	Date Changed:	Time Changed:	GA CHK:
mentor	Tuesday, September 04, 2003	7:25:29 pm	REV:
			Drawing Number:
			Page:
			5

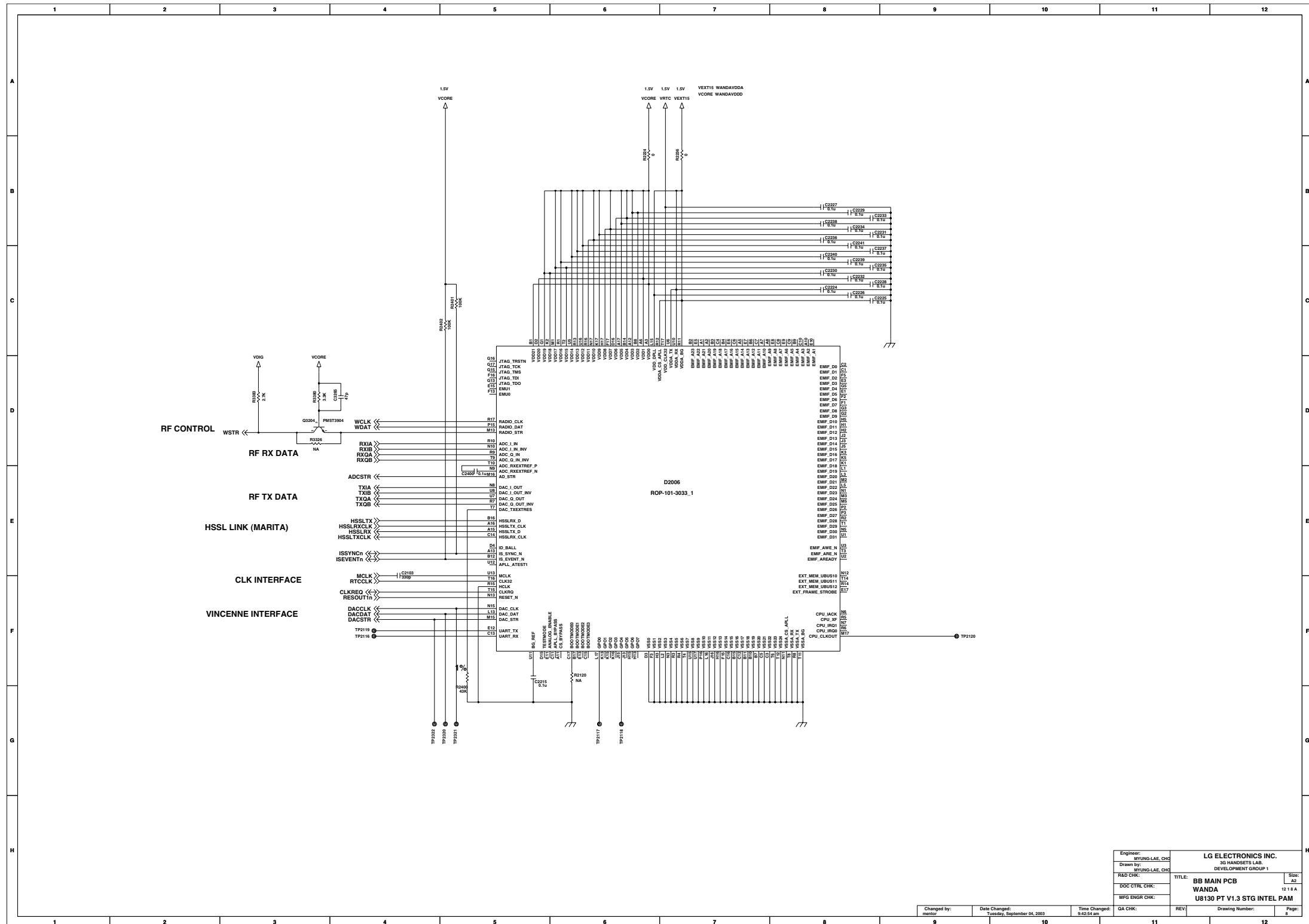
9. CIRCUIT DIAGRAM



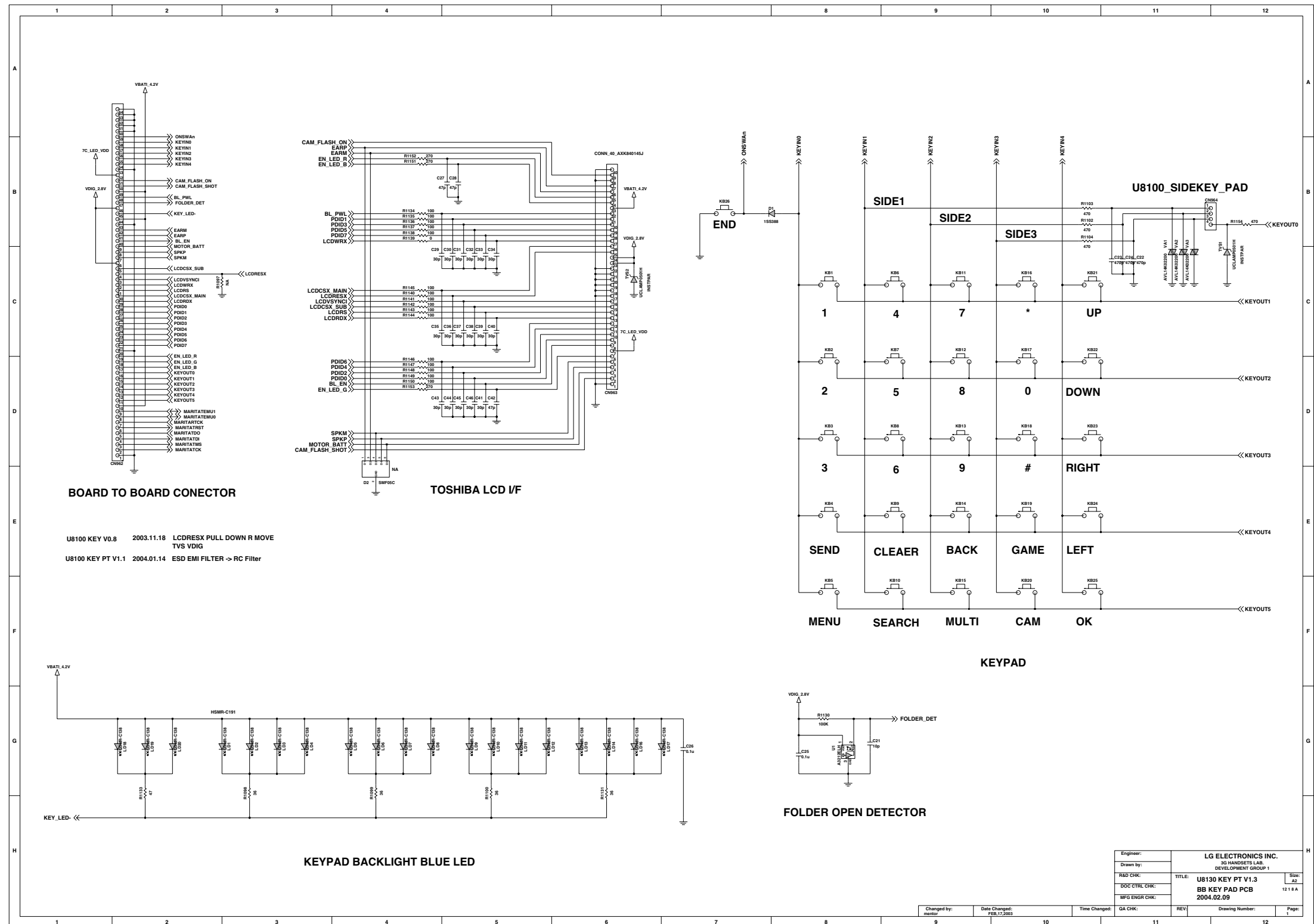
9. CIRCUIT DIAGRAM



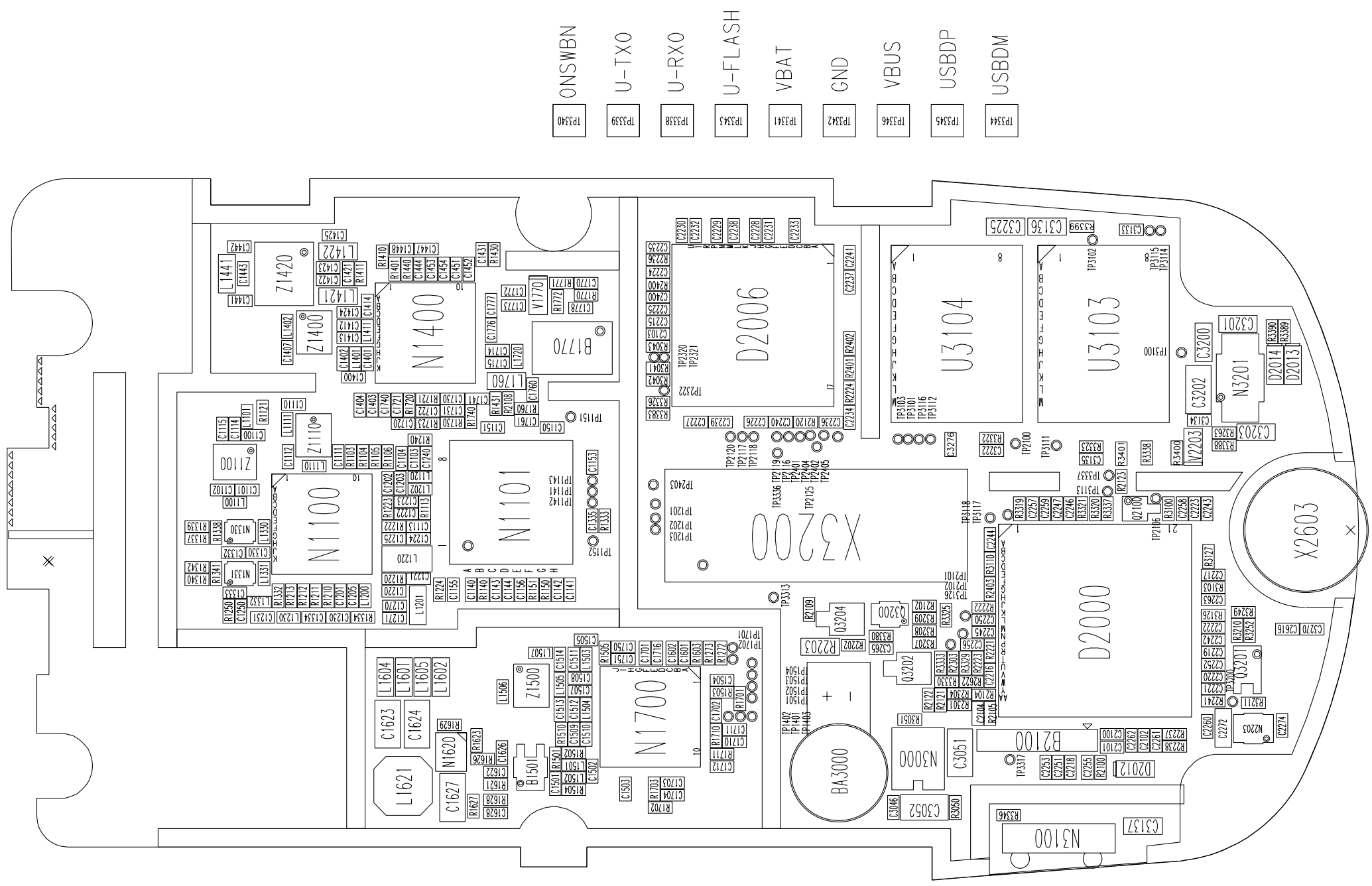
9. CIRCUIT DIAGRAM



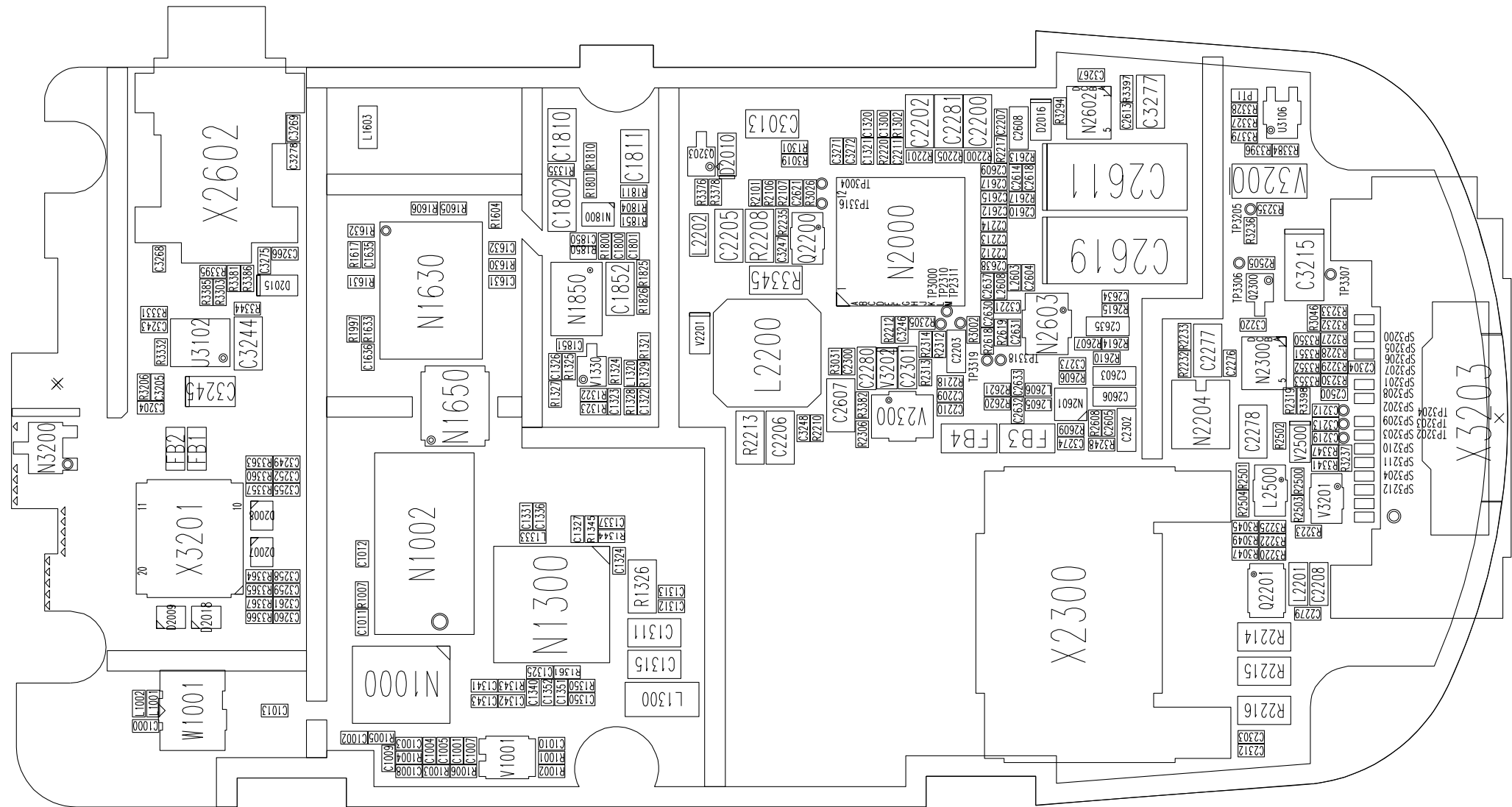
9. CIRCUIT DIAGRAM



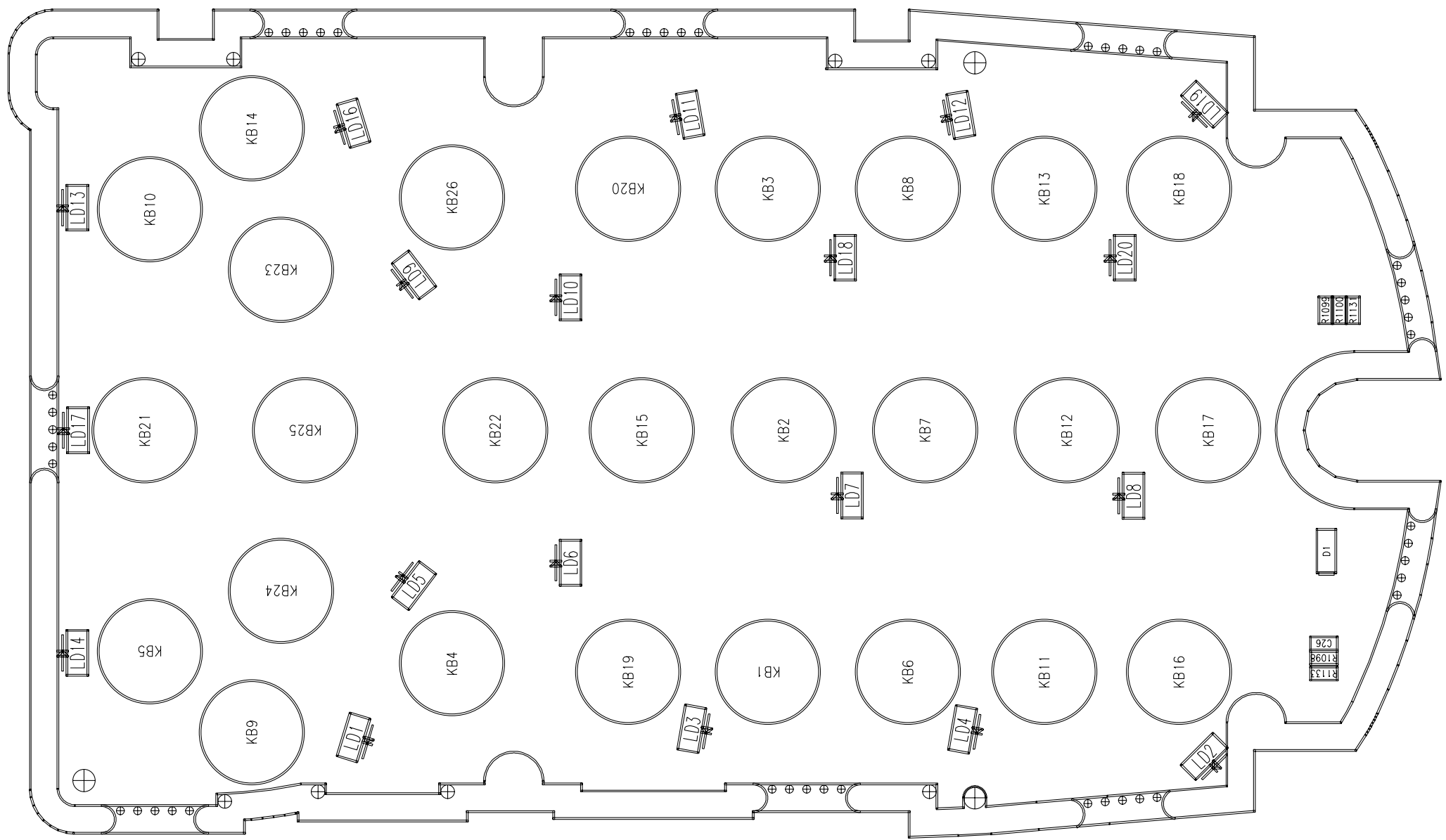
10. PCB LAYOUT



10. PCB LAYOUT



10. PCB LAYOUT



10. PCB LAYOUT

