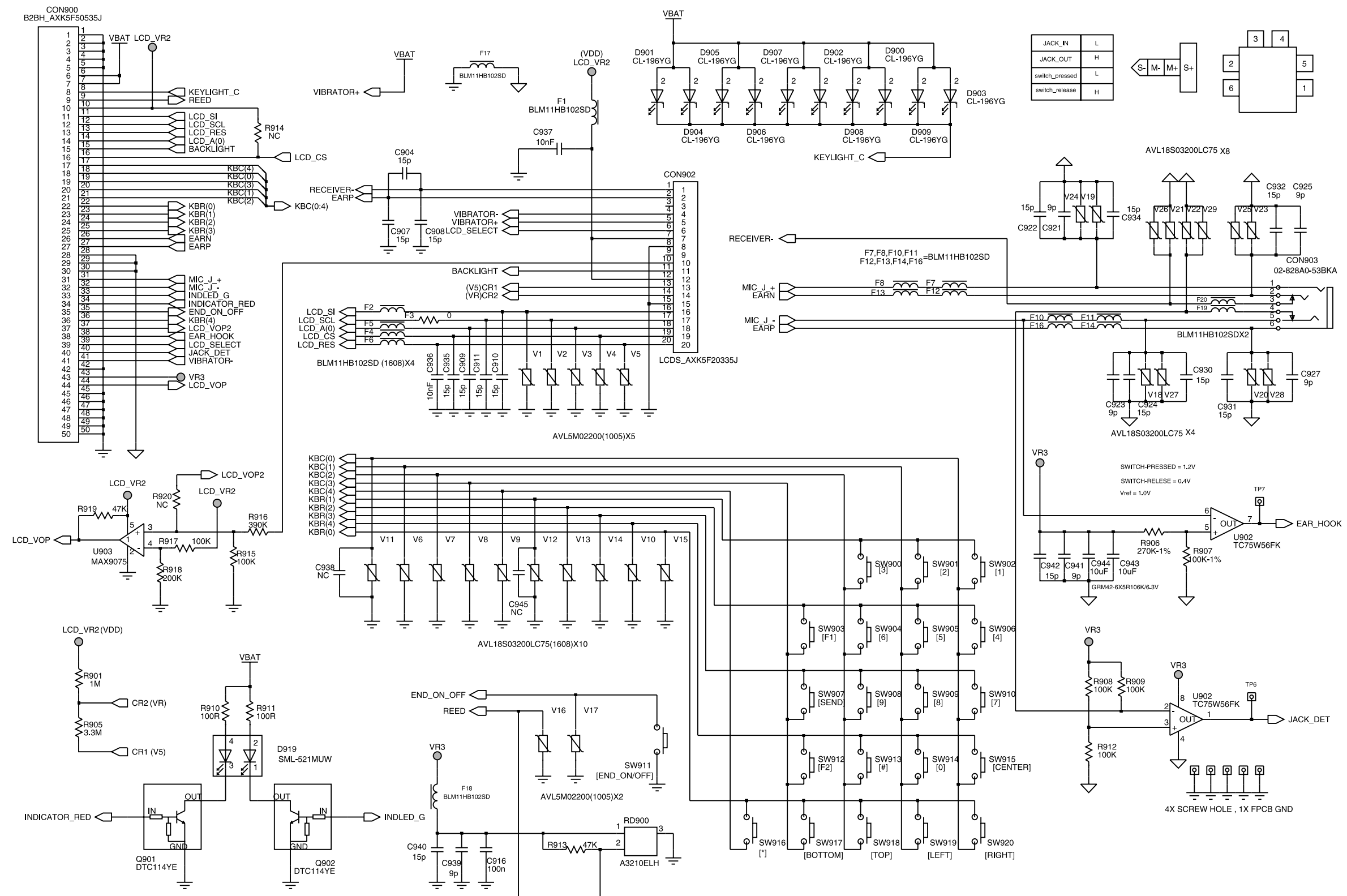


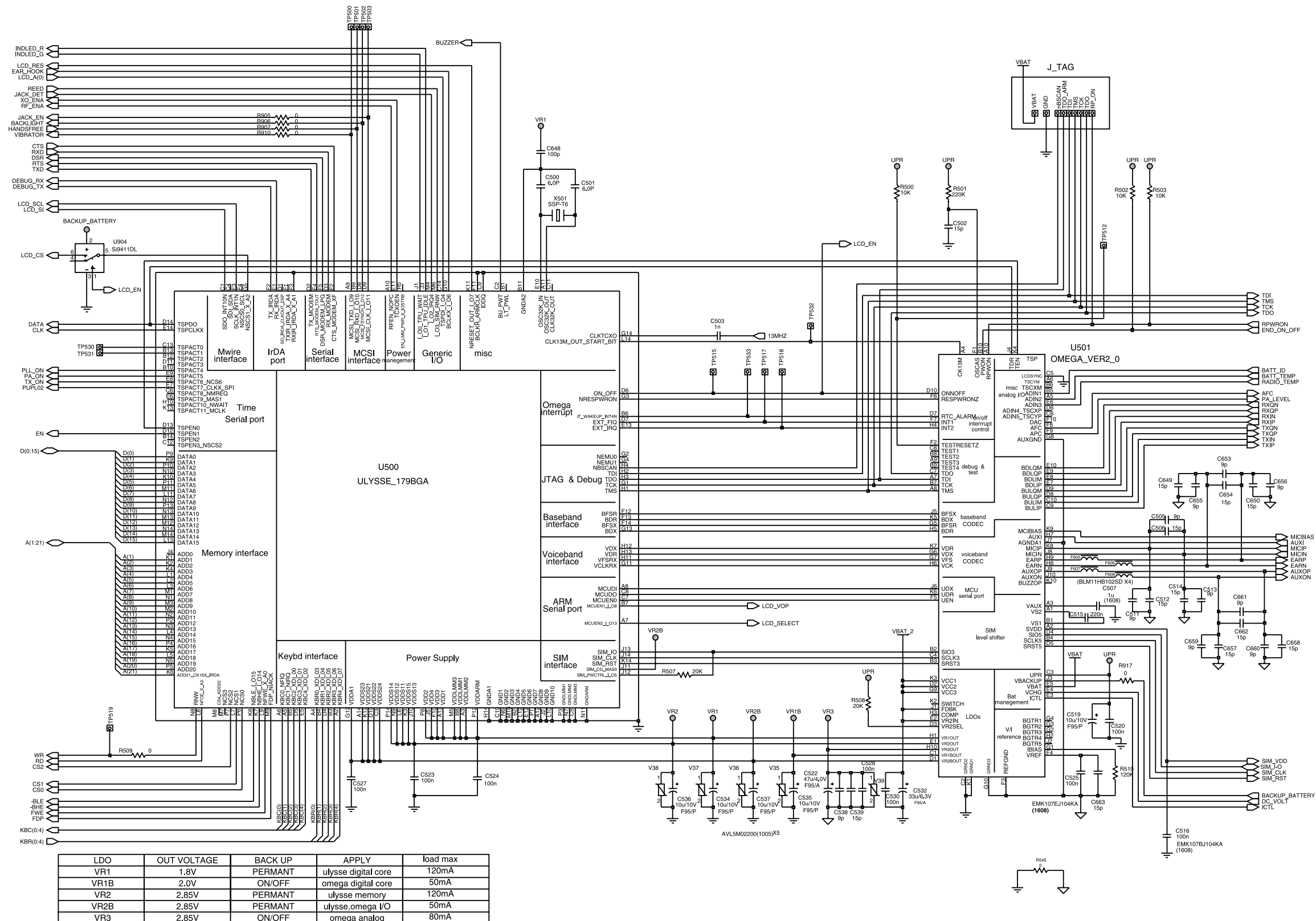
8. Schematic Diagram

8.1 Key Pad (LCD Interface & Earphone)

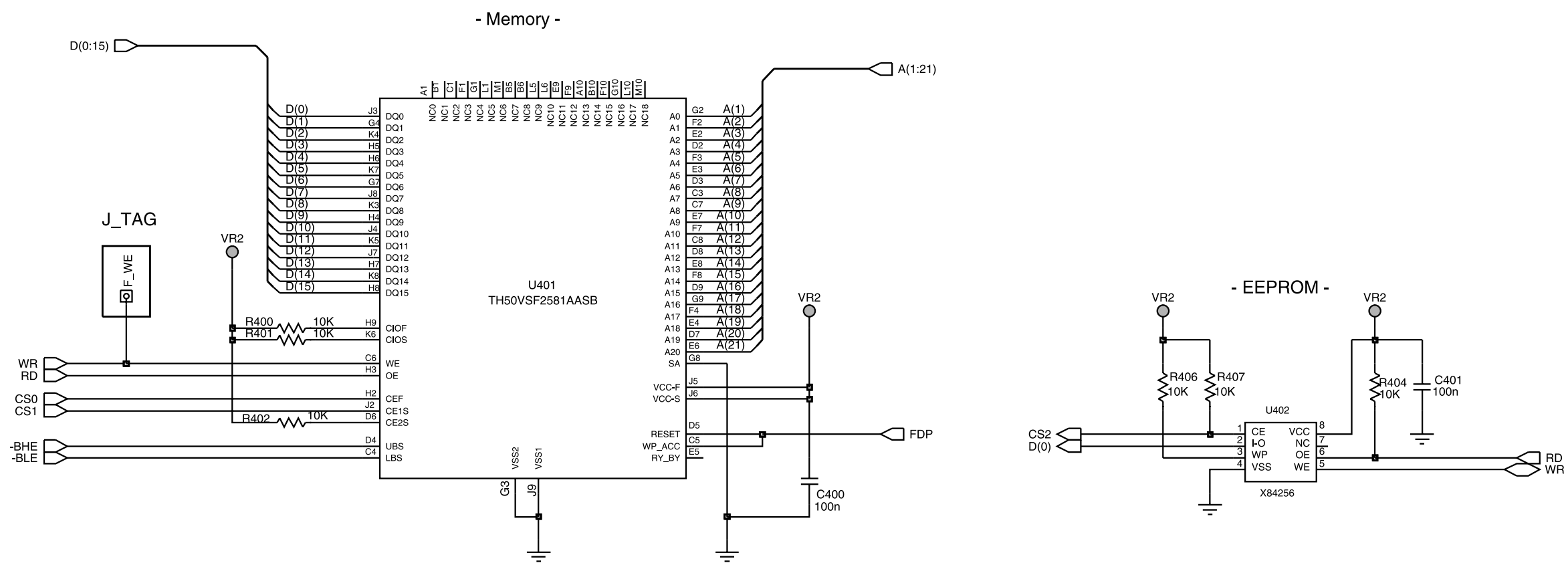


8.2 Main Board

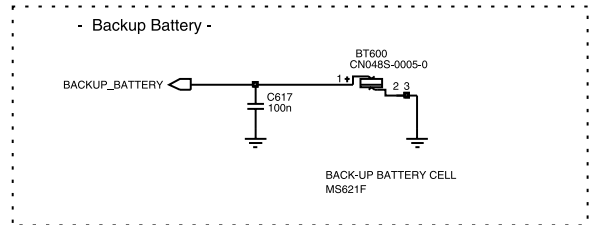
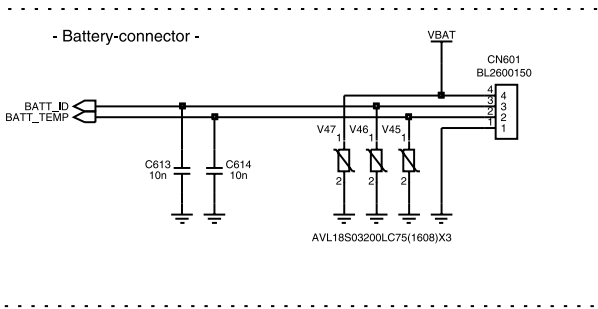
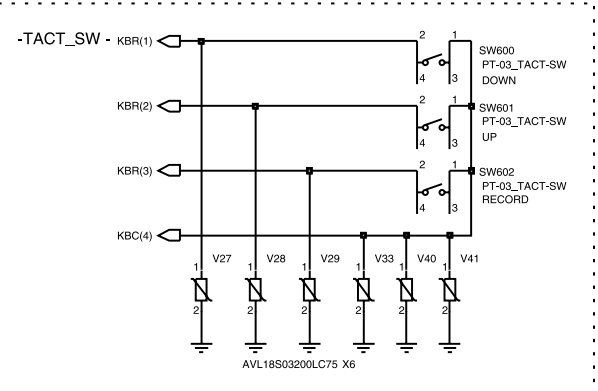
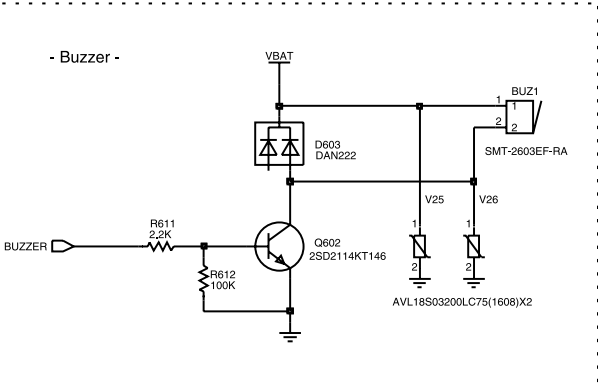
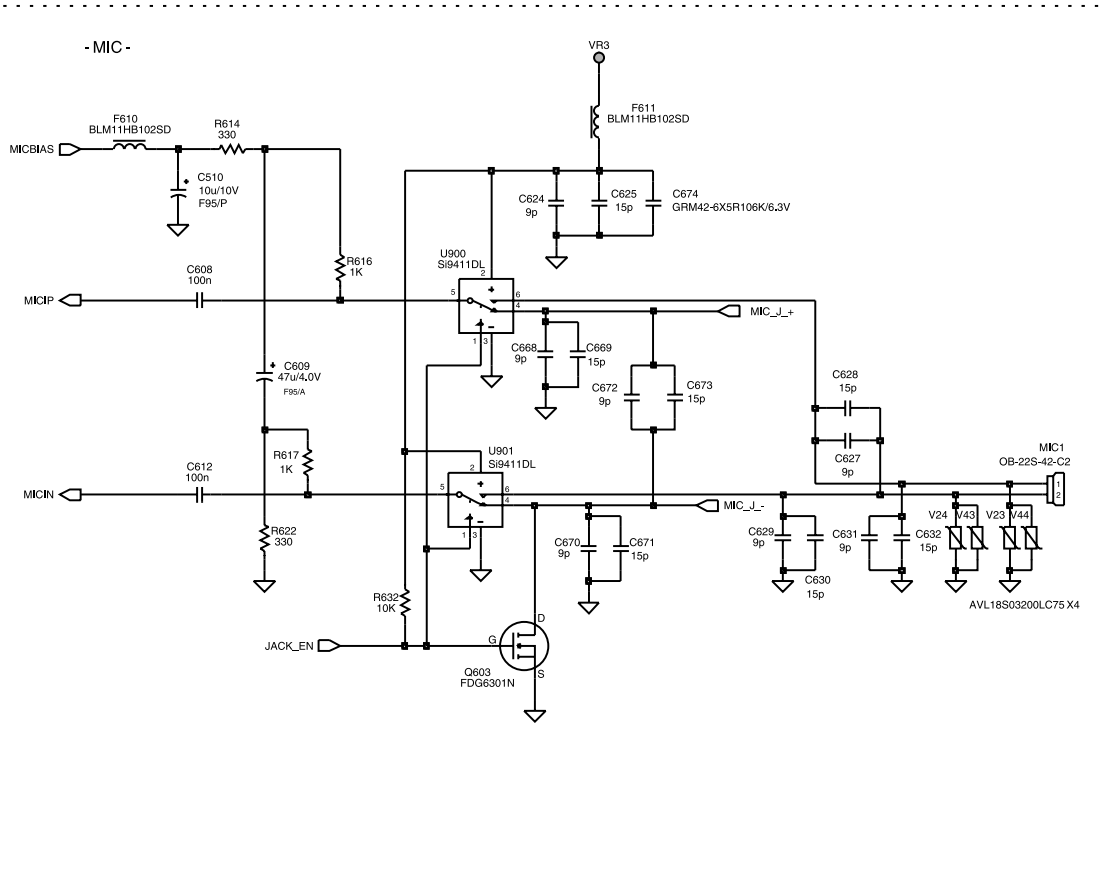
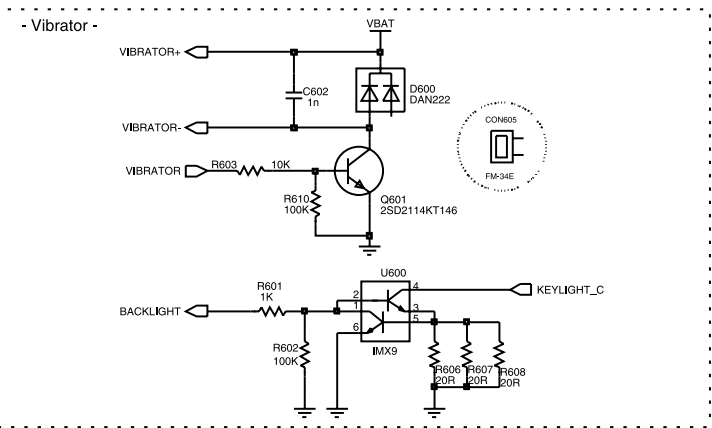
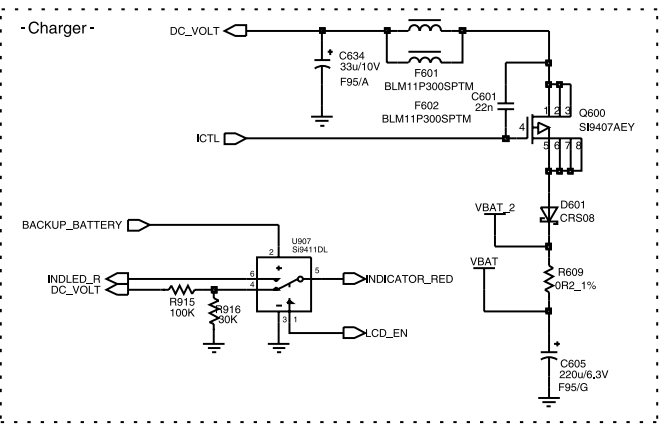
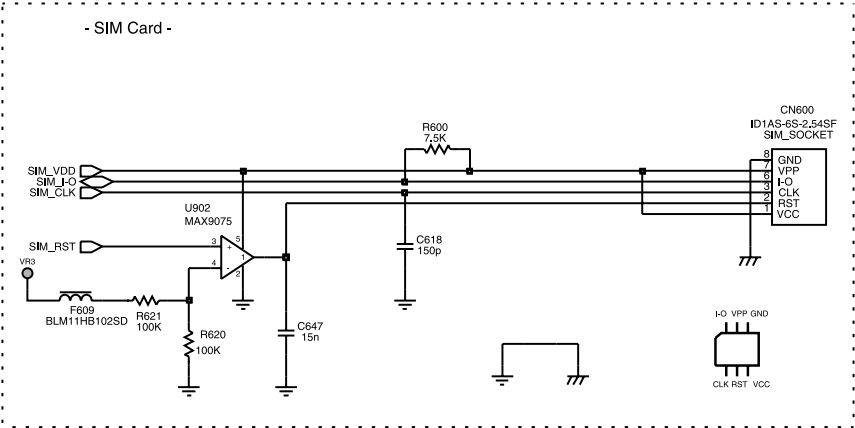
8.2.1 Baseband(Ulysse & Omega)



8.2.2 Memory Devices



8.2.3 Audio & Peripheral Circuit



SI9411DL LOGIC TABLE

LOGIC	NC(#4)	NO(#6)
0	ON	OFF
1	OFF	ON

FOLDER STATUS

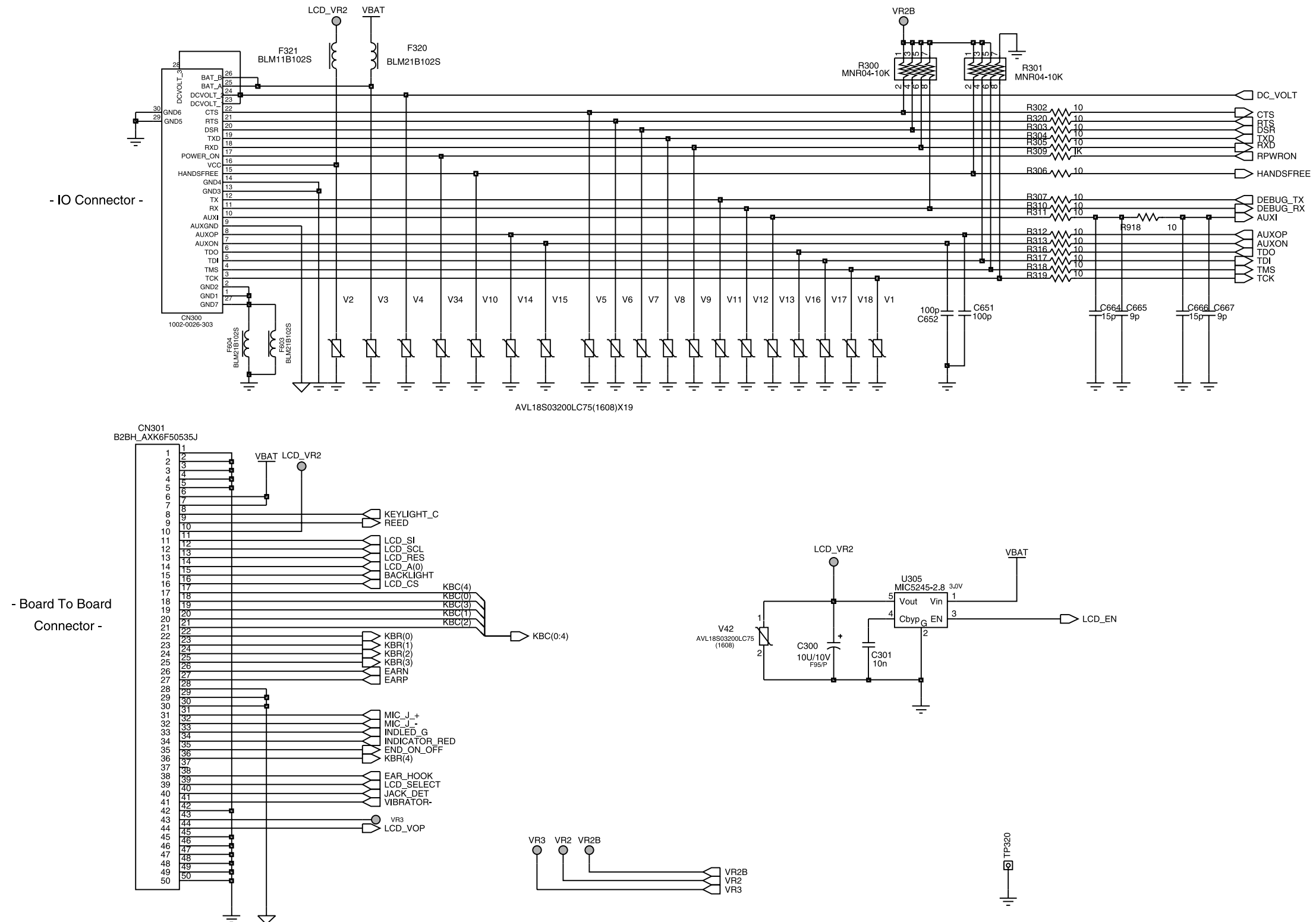
	OPEN	CLOSED
REED OUT	H	L

CURRENT SOURCE

	BAT-ID	TEMP
SET CURRENT	10uA	40uA

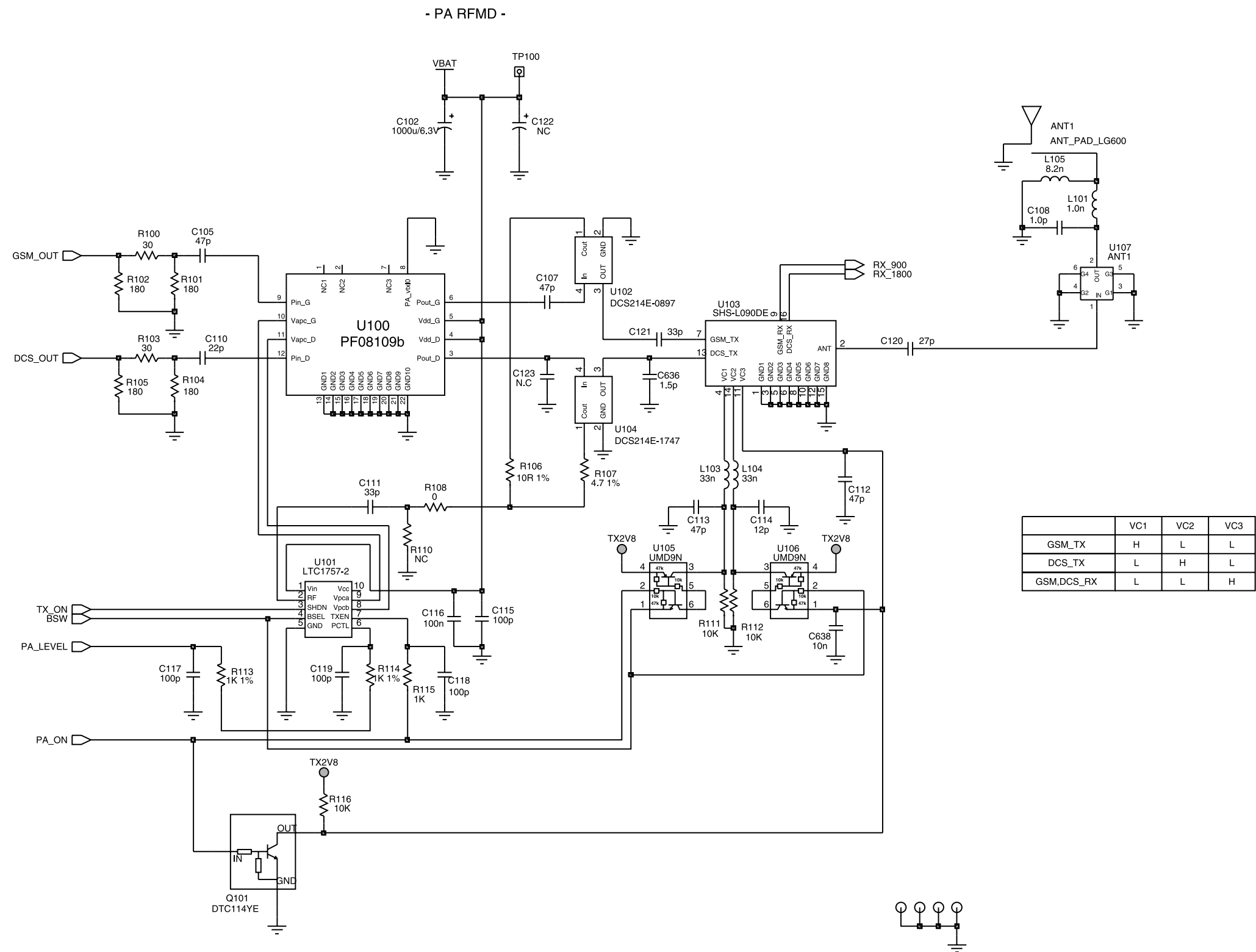
	standard	extended
BAT_ID_Res	3,3K	10K
Thermister	10K_1%	10K_1%

8.2.4 I/O & B' to B Connector Interface



8.3 RF Top Schematic

8.3.1 RF Front End



8.3.2 RF Main

