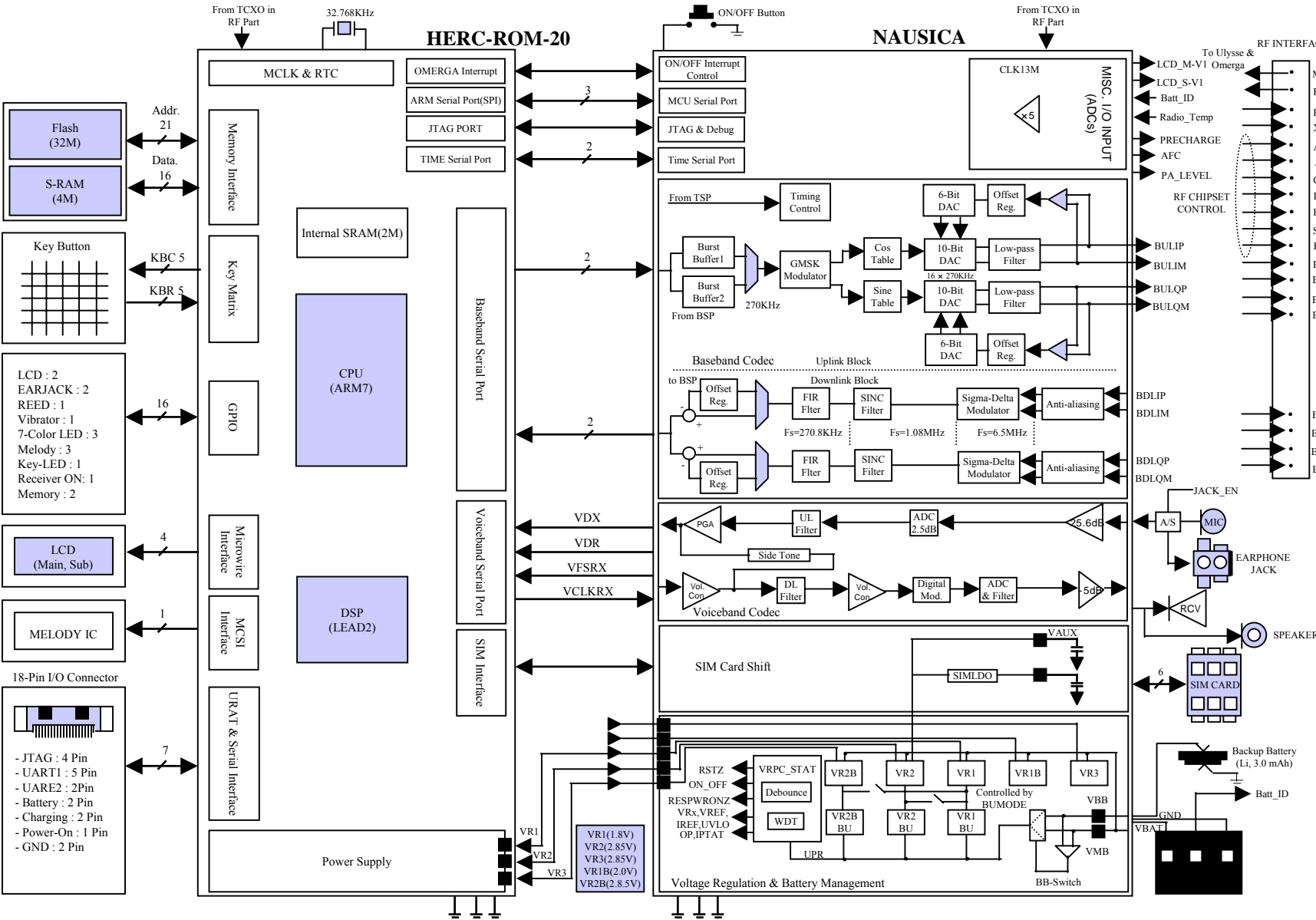
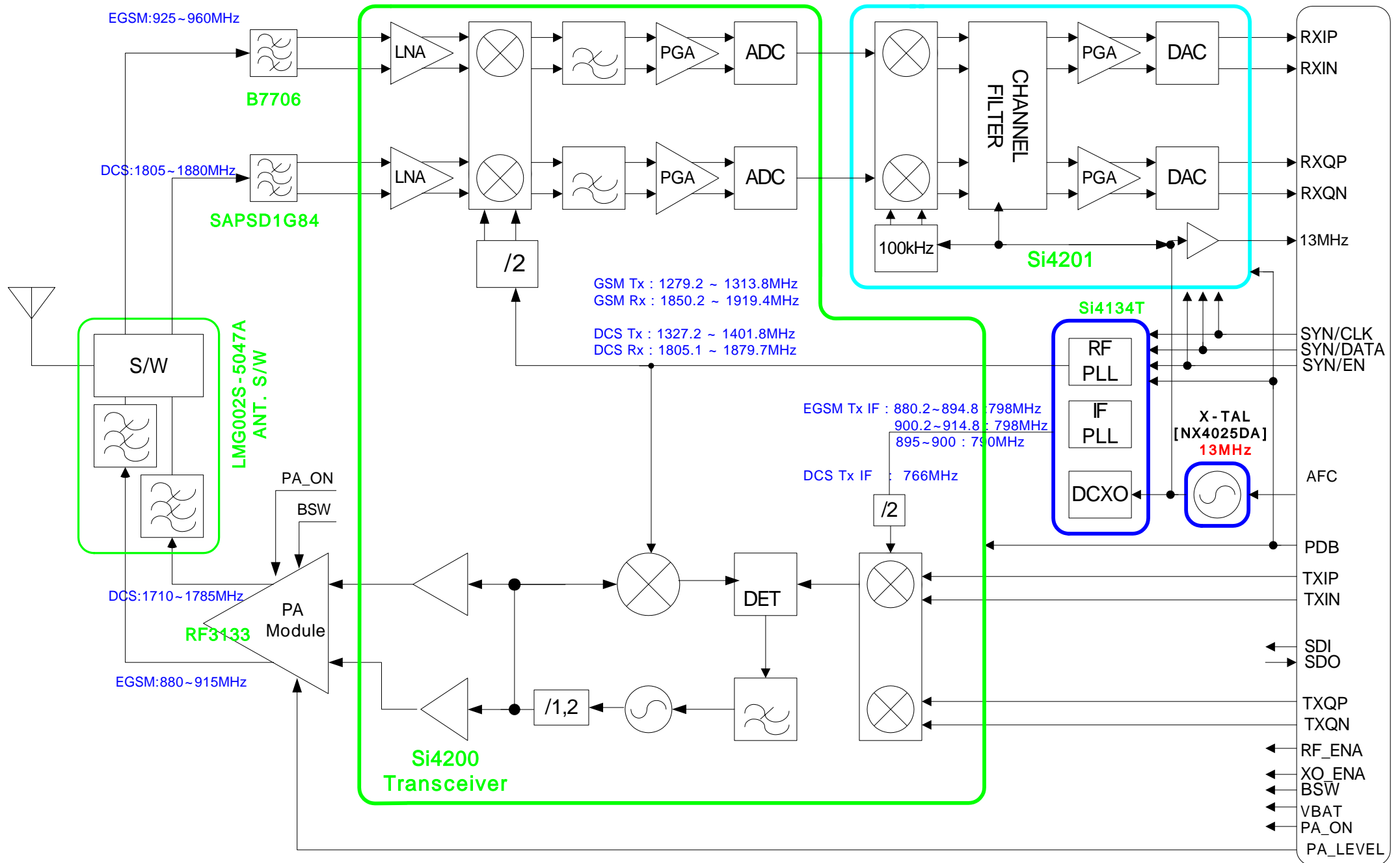


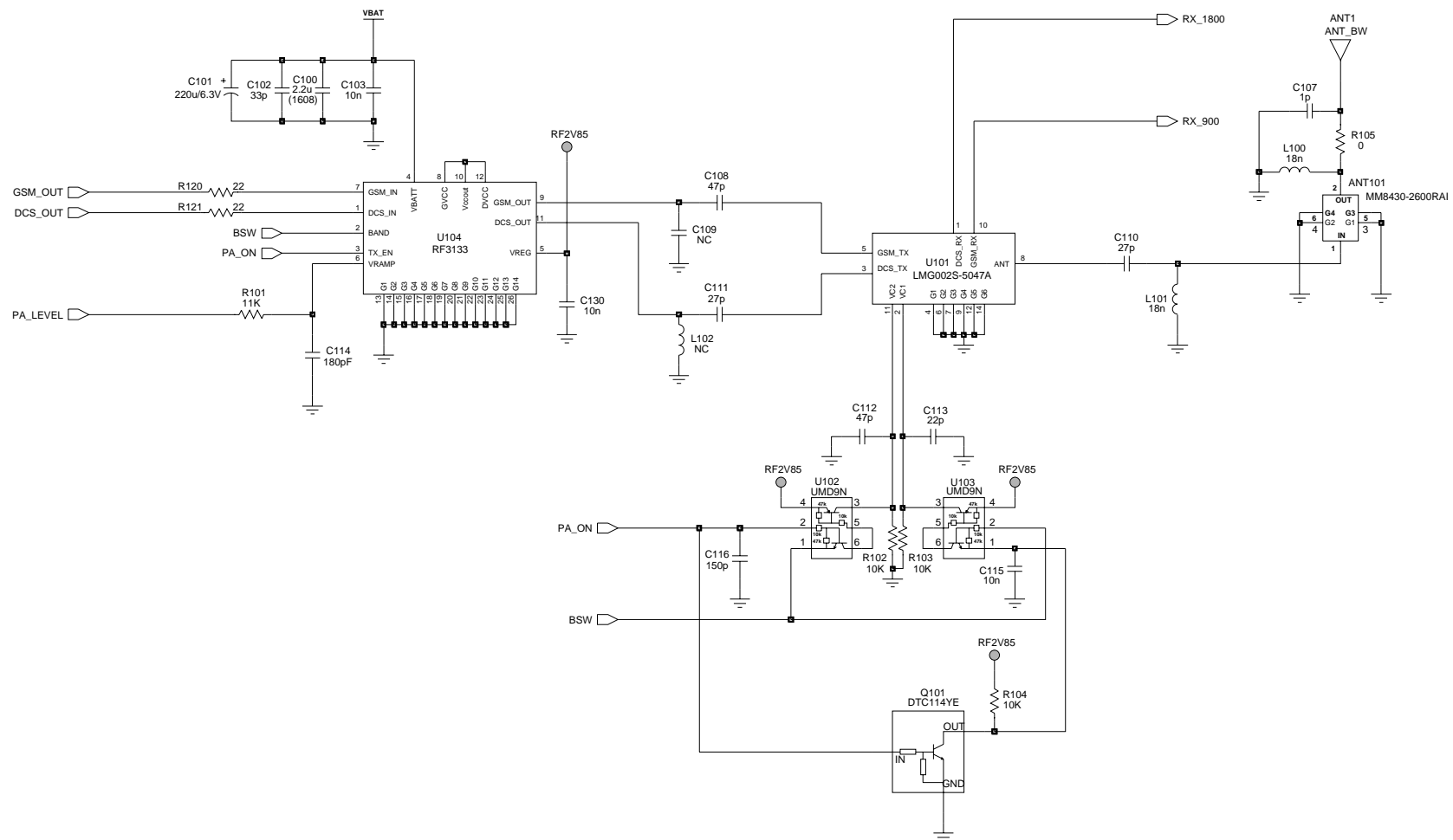
FT20 BB Block Diagram

BGP-FT20 BASEBAND BLOCK-DIAGRAM



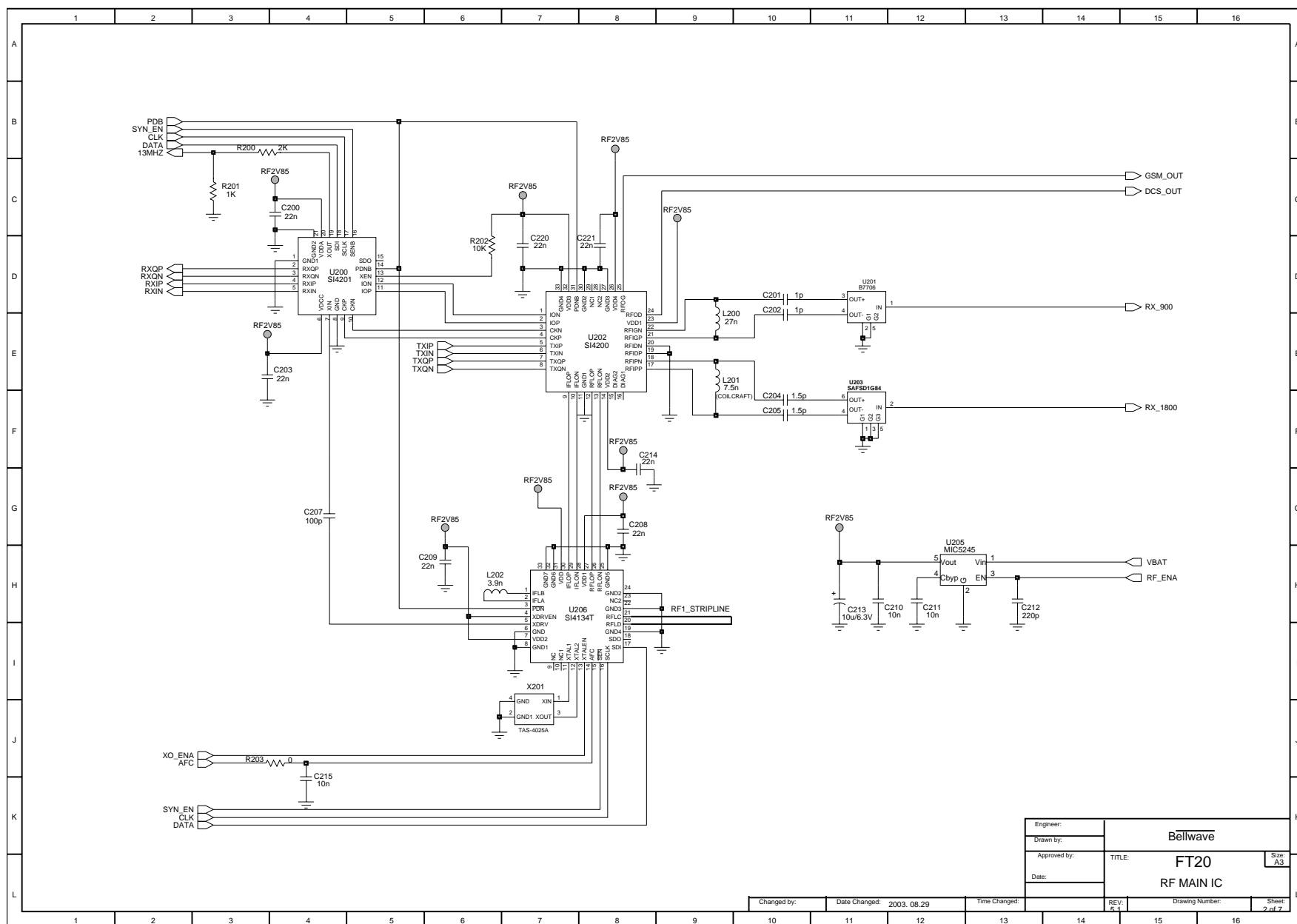
FT20 RF Block Diagram



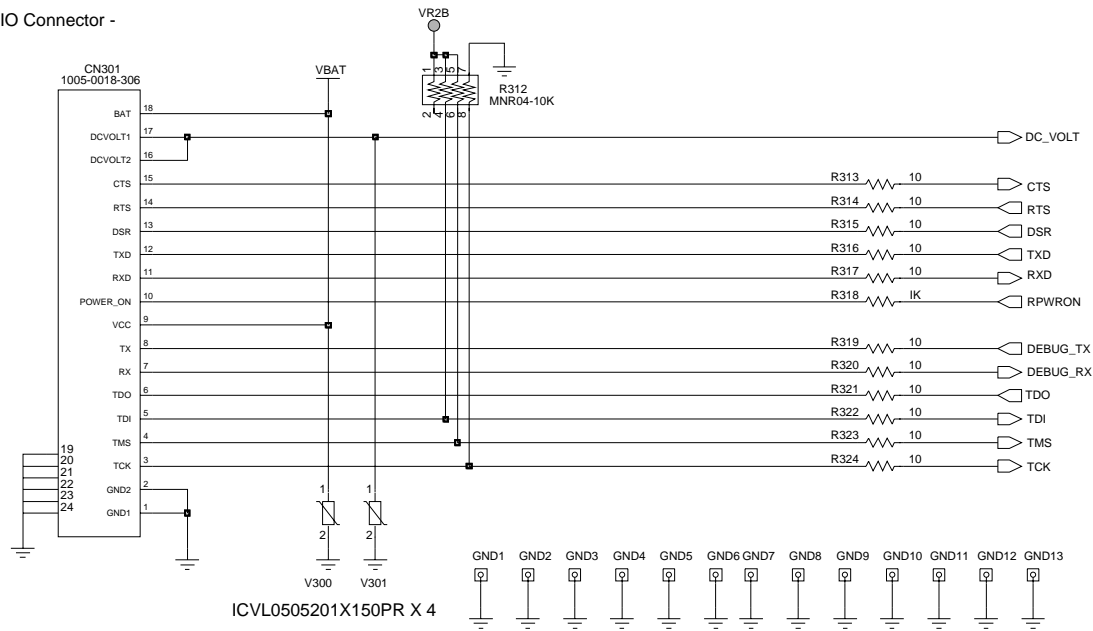


Engineer:	Bellwave		
Drawn by:			
R&D CHK:	TITLE: FT20		
DOC CTRL CHK:			
REV: 5.1	Drawing Number:	Page: 1 of 7	

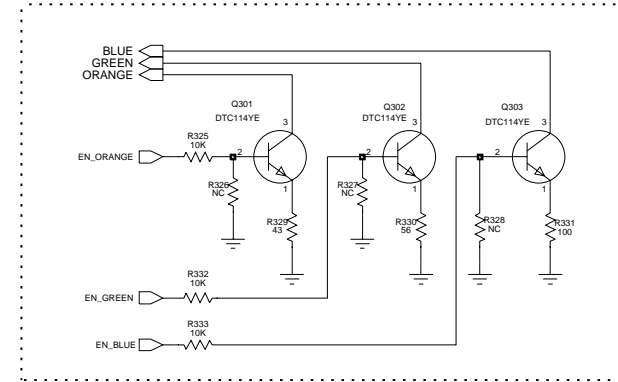
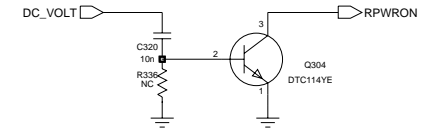
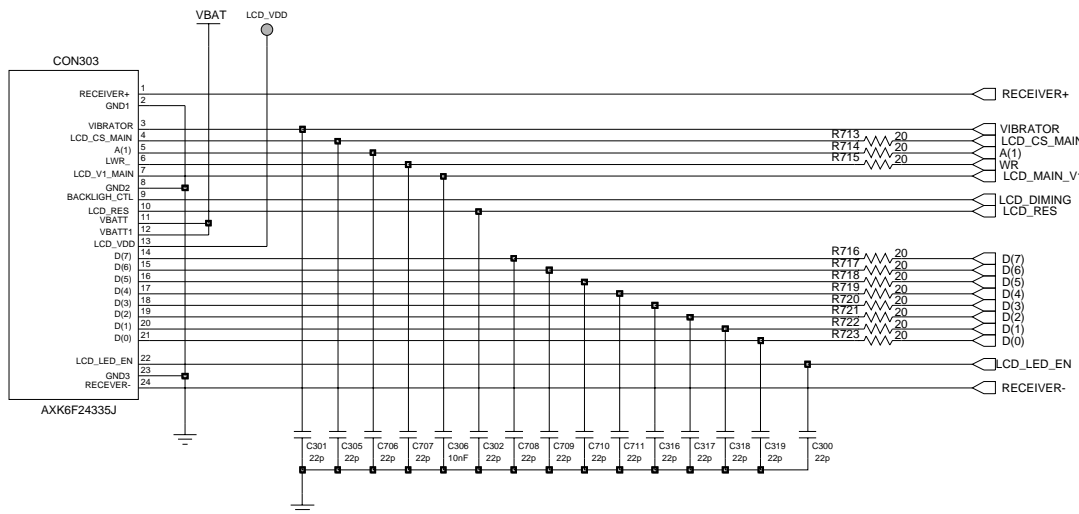
Changed by:	Date Changed: 2003. 08.29	Time Changed:	REV: 5.1	Drawing Number:	Page: 1 of 7
-------------	---------------------------	---------------	----------	-----------------	--------------



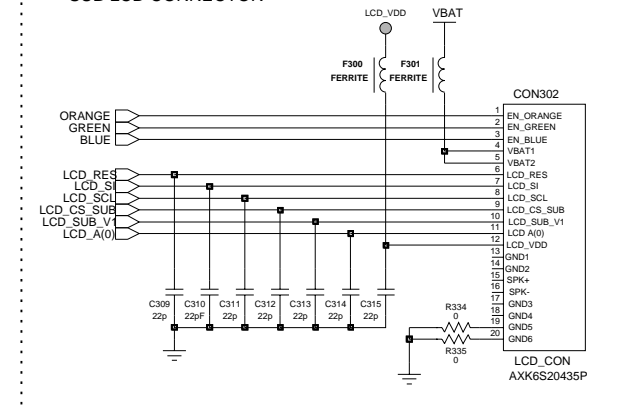
- IO Connector -



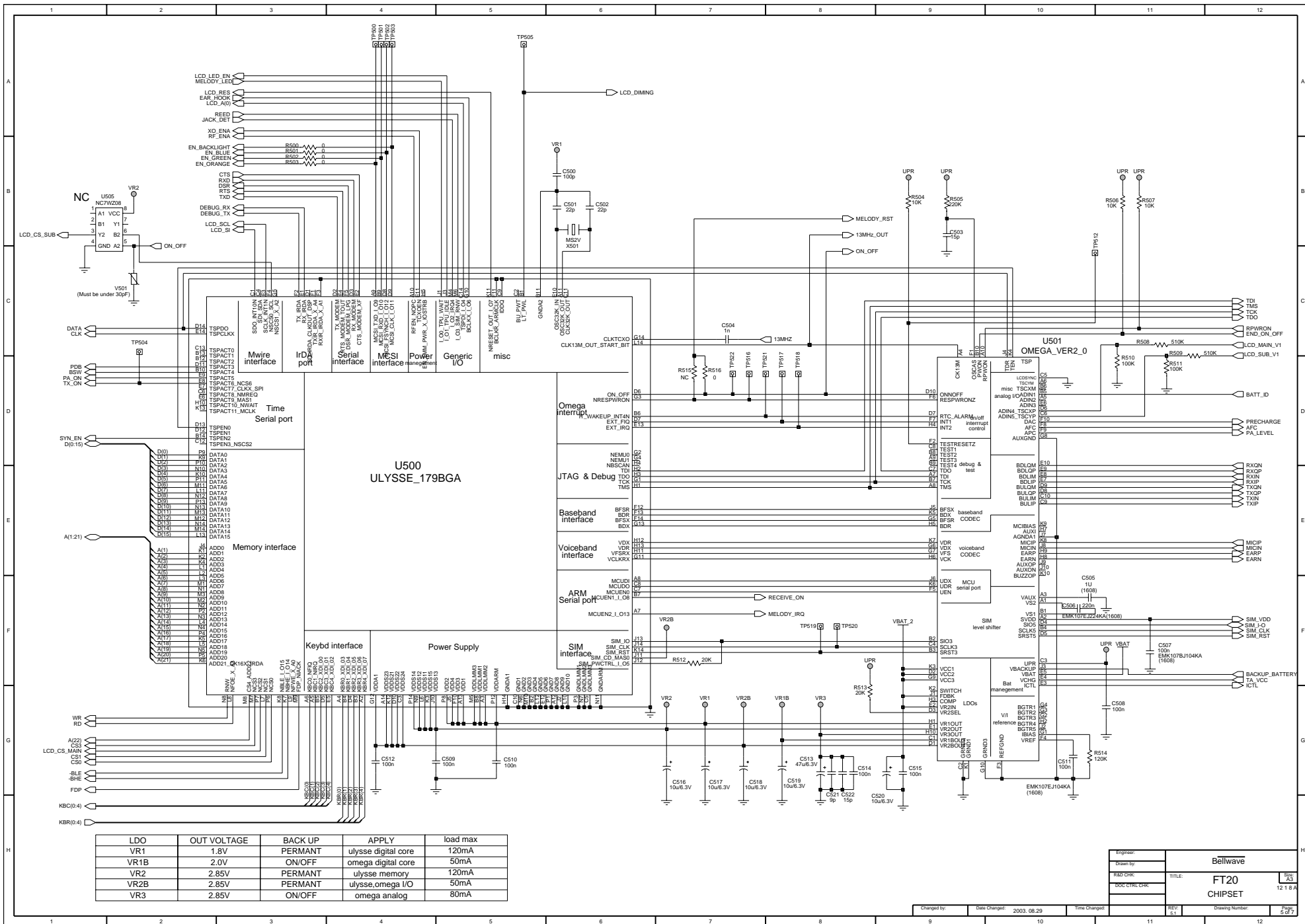
MAIN LCD CONNECTOR



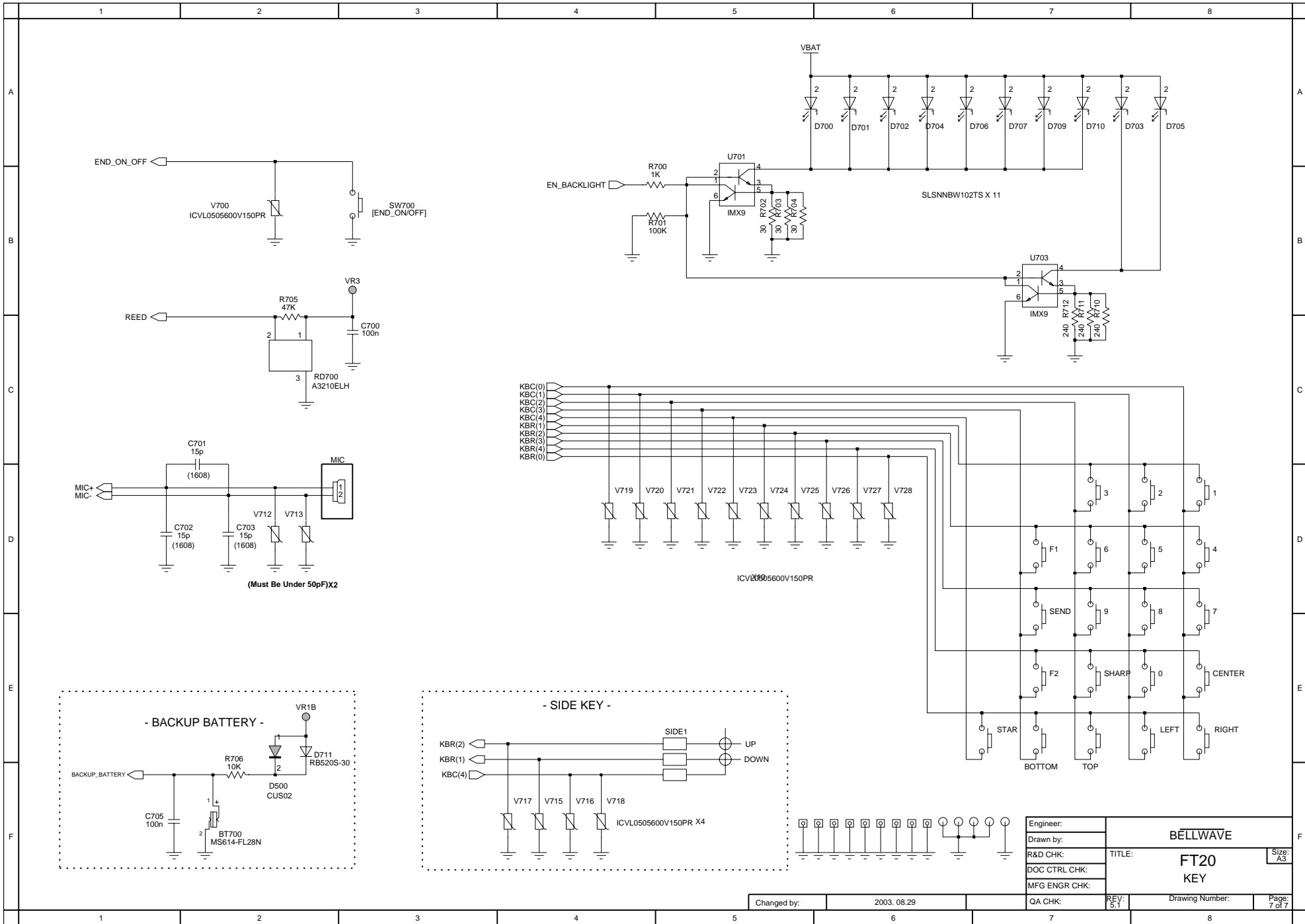
SUB LCD CONNECTOR



Engineer:	Bellwave		
Drawn by:			
Approved by:	TITLE: FT20 CONNECTOR		
Date:	REV: 5.1		
Changed by:	Date Changed: 2003. 08.29	Time Changed:	Drawing Number:
			Sheet: 3 of 7



Engineer:		Bellwave	
Drawn by:		TITLE: FT20	
Rev: 0.1		1218A	
VOC: CTR: CTR:		CHIPSET	



ASSY(COMP)

